

PIC16C62B Rev. A Silicon Errata Sheet

The PIC16C62B (Rev. A) parts you have received conform functionally to the Device Data Sheet (DS35008A), except for the anomalies described below.

All the problems listed here will be addressed in future revisions of the PIC16C62B silicon.

1. Module: RESET

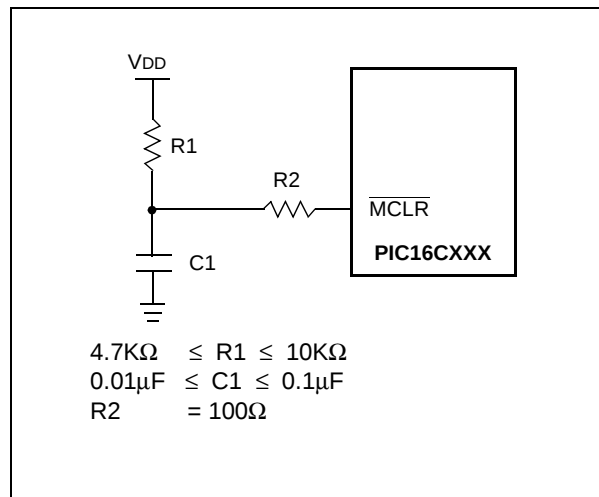
The minimum specification for the $\overline{\text{MCLR}}$ must be met in order to RESET the PIC16CXXX. If a $\overline{\text{MCLR}}$ pulse occurs that is less than the minimum specification (parameter #30), improper device operation can occur.

If the minimum specification cannot be met, then an external circuit must be used to insure that any pulse width less than the specification will be filtered before it reaches the $\overline{\text{MCLR}}$ pin.

Work Around

A possible circuit is shown in Figure 1. Proper design validation needs to be done to ensure desired operation over the applications operating conditions

FIGURE 1: $\overline{\text{MCLR}}$ EXTERNAL CIRCUIT



Note: As with any windowed EPROM device, please cover the window at all times, except when erasing.

3. Module: TMR1

When operating in external clock mode (TMR1CS is set), reading either of the timer 1 registers (TMR1H or TMR1L) may cause the timer not to increment as expected. This occurs for both synchronous and asynchronous inputs.

The scenarios which display this are:

- When a read operation of the TMR1H register occurs, the TMR1L register may not increment.
- When a read operation of the TMR1L register occurs, the TMR1H register may not increment. This improper operation is only an issue when the TMR1L register increments from FFh to 00h (FFh → 00H) during the read of the TMR1L register.

Work Around

Do not read either the TMR1H or the TMR1L registers when operating in external clock mode (TMR1CS is set). If the application needs to read the 16-bit counter, evaluate if this function can be moved to the TMR0 or one of the other timer resources on the device.

Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (DS35008A), the following clarifications and corrections should be noted.

none.

Note the following details of the code protection feature on PICmicro® MCUs.

- The PICmicro family meets the specifications contained in the Microchip Data Sheet.
- Microchip believes that its family of PICmicro microcontrollers is one of the most secure products of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the PICmicro microcontroller in a manner outside the operating specifications contained in the data sheet. The person doing so may be engaged in theft of intellectual property.
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- Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our product.

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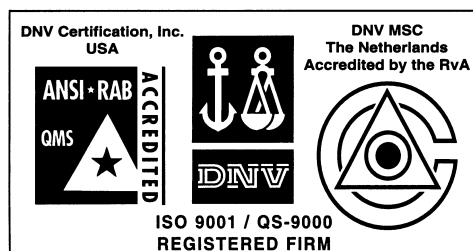
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