PC-TFT-20TD

Interface Board

Features

- Used for TFT-LCD display: 2.0" ANDpSi020TD-LED
- Both PAL + NTSC
- 1 x CVBS input signal (1.0Vp-p)
- OSD Menu
- IR remote control (optional).

The PC-TFT-20TD is designed to work with the ANDpSi020TD-LED color TFT display which is suitable for Mobile and small home use electronic TV (automobile TV / DVD display, handheld TV / portable DVD.)

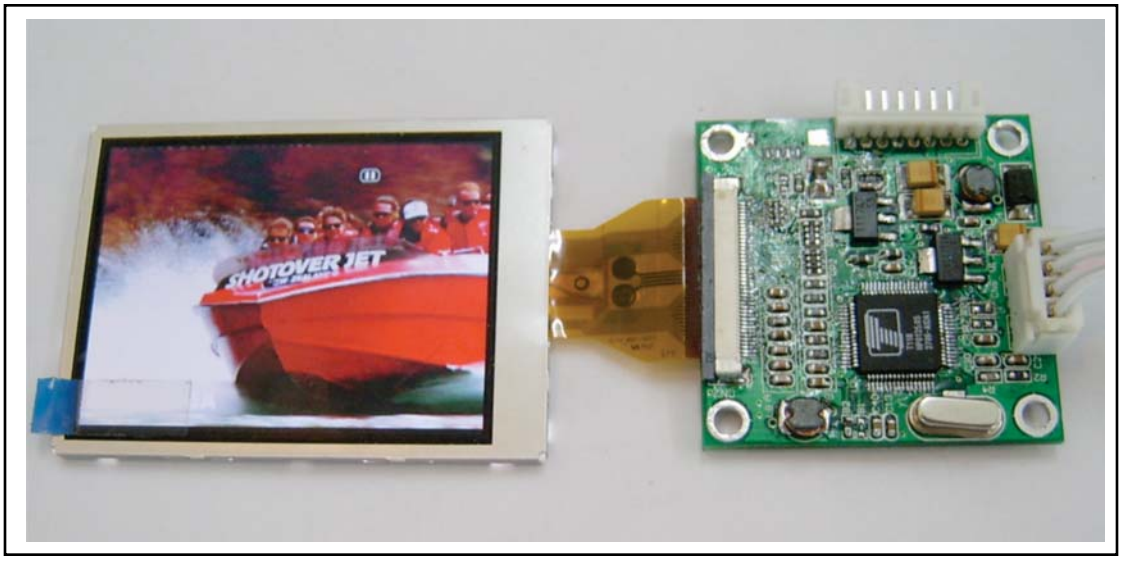
Environment:
Working Temp.: 0~60ºC
Humidity: 5~95% RH
Storage Temp: -25ºC ~ +70ºC
Humidity: 0~90% RH

OSD Function Description: Menu (PICTURE)

Item	Description	Value
Brightness	Brightness Value	0-100
Contrast	Contrast Value	0-100
Color	Color Value	0-100
Sharpness	Sharpness Value	0-100
Hue	HUE Value (only work on NTSC)	0-100

Keys function:

Button	Function
Y2	Power On/Off
MENU	OSD Menu On/Off
ENTER	When OSD on, Select the item
UP	When OSD on, Increase the item's value
DOWN	When OSD on, decrease the item's value

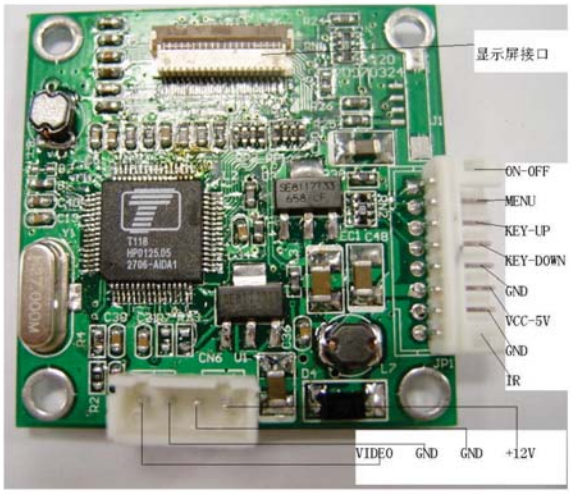


Product specifications contained herein may be changed without prior notice.
It is therefore advisable to contact Purdy Electronics before proceeding with the design of equipment incorporating this product.

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PANEL Connector

Pin No.	Symbol	I/O	Description
1	CP3	C	Capacitor for power setting
2	CP4	C	Capacitor for power setting
3	CP5	C	Capacitor for charge pump
4	CP6	C	Capacitor for charge pump
5	CP7	C	Capacitor for charge pump
6	CP8	C	Capacitor for charge pump
7	NC	–	No connection
8	PCDL	C	Capacitor for pre-charge data signal low
9	PCDH	C	Capacitor for pre-charge data signal high
10	VCOML	C	Capacitor for VCOM low
11	VCOMH	C	Capacitor for VCOM high
12	AGND	–	Analog ground
13	PVDD	C	Regulation capacitor for charge pump
14	AVDD	C	Regulation capacitor for analog voltage
15	CP1	C	Capacitor for charge pump
16	CP2	C	Capacitor for charge pump
17	PWM	O	Power transistor gate signal for the boost converter
18	FB	I	Main boost regulator feedback input
19	LED-	–	LED power: cathode
20	NC	–	No connector
21	NC	–	No connector
22	LED+	–	LED power: anode
23	GND	–	Ground
24	VCC	–	Power supply for digital circuit and charge pump circuit
25	VD	I	Vertical sync input. Negative polarity
26	HD	I	Horizontal sync input. Negative polarity
27	DCLK	I	Clock signal, latch data onto line latches at the rising edge
28	DIN0	I	Data input
29	DIN1	I	Data input
30	DIN2	I	Data input
31	DIN3	I	Data input
32	DIN4	I	Data input
33	DIN5	I	Data input
34	DIN6	I	Data input
35	DIN7	I	Data input
36	SDA	I/O	Serial interface data line
37	SCL	I	Serial interface clock line
38	SCEN	I	Serial interface chip enable line
39	NSTBY	I	Sleep mode setting pin
40	NRESET	I	System reset pin



Picture 2. 39Pin FPC connector, DEMO board front side

ID	Input/Output	Remarks
CN6	CVBS signal & Power Input	Suggest to use stabilized 12 V DC
JP1	OSD control connector	
CN1	TFT panel input connector	Molex 51374-4073

Electrical Parameters

Item	Value			Unit
	Min.	Typ.	Max.	
VCC	6	12	15	V
I Total	110	110	110	mA
I Display	60	60	60	mA