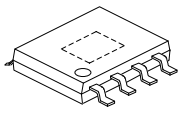
PWM CONTROL 3A STEP-DOWN CONVERTER



HSOP-8

■ DESCRIPTION

The UTC **P34563** consists of step-down switching regulator with PWM control.

The UTC **P34563** provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to the duty ratio linearly forms 0 up to 100%. With the addition of an internal P-channel Power MOS, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the HSOP-8 package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 40V.

■ FEATURES

* Input voltage: 8V~40V
* Oscillation frequency: 100K/350KHz
* Duty ratio: 0%~100% PWM control
* Adjustable version output voltage range from 3.3V to 38V
* Enable and auto restart function.
* Short Circuit Protect (SCP).
* Thermal Shutdown function / Internal OVP.
* Built-in internal SW P-channel MOS.

■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
P34563L-SH2-T	P34563G-SH2-T	HSOP-8	Tube
P34563L-SH2-R	P34563G-SH2-R	HSOP-8	Tape Reel

P34563L-SH2-T
(1)Packing Type
(2)Package Type
(3)Lead Free

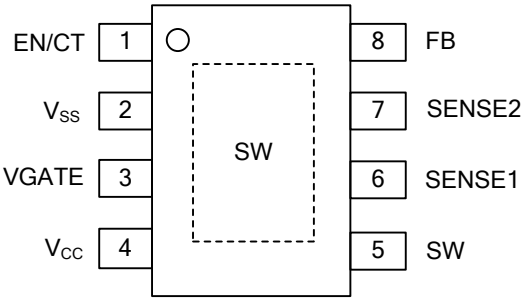
(1) T: Tube, R: Tape Reel
(2) SH2: HSOP-8
(3) L: Lead Free, G: Halogen Free

■ MARKING INFORMATION

PACKAGE	MARKING
HSOP-8	UTC □□□□ P34563□ □□ Date Code L: Lead Free G: Halogen Free Lot Code

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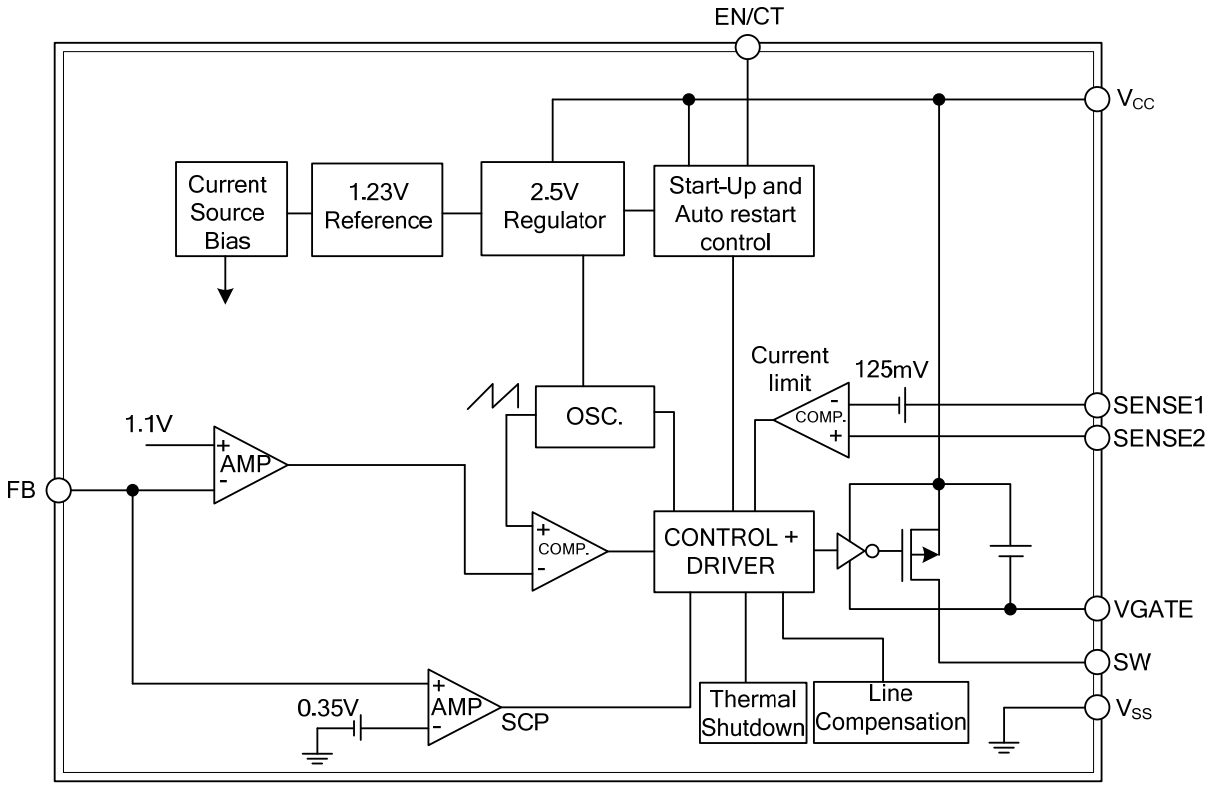
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	EN/CT	ON/OFF and auto restart control
2	V_{SS}	GND pin
3	VGATE	Driver gate clamping pin.
4	V_{CC}	Operating voltage input
5	SW	Switch pin.
6	SENSE1	Current sense input1
7	SENSE2	Current sense input2
8	FB	Feedback pin

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise specified.)

PARAMETER	SYMBOL	RATINGS	UNIT
VCC Pin Voltage	V_{CC}	V_{SS} -0.3~V_{SS}+45	V
Feedback Pin Voltage	V_{FB}	V_{SS}-0.3~38	V
EN/CT, OVP Pin Voltage		V_{SS}-0.3~6	V
SENSE1,SENSE2 Pin Voltage		V_{SS}-0.3~38	V
Switch Pin Voltage	V_{SW}	V_{SS}-0.3~V_{CC}+0.3	V
Power Dissipation	P_D	(T_J -T_A)/ θ_{JA}	W
Operating Supply Voltage	V_{OP}	+8~+40	V
Output Current	I_{OUT}	0~3	A
Operating Temperature	T_{OPR}	-20~+125	°C
Storage Temperature	T_{STG}	-40~+165	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS

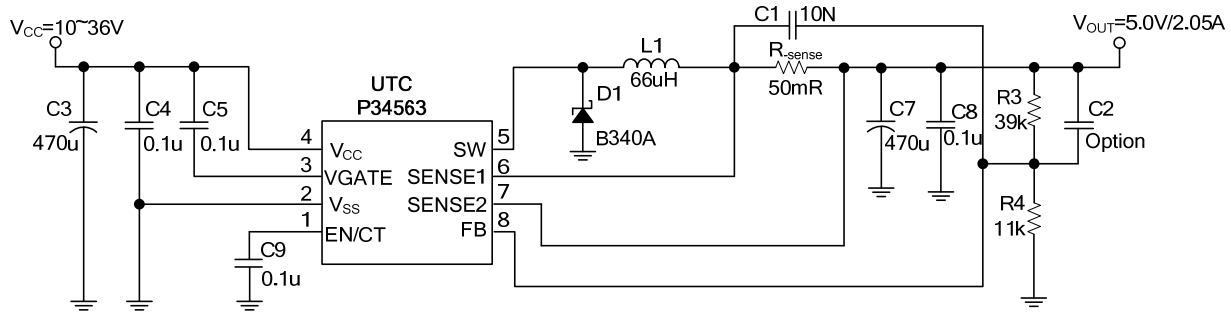
PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	143	°C/W
Junction to Case	θ_{JC}	15	°C/W

■ ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
FB	V_{FB}	I_{OUT}=10mA	1.08	1.10	1.12	V
Under Voltage Lockout	U_{VLO}	Falling		6		V
UVLO Hysteresis				0.8		V
OVP Detect Voltage	V_{OVP}	FB Floating		1.250		V
Line Regulation	ΔV_{OUT}	V_{CC}=10~40V		0.5	1	%
Load Regulation	ΔV_{OUT}	I_{OUT}=0~1A, R_{SENSE}=140mΩ		10		mV
Quiescent Current	I_{CCQ}	V_{FB}>1.2V		3	7	mA
Oscillator Frequency	F_{OSC}		80	100	120	KHz
			300	350	400	
Max. Duty Cycle (ON)	DC	Force Driver On V_{FB}=0.7V		100		%
Min. Duty Cycle (OFF)		Force Driver Off V_{FB}=1.2V		0		%
Internal MOSFET R_{DSON}	R_{DSON}	V_{CC}=12V, V_{FB} = 0.7V		110	170	mΩ
Sense Voltage	V_{SENSE}	V_{SENSE1}-V_{SENSE2}		150		mV
Sense Voltage Hysteresis	$V_{SENSE-h}$			45		mV
EN/CT Pin Logic Input Threshold Voltage	V_{EN}	Shutdown Mode			0.3	V
	V_{CT}	Auto Restart, V_{FB}<0.4V	0.5		1.5	V
EN/CT Pin Current	$I_{EN/CT-C}$	Charge Current		-32		μA
EN/CT Pin Current	$I_{EN/CT-D}$	Discharge Current		1.5		μA
Thermal Shutdown Temp	T_{SD}			160		°C
Thermal Shutdown Hysteresis	T_{SH}			40		°C

■ TYPICAL APPLICATION CIRCUIT

AL CAPACITOR



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