

PART NUMBERING GUIDE

Environmental/Mechanical Specifications on page F5

Package		OC-3.3- 100 48 A T - 30.000MHz	Pin One Connection
OC-3.3 = 5X7X1.6mm / 3.3Vdc / HCMOS-TTL			T = Tri State Enable High
OC-2.5 = 5X7X1.6mm / 2.5Vdc / HCMOS-TTL			
Inclusive Stability			Output Symmetry
100= +/-100ppm, 50= +/-50ppm, 30= +/-30ppm, 25= +/-25ppm, 20= +/-20ppm, 15= +/-15ppm, 10= +/-10ppm (25,20,15,10= 0°C-70°C Only)			Blank = 40/60%, A = 45/55%
			Operating Temperature Range
			Blank = -10°C to 70°C, 27 = -20°C to 70°C, 48 = -40°C to 85°C

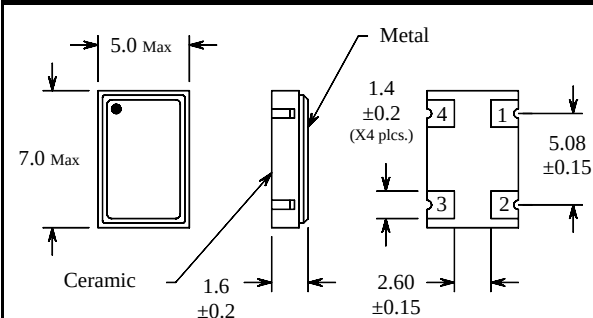
ELECTRICAL SPECIFICATIONS

Revision: 1996-G

Frequency Range		1.544MHz to 185.000MHz
Operating Temperature Range		-10°C to 70°C / -20°C to 70°C / -40°C to 85°C
Storage Temperature Range		-55°C to 125°C
Supply Voltage		2.5Vdc ±10% or 3.3Vdc ±10%
Input Current		1.544MHz to 36.000MHz 36,001MHz to 70.000MHz 70.001MHz to 185.000MHz
Frequency Tolerance / Stability		Inclusive of Operating Temperature Range, Supply Voltage and Load ±100ppm, ±50ppm, ±30ppm, ±25ppm, ±20ppm, ±15ppm or ±10ppm (25, 20, 15, 10 = 0°C to 70°C)
Output Voltage Logic High (Voh)		w/HCMOS or TTL Load 90% of Vdd Min. / Ioh=-8mA
Output Voltage Logic Low (Vol)		w/HCMOS or TTL Load 10% of Vdd Max. / Iol=8mA
Rise / Fall Time		10% to 90% of Waveform w/HCMOS Load; 0.4Vdc to 2.4V w/TTL Load / 5nSec Max.
Duty Cycle		@1.4Vdc w/TTL Load; @50% w/HCMOS Load @1.4Vdc w/TTL Load or w/HCMOS Load
Load Drive Capability		1.544MHz - 36.000MHz 36.0001MHz - 70.000MHz 70.0001MHz - 185.000MHz
Pin 1 Tristate Input Voltage		No Connection VIH VIL
Aging (@ 25°C)		±5ppm / year Maximum
Start Up Time		10mSeconds Maximum
Stand By Current		10uA Maximum (Disabled Output, High Imp)
RMS Phase Jitter		1pSec Maximum / 12kHz to 20.000MHz offset

MECHANICAL DIMENSIONS

Marking Guide



All Dimensions in mm.

Line 1: 3T- Frequency
Line 2: CEI YM

3 = 3.3V designator
T = Tristate
CEI = Caliber Electronics Inc.
YM = Date Code (Year / Month)

Pin 1: Tri-State
Pin 2: Case Ground

Pin 3: Output
Pin 4: Supply Voltage