

Thyristor-Thyristor Module, 106 Amps

Features

- Improved glass passivation for high reliability
- Exceptional stability at high temperatures
- High di/dt and dv/dt capabilities
- Low thermal resistance



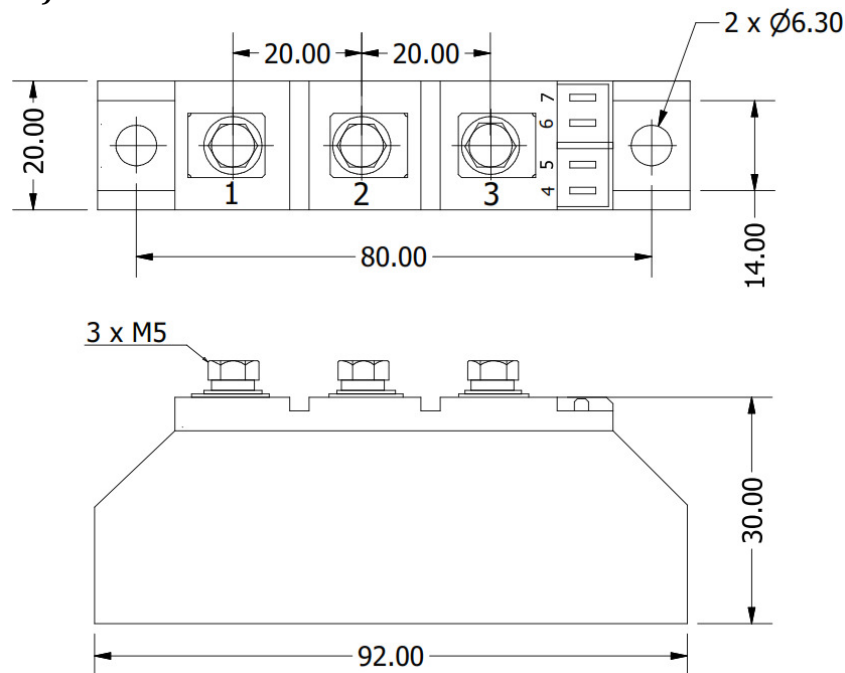
Voltage Ratings ($T_A = 25^\circ\text{C}$, unless otherwise noted)					
Type number	Voltage Code	V_{RRM} , Maximum repetitive peak reverse voltage (V)	V_{RSM} , Maximum non-repetitive peak reverse voltage (V)	V_{DRM} , Maximum repetitive peak off-state voltage (V)	I_{RRM} , Maximum reverse leakage current @ T_{JMAX} (mA)
NTT106	60	600	700	600	max. 15
	80	800	900	800	
	100	1000	1100	1000	
	120	1200	1300	1200	
	140	1400	1500	1400	
	160	1600	1700	1600	
	180	1800	1900	1800	

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)			
Parameter	Symbol	Values	Units
Maximum average forward current @ $T_J = 85^\circ\text{C}$	$I_{T(AV)}$	106	A
Maximum average RMS forward current	$I_{T(RMS)}$	166	A
Maximum non-repetitive surge current	I_{TSM}	2250	A
Maximum I^2t for fusing	I^2t	25300	A^2s
Forward voltage drop	V_{TM}	max. 1.6	V
Critical rate of rise of on-state current	di/dt	max. 150	$\text{A}/\mu\text{s}$
Critical rate of rise of off-state voltage	dv/dt	max. 1000	$\text{V}/\mu\text{s}$
Gate current required to trigger	I_{GT}	min. 150	mA
Gate voltage required to trigger	V_{GT}	min. 3	V
Maximum holding current	I_H	150	mA
Maximum latching current	I_L	300	mA
Isolation voltage	V_{ISO}	3000	V

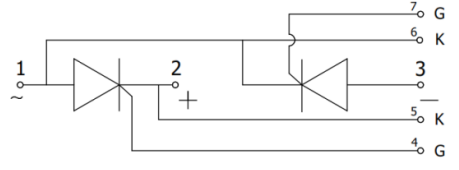
Thermal & Mechanical Specifications ($T_A = 25^\circ\text{C}$ unless otherwise noted)			
Parameter	Symbol	Values	Units
Operating junction temperature range	T_J	-40 to +130	$^\circ\text{C}$
Storage temperature	T_{stg}	-40 to +125	$^\circ\text{C}$
Thermal resistance, junction to case	$R_{th(JC)}$	0.28	$^\circ\text{C}/\text{W}$
Mounting torque	to heatsink	F	Nm
	to terminals		
Weight	W	100	g

Package Outline

(All dimensions in mm)



Circuit Configuration

Circuit Description	Configuration Code	Circuit Drawing
Series Connection (doubler circuit)	N	 The circuit drawing shows a series connection of two thyristors. The first thyristor has its anode connected to terminal 1 and its cathode to terminal 2. The second thyristor has its anode connected to terminal 2 and its cathode to terminal 3. The gate of the first thyristor is connected to terminal 4 (G) and the gate of the second thyristor is connected to terminal 5 (K). The terminals are labeled 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100.

Ordering Table

NTT	106	N	160
1	2	3	4

1 – Power Module

- > DD = Diode-Diode
- > TD = Thyristor-Diode
- > TT = Thyristor-Thyristor

2 – Current Rating = $I_T(AV)$

3 – Circuit Configuration (see Table)

4 – Voltage Code (see Voltage Ratings table)