NST857BF3T5G

PNP General Purpose Transistor

The NST857BF3T5G device is a spin−off of our popular SOT−23/SOT−323/SOT−563/SOT−963 three−leaded device. It is designed for general purpose amplifier applications and is housed in the SOT−1123 surface mount package. This device is ideal for low−power surface mount applications where board space is at a premium.

Features

- h_{FE}, 220−475
- Low $V_{CE(sat)}$, ≤ −0.3 V
- Reduces Board Space
- This is a Pb−Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector − Emitter Voltage	V_{CEO}	−45	Vdc
Collector − Base Voltage	V_{CBO}	−50	Vdc
Emitter − Base Voltage	V_{EBO}	−5.0	Vdc
Collector Current − Continuous	I_C	−100	mAdc

THERMAL CHARACTERISTICS

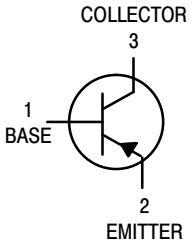
Characteristic	Symbol	Max	Unit
Total Device Dissipation, T_A = 25°C Derate above 25°C	P_D (Note 1)	290 2.3	mW mW/°C
Thermal Resistance, Junction−to−Ambient	$R_{\theta JA}$ (Note 1)	432	°C/W
Total Device Dissipation, T_A = 25°C Derate above 25°C	P_D (Note 2)	347 2.8	mW mW/°C
Thermal Resistance, Junction−to−Ambient	$R_{\theta JA}$ (Note 2)	360	°C/W
Thermal Resistance, Junction−to−Lead 3	$R_{\Psi JL}$ (Note 2)	143	°C/W
Junction and Storage Temperature Range	T_J, T_{stg}	−55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

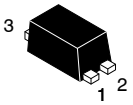
1. 100 mm^2 1 oz, copper traces.
2. 500 mm^2 1 oz, copper traces.

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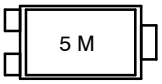


NST857BF3T5G



SOT−1123
CASE 524AA
STYLE 1

MARKING DIAGRAM



5 = Device Code
M = Date Code

ORDERING INFORMATION

Device	Package	Shipping†
NST857BF3T5G	SOT−1123 (Pb−Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I_C = −10 mA)	$V_{(BR)CEO}$	−45	–	–	V
Collector – Emitter Breakdown Voltage (I_C = −10 μA, V_{EB} = 0)	$V_{(BR)CES}$	−50	–	–	V
Collector – Base Breakdown Voltage (I_C = −10 μA)	$V_{(BR)CBO}$	−50	–	–	V
Emitter – Base Breakdown Voltage (I_E = −1.0 μA)	$V_{(BR)EBO}$	−5.0	–	–	V
Collector Cutoff Current (V_{CB} = −30 V) (V_{CB} = −30 V, T_A = 150°C)	I_{CBO}	– –	– –	−15 −4.0	nA μA
ON CHARACTERISTICS					
DC Current Gain (I_C = −10 μA, V_{CE} = −5.0 V) (I_C = −2.0 mA, V_{CE} = −5.0 V)	h_{FE}	 – 220	 150 290	 – 475	–
Collector – Emitter Saturation Voltage (I_C = −10 mA, I_B = −0.5 mA) (I_C = −100 mA, I_B = −5.0 mA)	$V_{CE(sat)}$	 – –	 – –	 −0.3 −0.7	V
Base – Emitter Saturation Voltage (I_C = −10 mA, I_B = −0.5 mA) (I_C = −100 mA, I_B = −5.0 mA)	$V_{BE(sat)}$	 – –	 −0.7 −0.9	 – –	V
Base – Emitter On Voltage (I_C = −2.0 mA, V_{CE} = −5.0 V) (I_C = −10 mA, V_{CE} = −5.0 V)	$V_{BE(on)}$	 −0.6 –	 – –	 −0.75 −0.82	V
SMALL−SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product (I_C = −10 mA, V_{CE} = −5.0 Vdc, f = 100 MHz)	f_T	100	–	–	MHz
Output Capacitance (V_{CB} = −10 V, f = 1.0 MHz)	C_{obo}	–	–	4.5	pF
Input Capacitance (V_{EB} = −0.5 V, I_C = 0 mA, f = 1.0 MHz)	C_{ibo}	–	–	10	pF
Noise Figure (I_C = −0.2 mA, V_{CE} = −5.0 Vdc, R_S = 2.0 kΩ, f = 1.0 kHz, BW = 200 Hz)	NF	–	–	10	dB

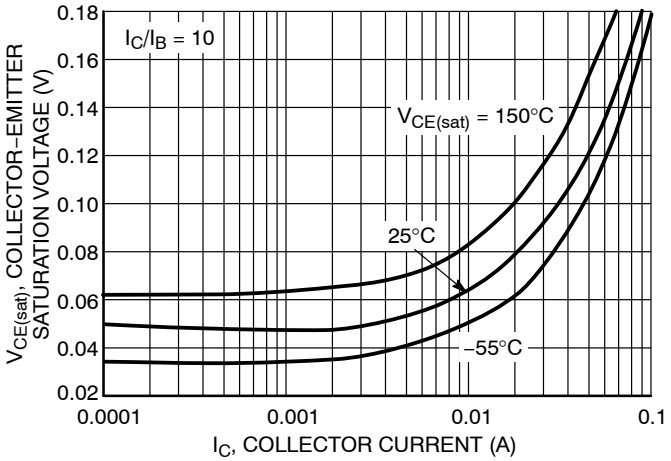


Figure 1. Collector Emitter Saturation Voltage vs. Collector Current

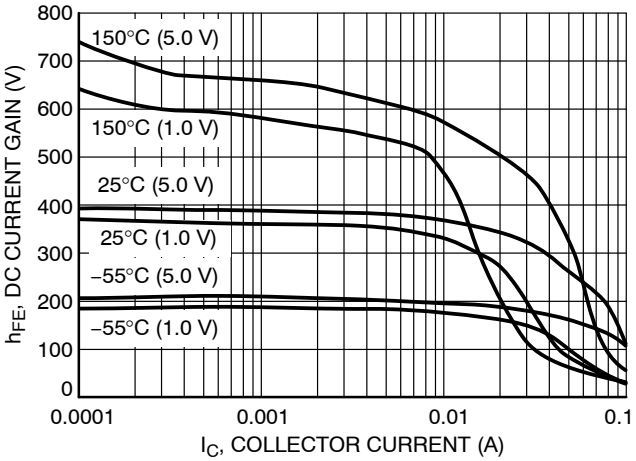


Figure 2. DC Current Gain vs. Collector Current

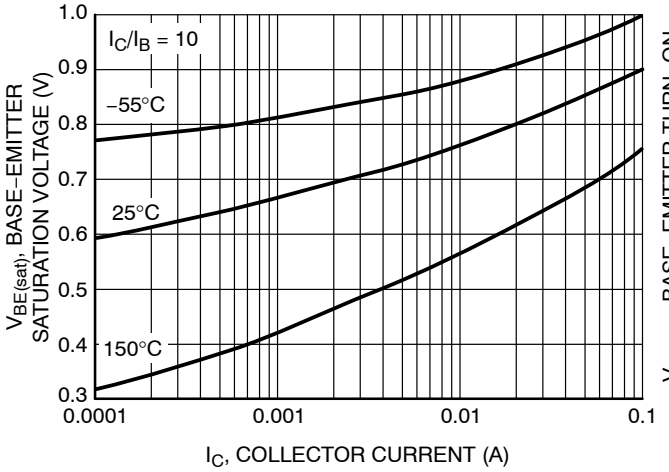


Figure 3. Base Emitter Saturation Voltage vs. Collector Current

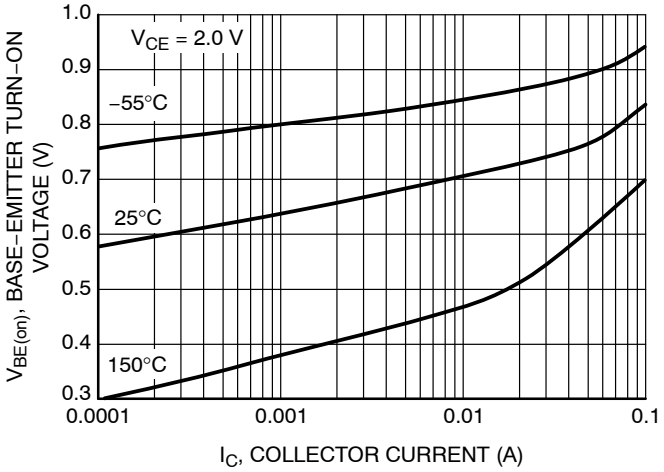


Figure 4. Base Emitter Turn−On Voltage vs. Collector Current

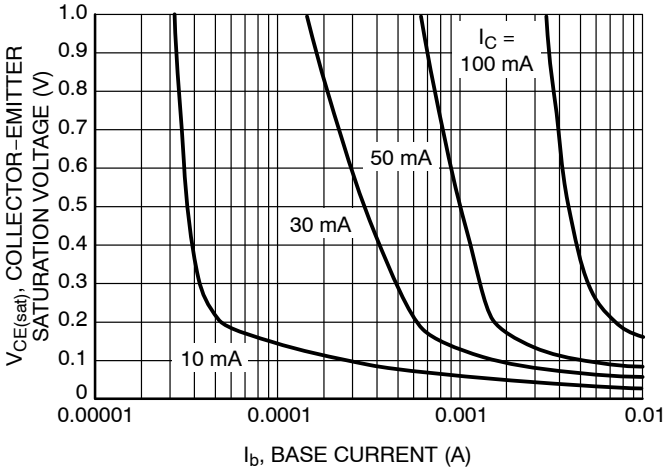


Figure 5. Saturation Region

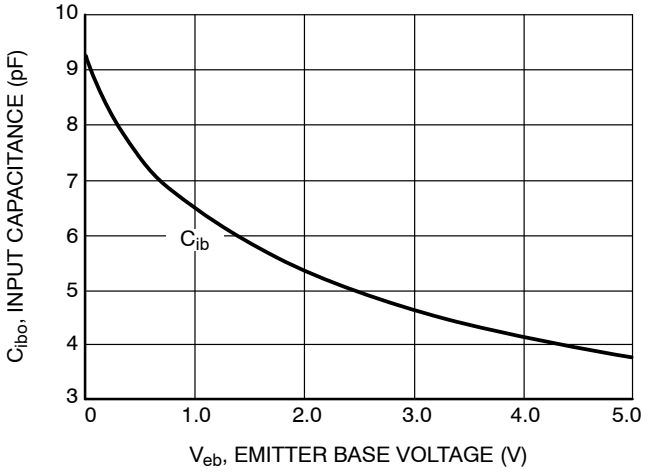


Figure 6. Input Capacitance

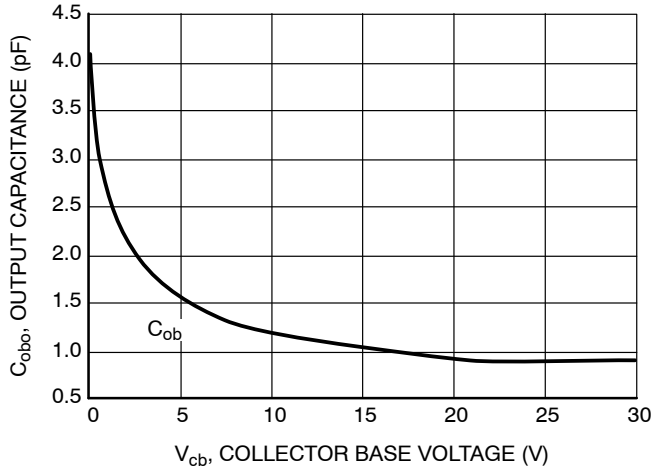
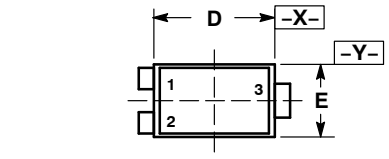


Figure 7. Output Capacitance

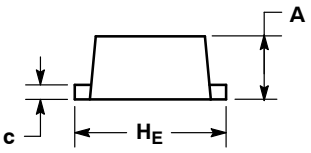
NST857BF3T5G

PACKAGE DIMENSIONS

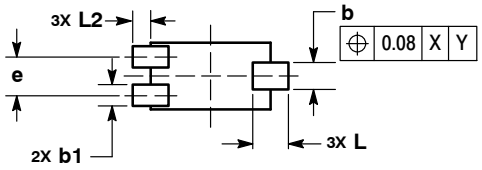
SOT−1123
CASE 524AA
ISSUE C



TOP VIEW



SIDE VIEW



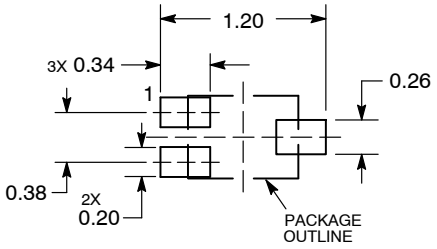
BOTTOM VIEW

NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS	
DIM	MIN	MAX
A	0.34	0.40
b	0.15	0.28
b1	0.10	0.20
c	0.07	0.17
D	0.75	0.85
E	0.55	0.65
e	0.35	0.40
H_E	0.95	1.05
L	0.185 REF	
L2	0.05	0.15

STYLE 1:
PIN 1. BASE
2. EMITTER
3. COLLECTOR

SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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