NELL SEMICONDUCTOR

NKF200-60(A)

FAST RECOVERY DIODE MODULES TYPE

Features

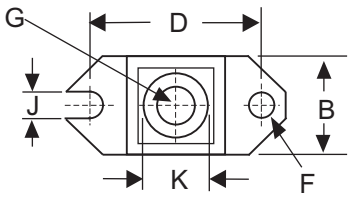
- High Surge Capability
- Dual Diode Consturction
- Low Leakage Current
- Low Forward Voltage
- Types Up to 600V V_{RRM}

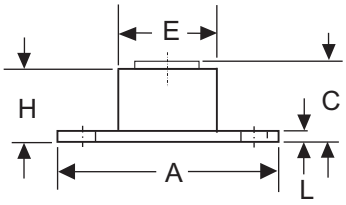


Baseplate
Common Cathode
Without Suffix " A "
for common Cathodes
to baseplate

Baseplate
A = Common Anode
With Suffix " A "
for common Anodes
to baseplate

Package : NKF





Maximum Ratings

Operating Temperature : -55 °C to +150 °C
Storage Temperature : -55 °C to +150 °C

Part Number	Maximum Recurrent Peak Reverse Voltage	Maximum RMS Voltage	Maximum DC Blocking Voltage
NKF200 - 60(A)	600 V	420 V	600 V

Ordering code

NK	F		200 –	60	(A)
(1)	(2)	(3)	(4)	(5)	(6)

(1) Module Type
(2) for Fast Recovery diode
(3) D - For dual terminals
Blank - single terminal
(4) Maximum average forward current , A
(5) Voltage code (code x 10 = V_{RRM})
(6) Blank - For common cathodes to base plate
A - For common anodes to base plate

Electrical Characteristics

Average Forward Current (per leg)	$I_{F(AV)}$	200 A	Tc=100 °C
Peak Forward Surge Current (per leg)	I_{FSM}	1200 A	8.3ms , half sine
Maximum Instantaneous Forward Voltage (per leg)	V_F	1.50 V	IFM = 200A Tj = 25 °C
Maximum DC Reverse Current at Rated DC Blocking Voltage (per leg)	I_R	10 uA 1000 uA	Tj = 25 °C Tj = 125 °C
Maximum (per leg) Reverse Recovery Time	trr	110 ns	IF = 0.5A , IR = 1.0A IRR = 0.25A
Maximum Thermal Resistance Junction to Case (per leg)	Rthjc	0.27 °C/W	
Maximum Terminal Torque	T	57 Kg-cm	
Maximum Mounting Base Torque	T	45 Kg-cm	

* Pulse Test : Pulse Width 300 usec , Duty Cycle 2 %

DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
A	1.515	1.560	38.48	39.62	
B	.725	.775	18.42	19.69	
C	.595	.625	15.11	15.88	
D	.1.182	1.192	30.02	30.28	
E	.745	.755	18.92	19.18	
F	.152	.160	3.86	4.061	⌀
G	1/4	- 20	UNC	- 2B	
H	.540	.580	13.72	14.73	
J	.156	.160	3.96	4.06	
K	.495	.505	12.57	12.83	⌀
L	.120	.130	3.05	3.30	

Figure 1
Typical Forward Characteristics

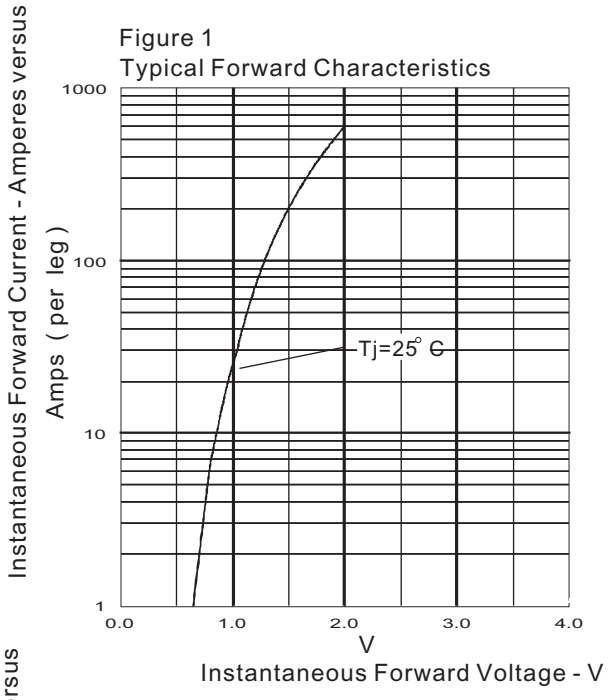


Figure 2
Forward Derating Curve

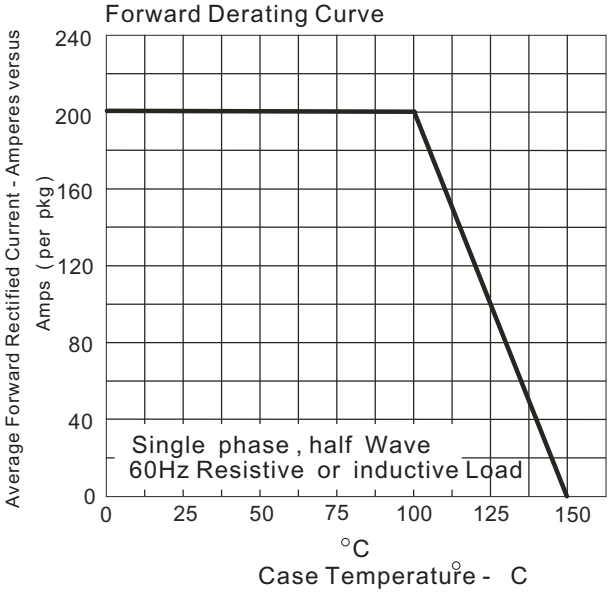


Figure 3
Peak Forward Surge Current

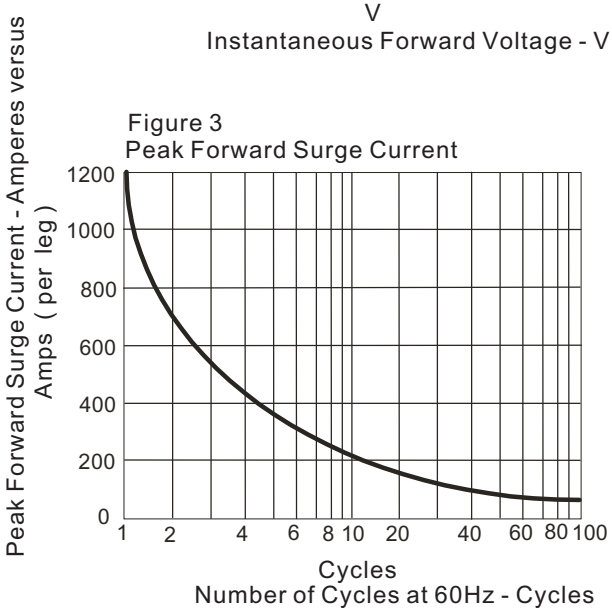


Figure 4
Typical Reverse Characteristics

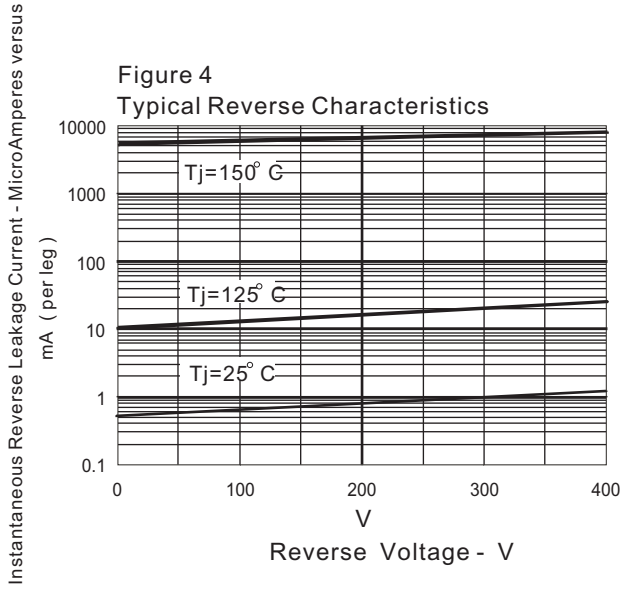


Figure 5 GR1 Test Circuit

