

High-Speed, Rail-to-Rail I/O, Single CMOS Operational Amplifier

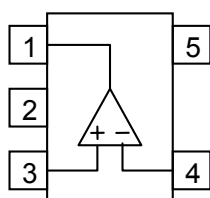
■ FEATURES

- Operating Voltage: 2.7V to 5.5V
- High speed SR=9V/ μ s. typ.(at V_{DD} = 5V)
GBW=5MHz typ.
- Rail-to-Rail Input/Output
- Low input bias current I_B =1pA typ.
- Unity gain stable
- Enhanced RF noise Immunity
- Package outline SOT-23-5, SC88A
- CMOS process
- Wide temperature range T_a =-40°C to 125°C

■ APPLICATIONS

- Current sensors / Sensor amplifiers
- Photodiode amplifiers
- ADC front ends
- ASIC input or output amplifiers
- Battery-powered instruments

■ PIN CONFIGURATION



SOT-23-5, SC88A
(Top View)

PIN FUNCTION

- 1: OUTPUT
- 2: V_{SS}
- 3: +INPUT
- 4: -INPUT
- 5: V_{DD}

■ GENERAL DESCRIPTION

The NJU7046 is a single rail to rail input/output operational amplifier featuring high speed, low input bias current and wide temperature range.

High-speed characteristics of slew rate(SR=9V/us) and bandwidth (GBW=5MHz), low input bias current (I_B =1pA typ.) and wide temperature range(T_a =-40°C to 125°C) makes NJU7046 especially suitable for sensors and filters application for industrial, buffers of input and output for ADC, DAC, ASIC and other wide output swing devices.

In addition to the NJU7046F3 is available in the ultra-small SC88A package. This package saves space on print circuit boards and enables the design of Battery-powered instruments.

■ PACKAGE OUTLINE



NJU7046F
(SOT-23-5)



NJU7046F3
(SC88A)

■ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless otherwise noted.)

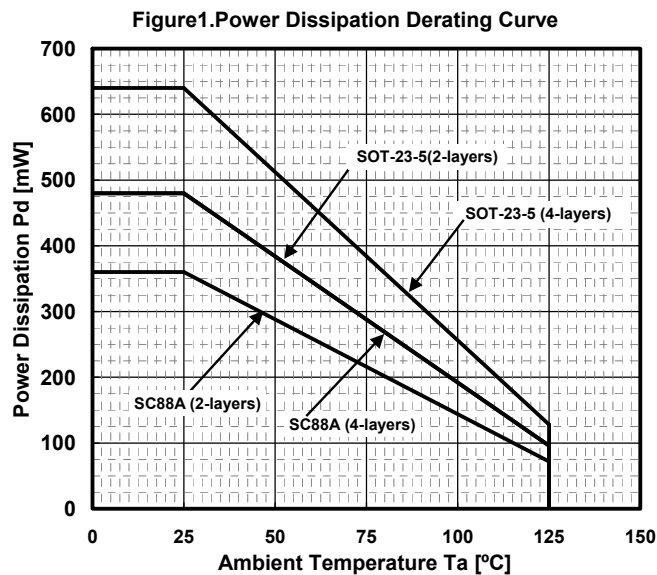
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{DD}	+7	V
Input Common Mode Voltage	V_{ICM}	$V_{SS}-0.3$ to $V_{DD}+0.3$	V
Differential Input Voltage	V_{ID}	± 7 (Note1)	V
Power Dissipation	P_D	[SC88A] 360 (Note2), 480 (Note3)	mW
		[SOT-23-5] 480 (Note2), 640 (Note3)	
Operating Temperature Range	T_{opr}	-40 to +125	°C
Storage Temperature Range	T_{stg}	-55 to +150	°C

(Note1) For supply voltage less than +7V, the absolute maximum rating is equal to the supply voltage.

(Note2) EIA/JEDEC STANDARD Test board (76.2 x 114.3 x 1.6mm, 2layers, FR-4) mounting.

(Note3) EIA/JEDEC STANDARD Test board (76.2 x 114.3 x 1.6mm, 4layers, FR-4) mounting.

(Note4) Do not exceed "Power dissipation: P_D " in which power dissipation in IC is shown by the absolute maximum rating.
See Figure "Power Dissipation Curve" when ambient temperature is over 25°C.



■RECOMMENDED OPERATING CONDITION (Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V_{DD}	2.7 to 5.5	V

■ELECTRICAL CHARACTERISTICS

DC CHARACTER ($V_{DD}=5V$, $V_{SS}=0V$, $T_a=25^{\circ}C$, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current	I_{CC}	No Signal	-	1.4	2.2	mA
Input Offset Voltage	V_{IO}	$V_{ICM}=5V$	-	0.9	5	mV
		$V_{ICM}=2.5V$	-	0.9	5	mV
		$V_{ICM}=0V$	-	0.9	5	mV
Input Offset Voltage Drift	$\Delta V_{IO}/\Delta T$		-	2	-	$\mu V/^{\circ}C$
Input Bias Current	I_B		-	1	-	pA
Input Offset Current	I_{IO}		-	1	-	pA
Open Loop Gain	A_V	$V_{out}=1.5V$ to $3.5V$, $R_L=10k\Omega$ to $2.5V$	90	110	-	dB
Common Mode Rejection Ratio	CMR	$V_{ICM}=0V$ to $5V$	60	80	-	dB
Supply Voltage Rejection Ratio	SVR	$V_{DD}=2.7V$ to $5.5V$, $V_{ICM}=0V$	65	90	-	dB
Maximum Output Voltage	V_{OH}	$R_L=10k\Omega$ to $2.5V$	4.95	4.99	-	V
		$R_L=600\Omega$ to $2.5V$	4.88	4.93	-	V
	V_{OL}	$R_L=10k\Omega$ to $2.5V$	-	0.01	0.05	V
		$R_L=600\Omega$ to $2.5V$	-	0.07	0.12	V
Common Mode Input Voltage Range	V_{ICM}	CMR $\geq$ 60dB	0	-	5	V

AC CHARACTER ($V_{DD}=5V$, $V_{SS}=0V$, $V_{ICM}=2.5V$, $T_a=25^{\circ}C$, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gain Bandwidth Product	GBW	$G_V=40dB$, $R_F=100k\Omega$, $R_L=10k\Omega$, $C_L=20pF$	-	5	-	MHz
Phase Margin	ϕ_M	$G_V=40dB$, $R_F=100k\Omega$, $R_L=10k\Omega$, $C_L=20pF$	-	70	-	deg
Gain Margin	G_M	$G_V=40dB$, $R_F=100k\Omega$, $R_L=10k\Omega$, $C_L=20pF$	-	16	-	dB
Equivalent Input Noise Voltage	V_{NI}	$f=1kHz$	-	20	-	$nV/\sqrt{Hz}$
Slew Rate	SR	$G_V=0dB$, $R_L=10k\Omega$, $C_L=20pF$, $V_{IN}=2V_{PP}$	5	9	-	$V/\mu s$
Total Harmonic Distortion	THD	$G_V=0dB$, $R_L=10k\Omega$, $f=1kHz$, $V_O=1V_{rms}$	-	0.01	-	%

■ELECTRICAL CHARACTERISTICS

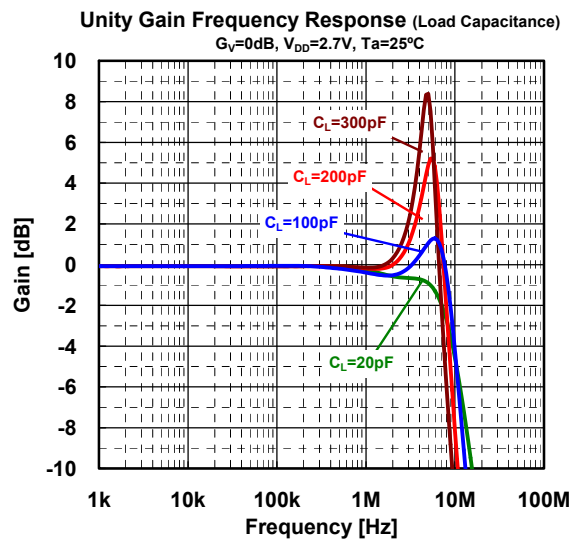
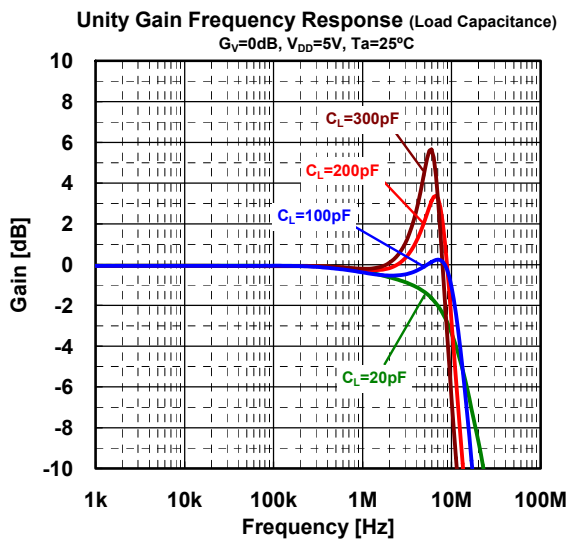
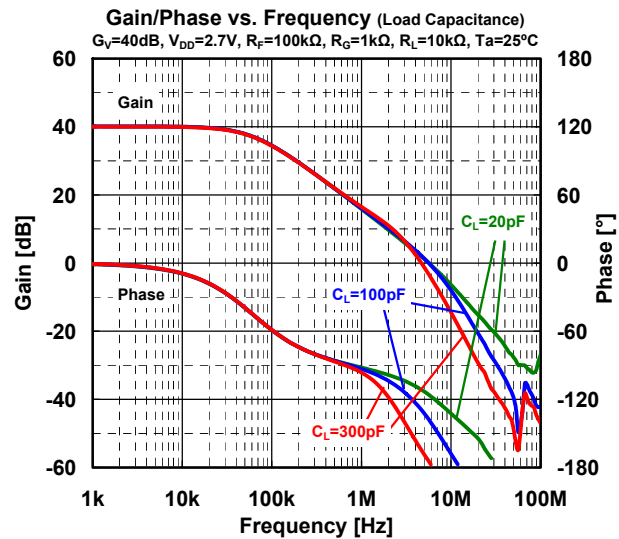
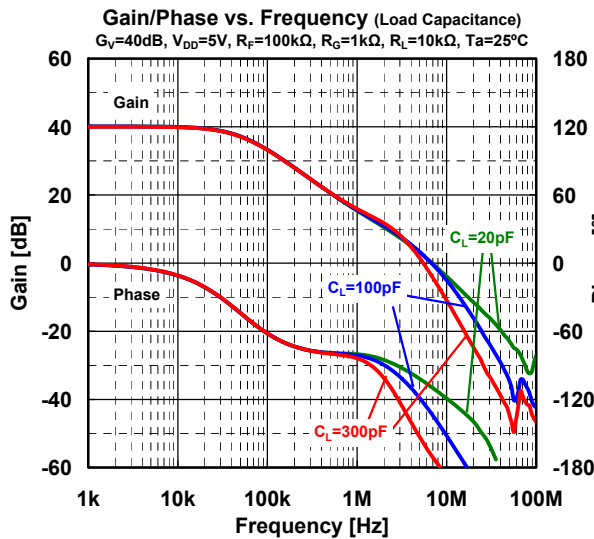
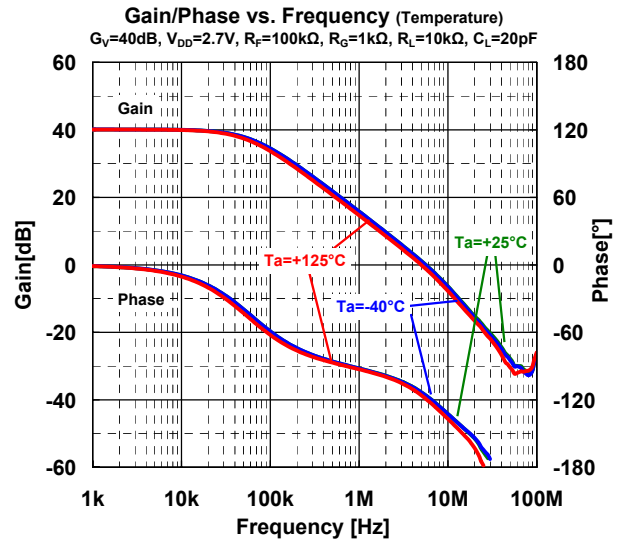
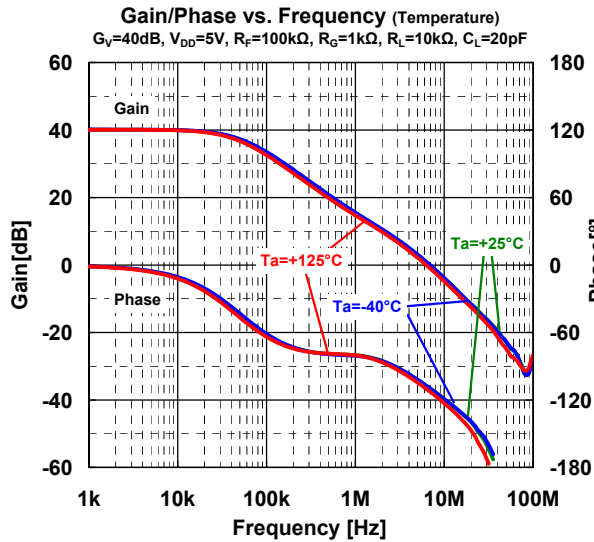
DC CHARACTER ($V_{DD}=2.7V$, $V_{SS}=0V$, $T_a=25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Current	I_{CC}	No Signal	-	1.2	2	mA
Input Offset Voltage	V_{IO}	$V_{ICM}=2.7V$	-	0.9	5	mV
		$V_{ICM}=1.35V$	-	0.9	5	mV
		$V_{ICM}=0V$	-	0.9	5	mV
Input Offset Voltage Drift	$\Delta V_{IO}/\Delta T$		-	2	-	$\mu V/^\circ C$
Input Bias Current	I_B		-	1	-	pA
Input Offset Current	I_{IO}		-	1	-	pA
Open Loop Gain	A_V	$V_{out}=0.35V$ to $2.35V$, $R_L=10k\Omega$ to $1.35V$	90	110	-	dB
Common Mode Rejection Ratio	CMR	$V_{ICM}=0V$ to $2.7V$	55	75	-	dB
Maximum Output Voltage	V_{OH}	$R_L=10k\Omega$ to $1.35V$	2.65	2.69	-	V
		$R_L=600\Omega$ to $1.35V$	2.6	2.64	-	V
	V_{OL}	$R_L=10k\Omega$ to $1.35V$	-	0.01	0.05	V
		$R_L=600\Omega$ to $1.35V$	-	0.05	0.1	V
Common Mode Input Voltage Range	V_{ICM}	CMR $\geq 55dB$	0	-	2.7	V

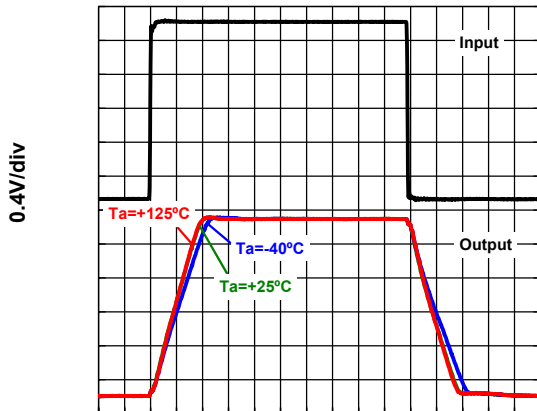
AC CHARACTER ($V_{DD}=2.7V$, $V_{SS}=0V$, $V_{ICM}=1.35V$, $T_a=25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gain Bandwidth Product	GBW	$G_V=40dB$, $R_F=100k\Omega$, $R_L=10k\Omega$, $C_L=20pF$	-	5	-	MHz
Phase Margin	ϕ_M	$G_V=40dB$, $R_F=100k\Omega$, $R_L=10k\Omega$, $C_L=20pF$	-	65	-	deg
Gain Margin	G_M	$G_V=40dB$, $R_F=100k\Omega$, $R_L=10k\Omega$, $C_L=20pF$	-	18	-	dB
Equivalent Input Noise Voltage	V_{NI}	$f=1kHz$	-	20	-	$nV/\sqrt{Hz}$
Slew Rate	SR	$G_V=0dB$, $R_L=10k\Omega$, $C_L=20pF$, $V_{IN}=2V_{PP}$	3.5	7	-	$V/\mu s$
Total Harmonic Distortion	THD	$G_V=0dB$, $R_L=10k\Omega$, $f=1kHz$, $V_O=1V_{rms}$	-	0.02	-	%

■ TYPICAL CHARACTERISTICS

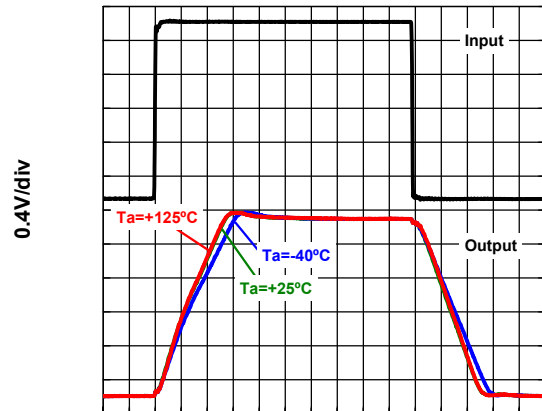


Transient Response (Temperature)
 $V_{DD}=5V$, $V_{IN}=2V_{pp}$, $R_L=10k\Omega$, $C_L=20pF$



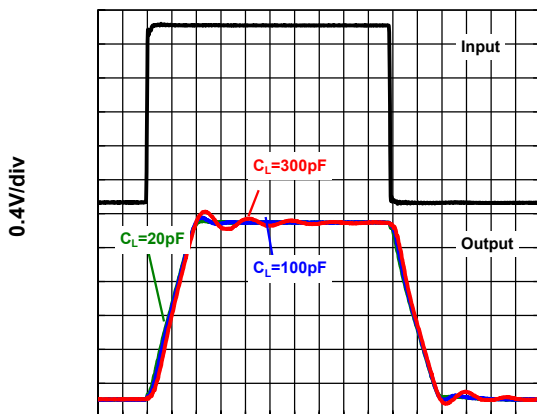
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Transient Response (Temperature)
 $V_{DD}=2.7V$, $V_{IN}=2V_{pp}$, $R_L=10k\Omega$, $C_L=20pF$



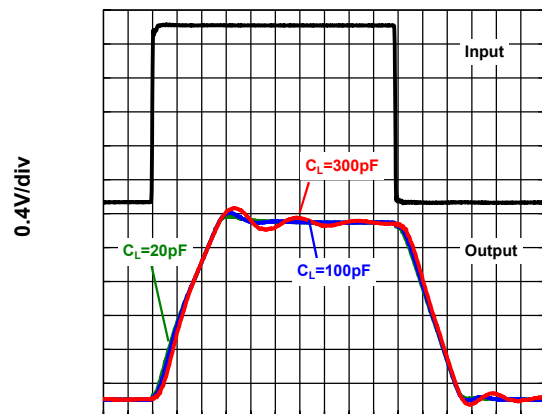
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Transient Response (Load Capacitance)
 $V_{DD}=5V$, $V_{IN}=2V_{pp}$, $R_L=10k\Omega$, $T_a=25^\circ C$



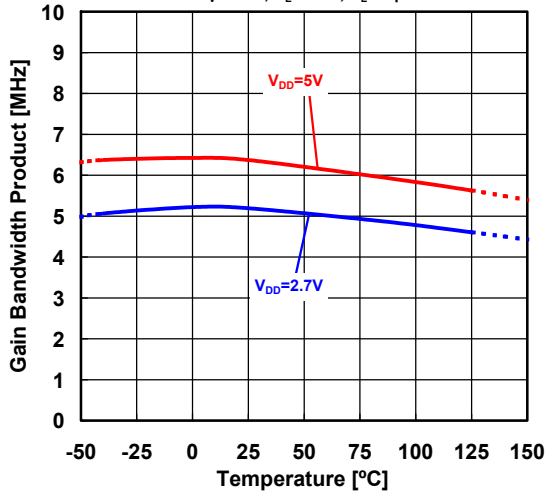
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Transient Response (Load Capacitance)
 $V_{DD}=2.7V$, $V_{IN}=2V_{pp}$, $R_L=10k\Omega$, $T_a=25^\circ C$

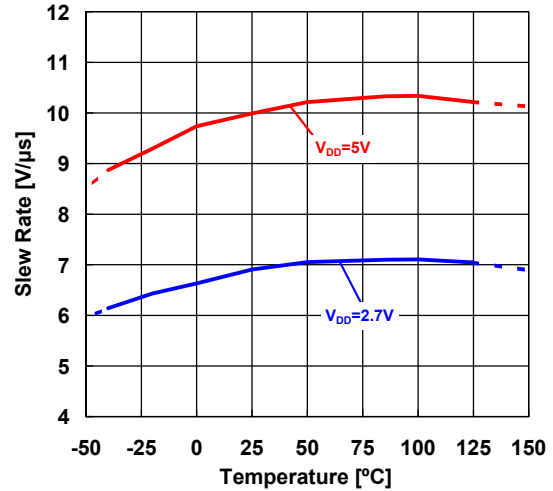


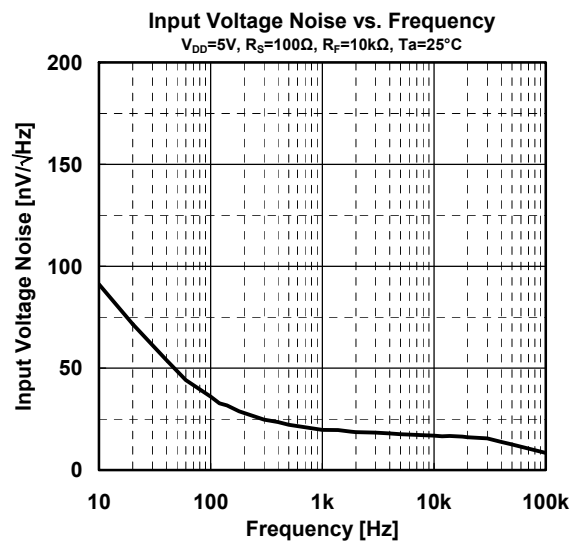
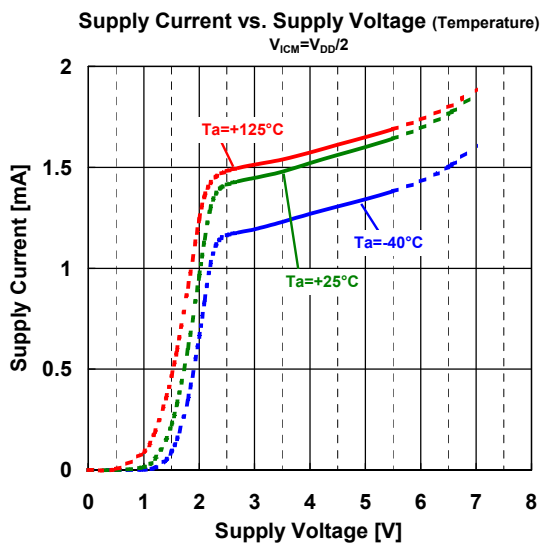
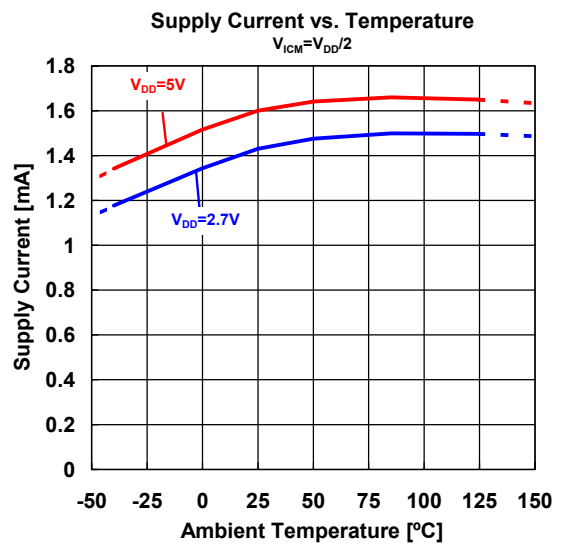
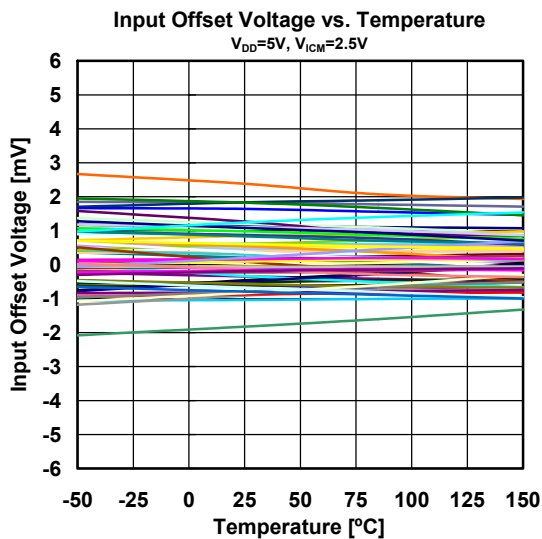
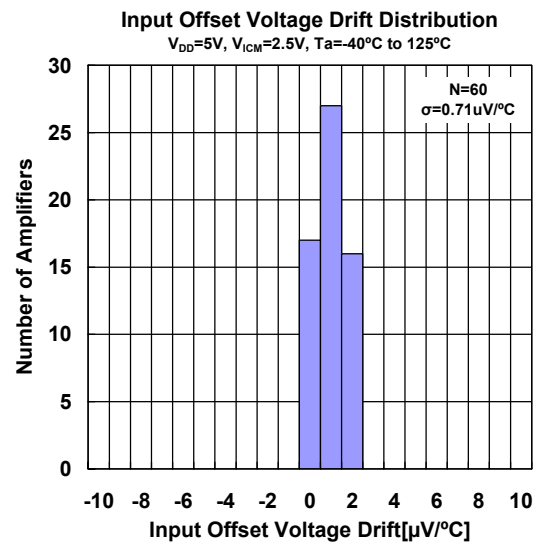
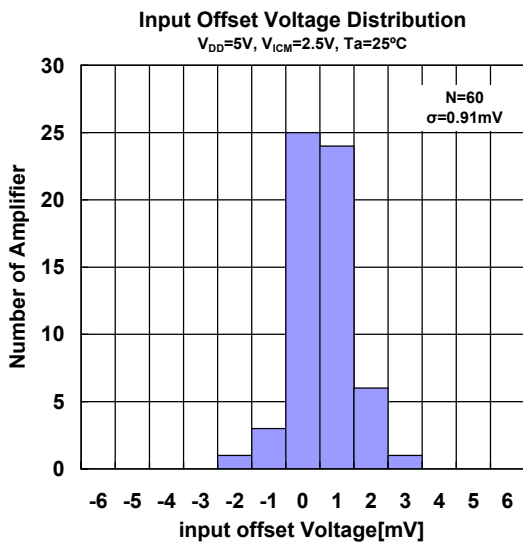
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Gain Bandwidth Product vs. Temperature
 $G_V=40dB$, $R_L=10k\Omega$, $C_L=20pF$



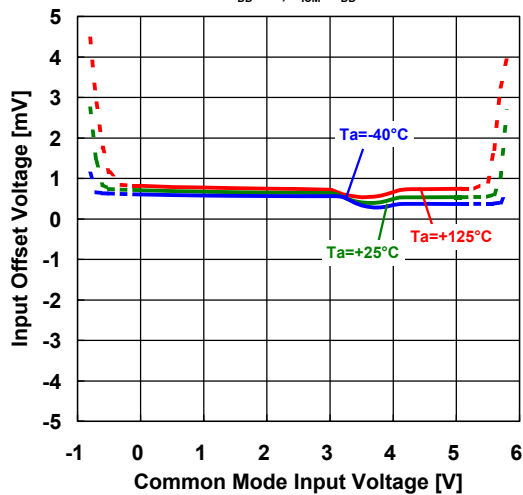
Slew Rate vs. Temperature (Supply Voltage)
 $G_V=0dB$, $V_{IN}=1V_{pp}$, $R_L=10k\Omega$, $C_L=20pF$





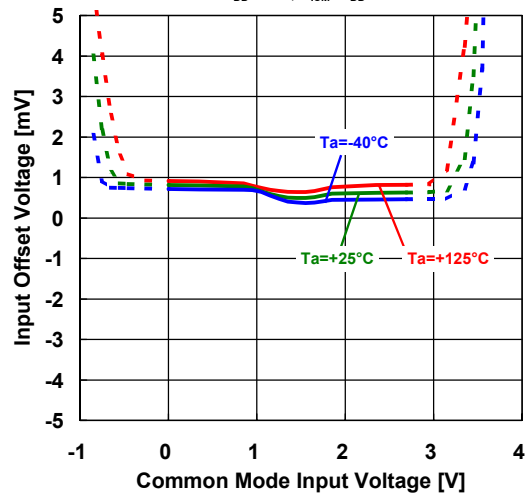
Input Offset Voltage vs. Common Mode Input Voltage (Temperature)

$V_{DD}=5V$, $V_{ICM}=V_{DD}/2$



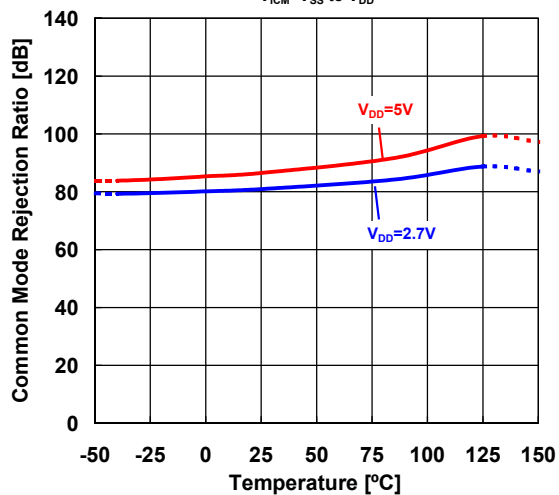
Input Offset Voltage vs. Common Mode Input Voltage (Temperature)

$V_{DD}=2.7V$, $V_{ICM}=V_{DD}/2$



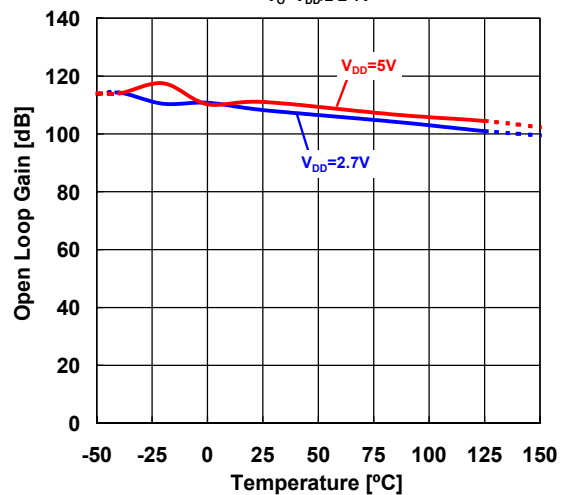
CMR vs. Temperature (Supply Voltage)

$V_{ICM}=V_{SS}$ to V_{DD}



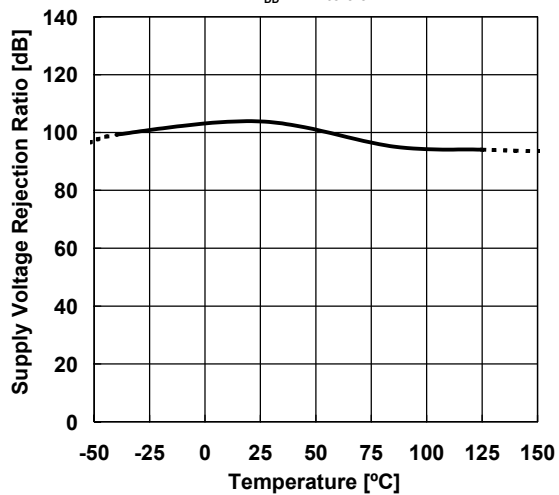
Open Loop Gain vs. Temperature (Supply Voltage)

$V_O=V_{DD}/2 \pm 1V$



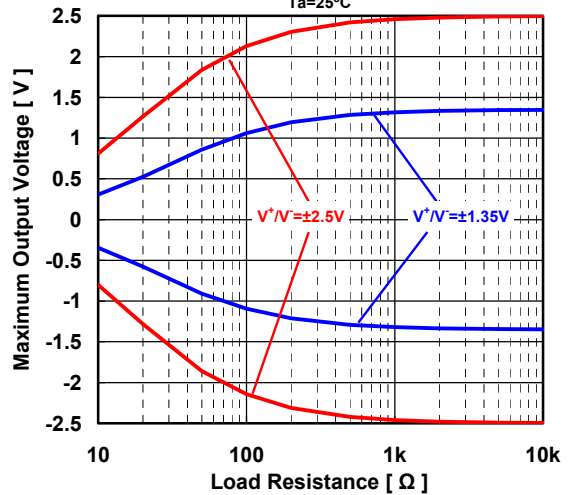
SVR vs. Temperature

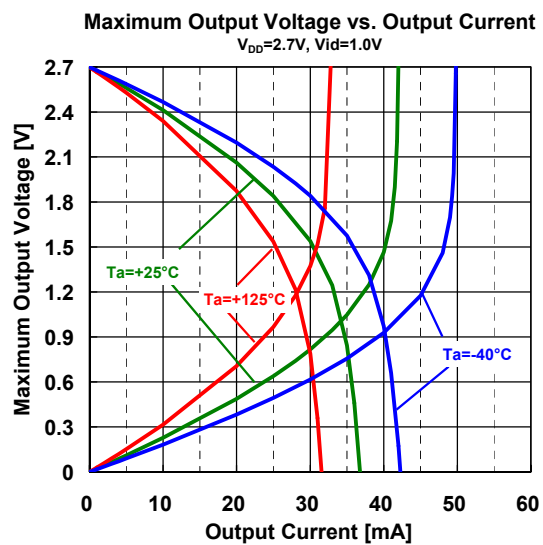
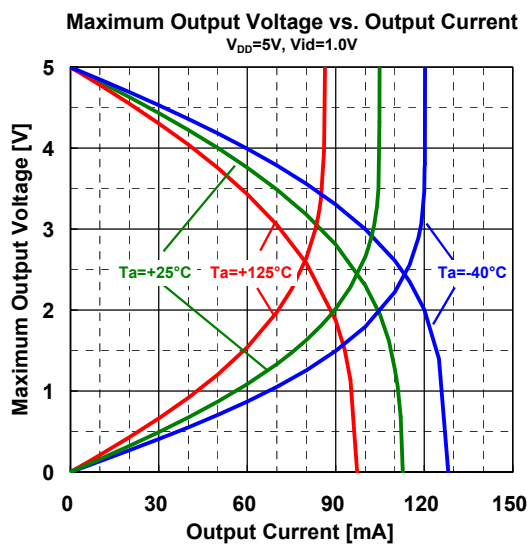
$V_{DD}=2.7V$ to $5.5V$



Maximum Output Voltage vs. Load Resistance

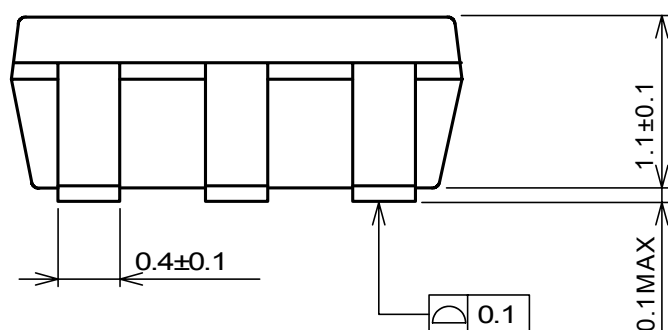
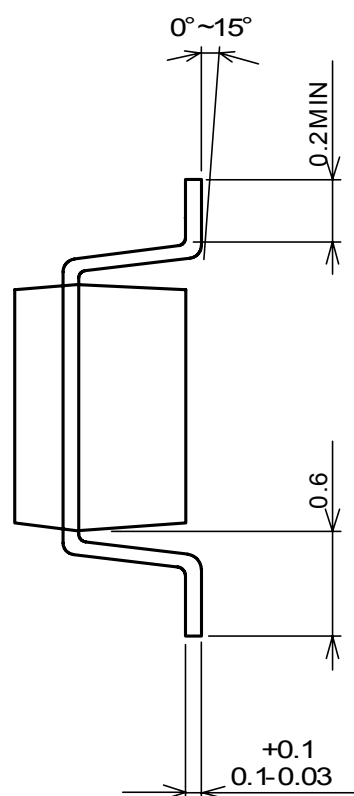
$T_a=25^\circ C$





Technical drawing of a rectangular plate with dimensions and labels. The plate has a central rectangular area with a double-line border. Five rectangular features are attached to the plate, labeled 1 through 5. Features 1, 2, and 3 are on the bottom edge, while features 4 and 5 are on the top edge. Dimensions are given in millimeters (mm) with tolerances.

- Overall width: 2.9 ± 0.2
- Distance between the centerlines of features 4 and 5: 1.9 ± 0.2
- Overall height: 2.8 ± 0.2
- Distance from the bottom edge to the centerline of feature 3: 0.95 ± 0.1
- Distance from the left edge to the centerline of feature 1: 0.6 MAX
- Distance from the centerline of feature 1 to the centerline of feature 2: 0.6 MAX
- Distance from the centerline of feature 2 to the centerline of feature 3: 0.6 MAX
- Distance from the centerline of feature 3 to the right edge: 0.6 MAX
- Distance from the centerline of feature 1 to the centerline of feature 5: 1.9 ± 0.2
- Distance from the centerline of feature 5 to the right edge: 0.6 MAX
- Distance from the centerline of feature 4 to the right edge: 0.6 MAX
- Distance from the centerline of feature 3 to the centerline of feature 4: 1.9 ± 0.2
- Distance from the centerline of feature 2 to the centerline of feature 5: 1.9 ± 0.2
- Distance from the centerline of feature 1 to the centerline of feature 4: 1.9 ± 0.2
- Distance from the centerline of feature 1 to the centerline of feature 3: 0.6 MAX
- Distance from the centerline of feature 2 to the centerline of feature 4: 0.6 MAX
- Distance from the centerline of feature 3 to the centerline of feature 5: 0.6 MAX
- Distance from the centerline of feature 4 to the centerline of feature 5: 0.6 MAX
- Distance from the centerline of feature 1 to the centerline of feature 4: 1.9 ± 0.2
- Distance from the centerline of feature 2 to the centerline of feature 5: 1.9 ± 0.2
- Distance from the centerline of feature 3 to the centerline of feature 5: 1.9 ± 0.2
- Distance from the centerline of feature 4 to the centerline of feature 5: 0.6 MAX
- Distance from the centerline of feature 1 to the centerline of feature 5: 1.9 ± 0.2
- Distance from the centerline of feature 2 to the centerline of feature 5: 1.9 ± 0.2
- Distance from the centerline of feature 3 to the centerline of feature 5: 1.9 ± 0.2
- Distance from the centerline of feature 4 to the centerline of feature 5: 0.6 MAX



UNIT: mm

Ver 01

■ MEMO

[CAUTION]

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