

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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MOS FIELD EFFECT TRANSISTOR

N0302P

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The N0302P is a switching device, which can be driven directly by a 4.0 V power source.

This N0302P features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- 4.0 V drive available
- Low on-state resistance
 $R_{DS(on)1} = 54 \text{ m}\Omega \text{ MAX. (} V_{GS} = -10 \text{ V, } I_D = -2.2 \text{ A)}$
 $R_{DS(on)2} = 77 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.5 \text{ V, } I_D = -2.2 \text{ A)}$
 $R_{DS(on)3} = 150 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.0 \text{ V, } I_D = -2.2 \text{ A)}$
- Built-in gate protection diode

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE
N0302P-T1-AT	Pure Sn (Tin)	Tape 3000 p/reel	SOT-23F

Marking: XW

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Drain to Source Voltage ($V_{GS} = 0 \text{ V}$)	V_{DSS}	-30	V
Gate to Source Voltage ($V_{DS} = 0 \text{ V}$)	V_{GSS}	± 20	V
Drain Current (DC)	$I_{D(DC)}$	± 4.4	A
Drain Current (pulse) ^{Note1}	$I_{D(pulse)}$	± 17.6	A
Total Power Dissipation	P_{T1}	0.2	W
Total Power Dissipation ^{Note2}	P_{T2}	1.3	W
Channel Temperature	T_{ch}	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

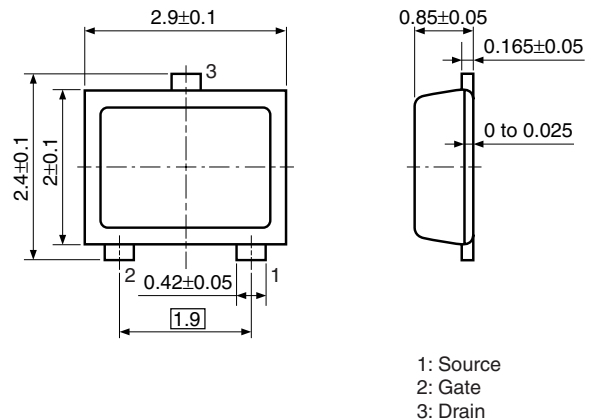
Notes 1. $PW \leq 10 \text{ }\mu\text{s}$, Duty Cycle $\leq 1\%$

2. Mounted on FR-4 board of 50 mm $\times$ 50 mm $\times$ 1.6 mm, copper foil 100%, $t \leq 5 \text{ sec}$.

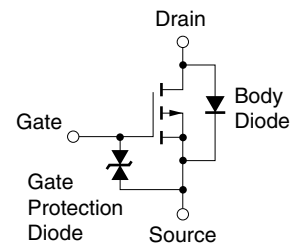
Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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PACKAGE DRAWING (Unit: mm)



EQUIVALENT CIRCUIT

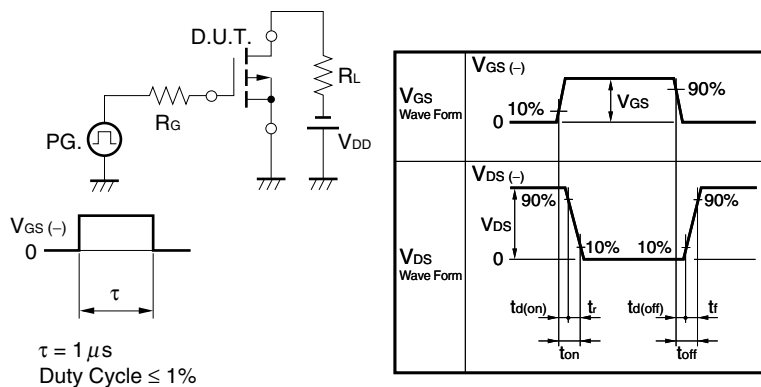


ELECTRICAL CHARACTERISTICS (T_A = 25°C)

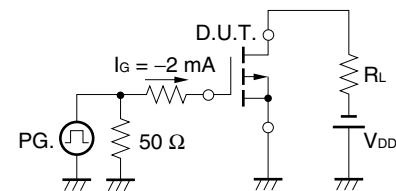
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V			-10	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V			±10	μA
Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1.0 mA	-1.0		-2.5	V
Forward Transfer Admittance ^{Note}	y _{fs}	V _{DS} = -10 V, I _D = -2.2 A	2.0			S
Drain to Source On-state Resistance ^{Note}	R _{DS(on)1}	V _{GS} = -10 V, I _D = -2.2 A		35	54	mΩ
	R _{DS(on)2}	V _{GS} = -4.5 V, I _D = -2.2 A		50	77	mΩ
	R _{DS(on)3}	V _{GS} = -4.0 V, I _D = -2.2 A		60	150	mΩ
Input Capacitance	C _{iss}	V _{DS} = -10 V,		620		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V,		130		pF
Reverse Transfer Capacitance	C _{rss}	f = 1.0 MHz		100		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = -15 V, I _D = -2.2 A,		8		ns
Rise Time	t _r	V _{GS} = -10 V,		3		ns
Turn-off Delay Time	t _{d(off)}	R _G = 10 Ω		53		ns
Fall Time	t _f			26		ns
Total Gate Charge	Q _G	V _{DD} = -24 V,		14		nC
Gate to Source Charge	Q _{GS}	V _{GS} = -10 V,		2.2		nC
Gate to Drain Charge	Q _{GD}	I _D = -4.4 A		4.4		nC
Body Diode Forward Voltage ^{Note}	V _{F(S-D)}	I _F = 4.4 A, V _{GS} = 0 V		0.88		V
Reverse Recovery Time	T _{rr}	I _F = 4.4 A, V _{GS} = 0 V,		29		ns
Reverse Recovery Charge	Q _{rr}	di/dt = 50 A/μs		12		nC

Note Pulsed

TEST CIRCUIT 1 SWITCHING TIME

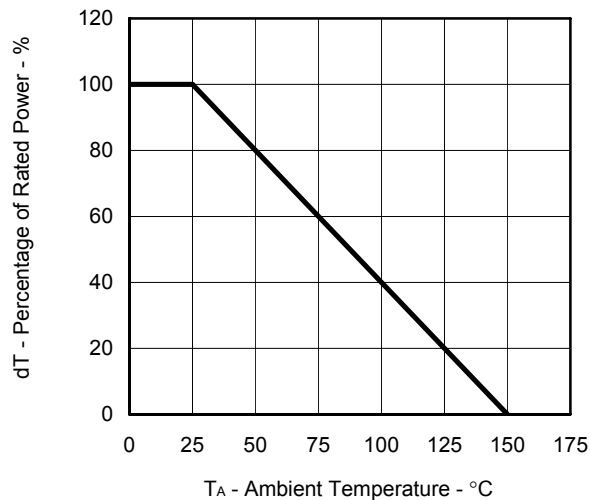


TEST CIRCUIT 2 GATE CHARGE

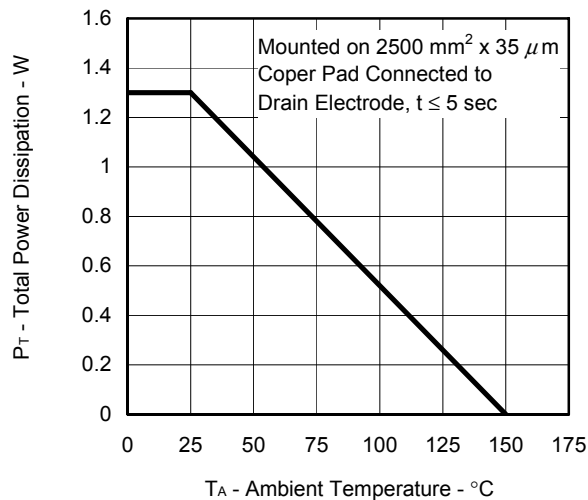


TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

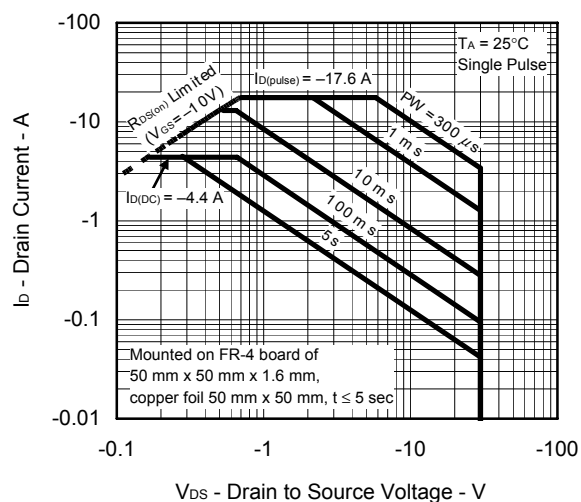
DERATING FACTOR OF FORWARD BIAS
SAFE OPERATING AREA



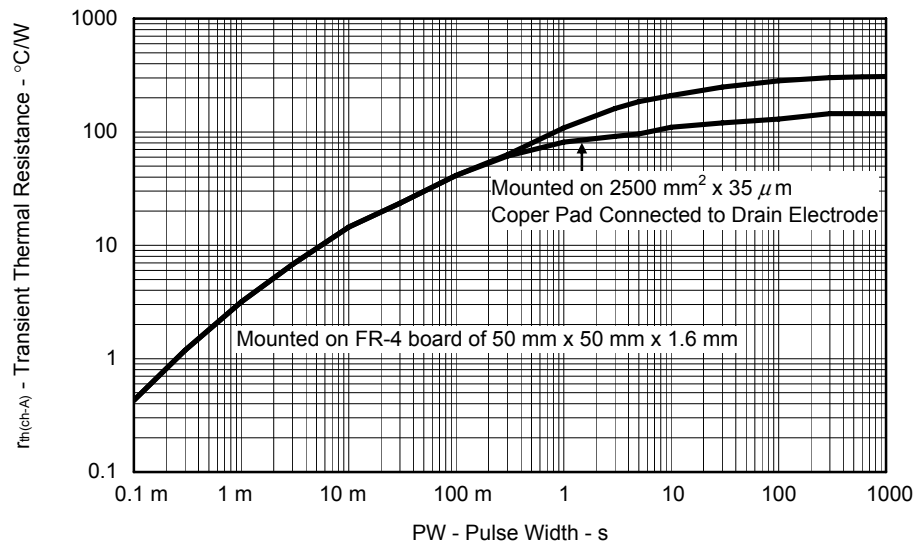
TOTAL POWER DISSIPATION vs.
AMBIENT TEMPERATURE



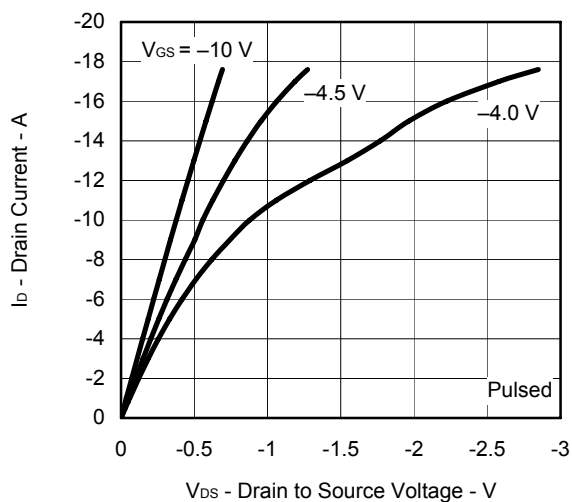
FORWARD BIAS SAFE OPERATING AREA



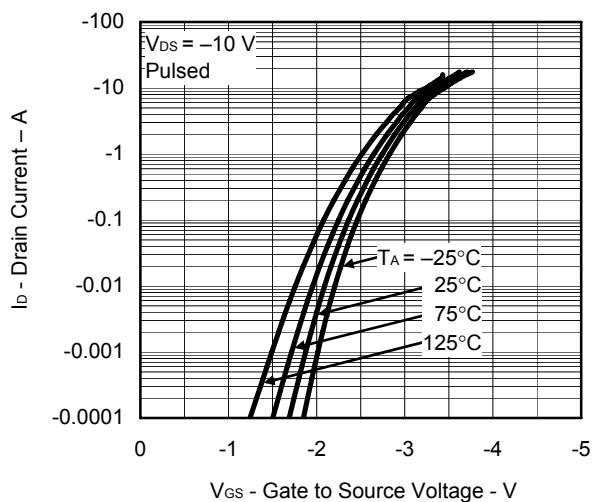
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



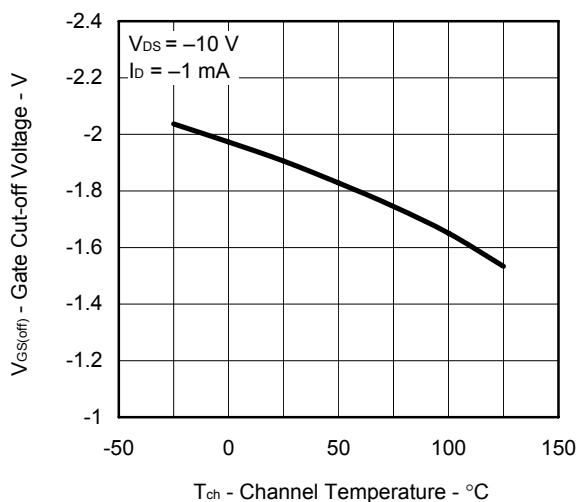
DRAIN CURRENT vs.
DRAIN TO SOURCE VOLTAGE



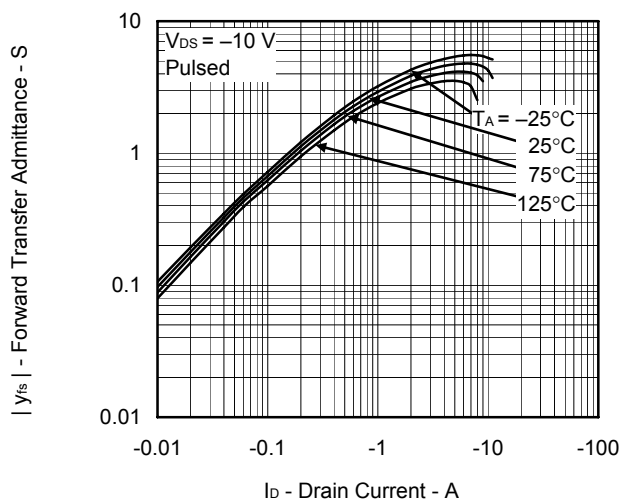
FORWARD TRANSFER CHARACTERISTICS



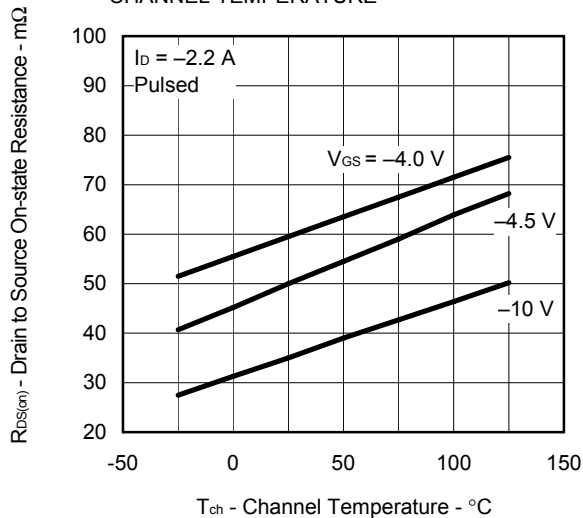
GATE CUT-OFF VOLTAGE vs.
CHANNEL TEMPERATURE



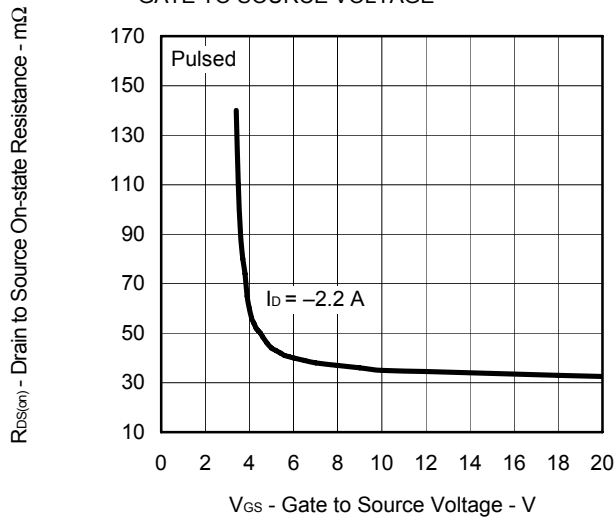
FORWARD TRANSFER ADMITTANCE vs.
DRAIN CURRENT



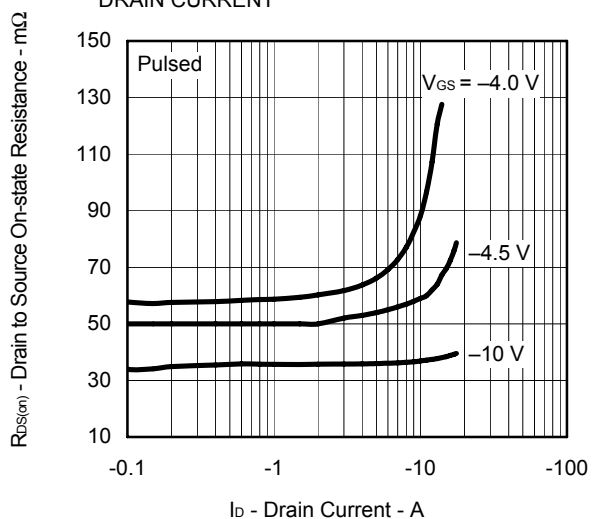
RAIN TO SOURCE ON-STATE RESISTANCE vs.
CHANNEL TEMPERATURE



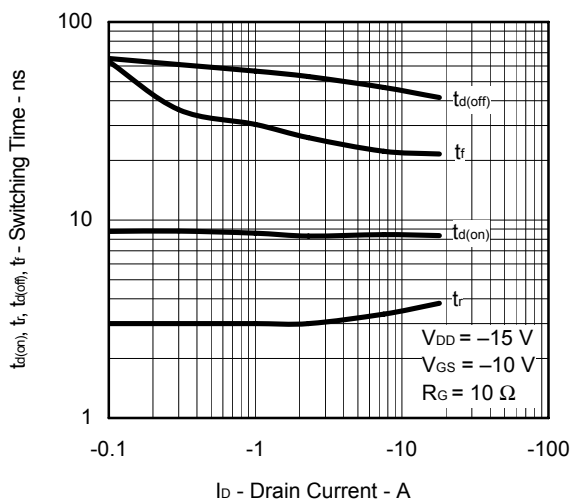
DRAIN TO SOURCE ON-STATE RESISTANCE vs.
GATE TO SOURCE VOLTAGE



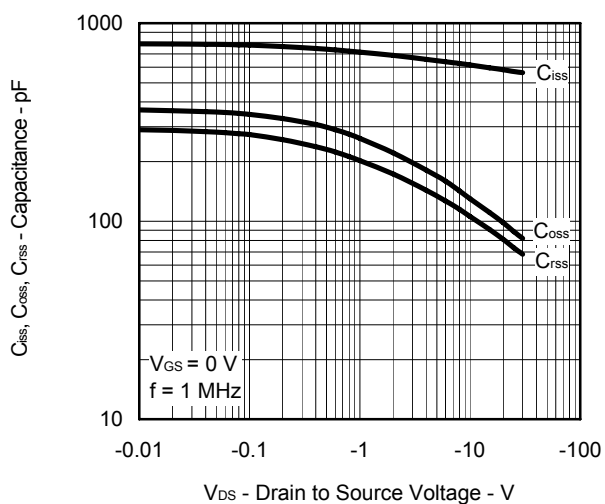
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



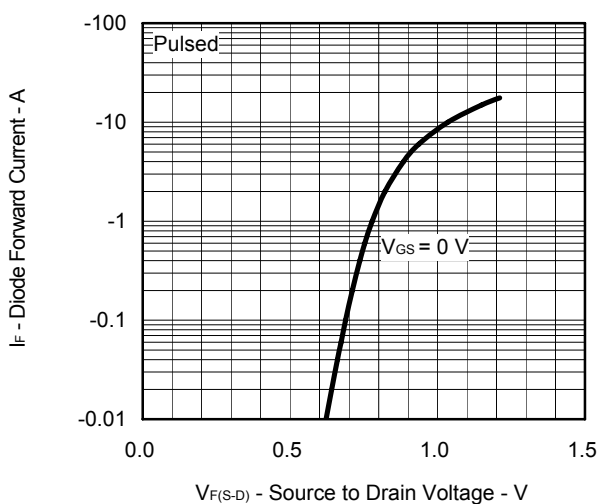
SWITCHING CHARACTERISTICS



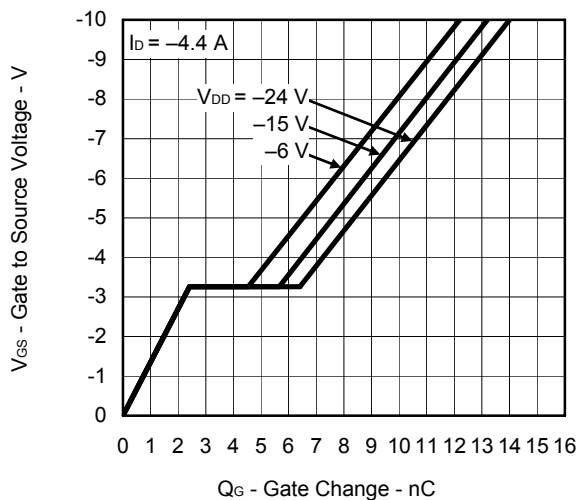
APACITANCE vs. DRAIN TO SOURCE VOLTAGE



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



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