Zibo Seno Electronic Engineering Co., Ltd.

淄博圣诺

MUR810D – MUR860D

8.0A GLASS PASSIVATED SUPERFAST RECTIFIER

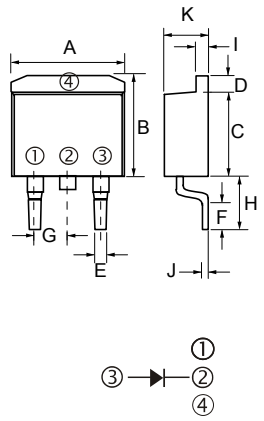
Features

- Glass Passivated Die Construction
- Super-Fast Switching
- Low Forward Voltage Drop
- Low Reverse Leakage Current
- High Surge Current Capability
- Plastic Material has UL Flammability Classification 94V-O

Mechanical Data

- Case: TO-263AC(D^2PAK), Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Mounting Position: Any
- **Lead Free: For RoHS / Lead Free Version**

D^2 PAK/TO-263AC



D^2 PAK/TO-263		
DIM.	MIN.	MAX.
A	9.80	10.20
B	9.60	10.60
C	8.50	9.20
D	----	1.67
E	0.51	1.01
F	2.10	2.50
G	2.44	2.64
H	4.40	4.70
I	1.10	1.40
J	0.30	0.64
K	4.40	4.80
All Dimensions in millimeter		

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	MUR 810D	MUR 820D	MUR 830D	MUR 840D	MUR 850D	MUR 860D	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	100	200	300	400	500	600	V
RMS Reverse Voltage	$V_{R(RMS)}$	70	140	210	280	350	420	V
Average Rectified Output Current @T_C = 105°C	I_O	8.0						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	90						A
Forward Voltage @I_F= 8.0A	V_{FM}	1.0		1.3		1.7		V
Peak Reverse Current @T_A = 25°C At Rated DC Blocking Voltage @T_A = 100°C	I_{RM}	5.0 400						μA
Reverse Recovery Time (Note 1)	t_{rr}	35						nS
Typical Junction Capacitance (Note 2)	C_j	80				50		pF
Operating and Storage Temperature Range	T_j, T_{STG}	-55 to +150						°C

Note: 1. Measured with IF = 0.5A, IR = 1.0A, IRR = 0.25A.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

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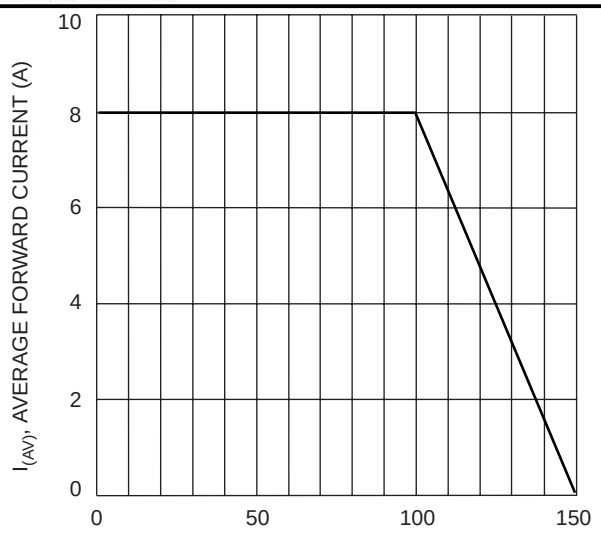


Fig. 1 Forward Current Derating Curve

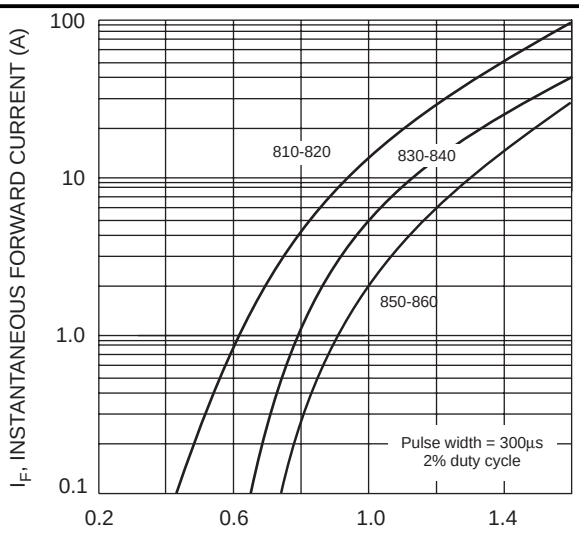


Fig. 2 Typical Forward Characteristics

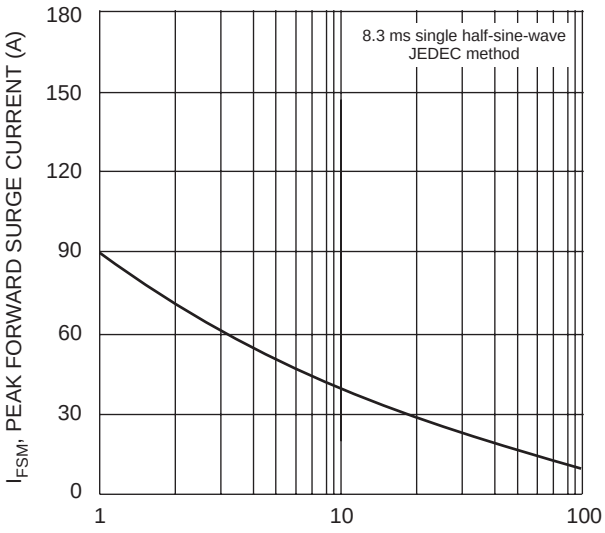


Fig. 3 Max Non-Repetitive Surge Current

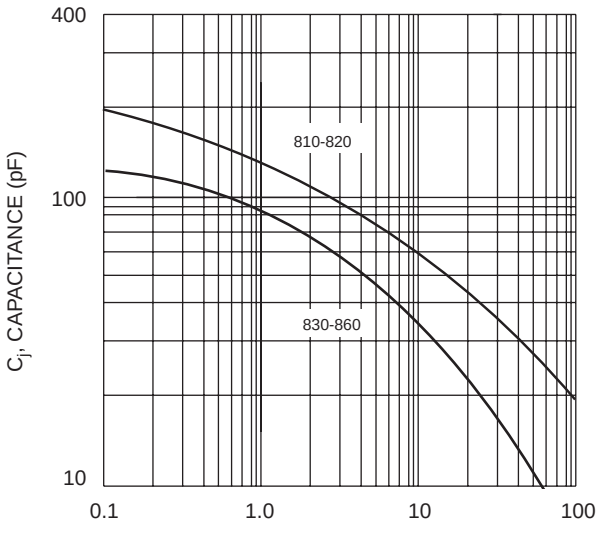
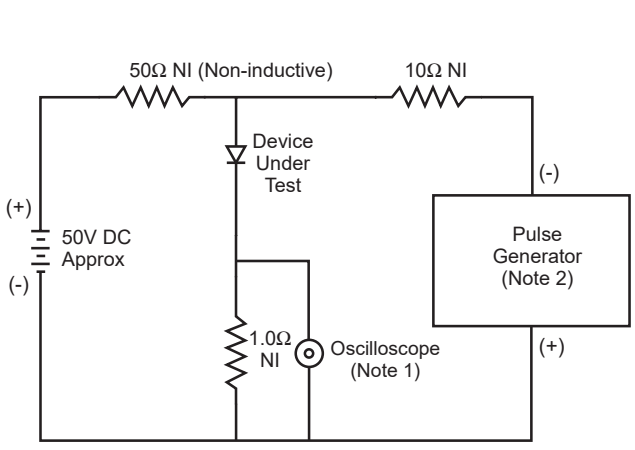
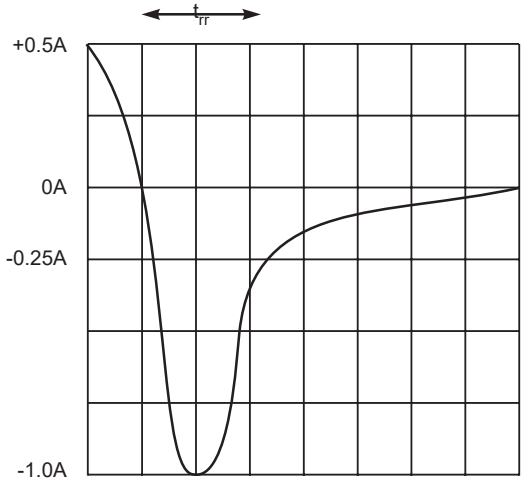


Fig. 4 Typical Junction Capacitance





Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
2. Rise Time = 10ns max. Input Impedance = 50Ω.

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

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