

## DESCRIPTION

The MP44014-A is a boundary conduction mode PFC controller that provides simple and high-performance active power-factor correction using minimum external components.

The output voltage is regulated accurately by a high-performance voltage-mode amplifier with an accurate internal voltage reference.

The precise adjustable output over-voltage protection greatly enhances the system reliability.

The on-chip R/C filter on the current sense pin can potentially eliminate the external R/C filter.

The extremely low start-up current, quiescent current, and the disable function reduces the power consumption, resulting in excellent efficiency performance.

The MP44014-A is available in a SOIC-8.

## FEATURES

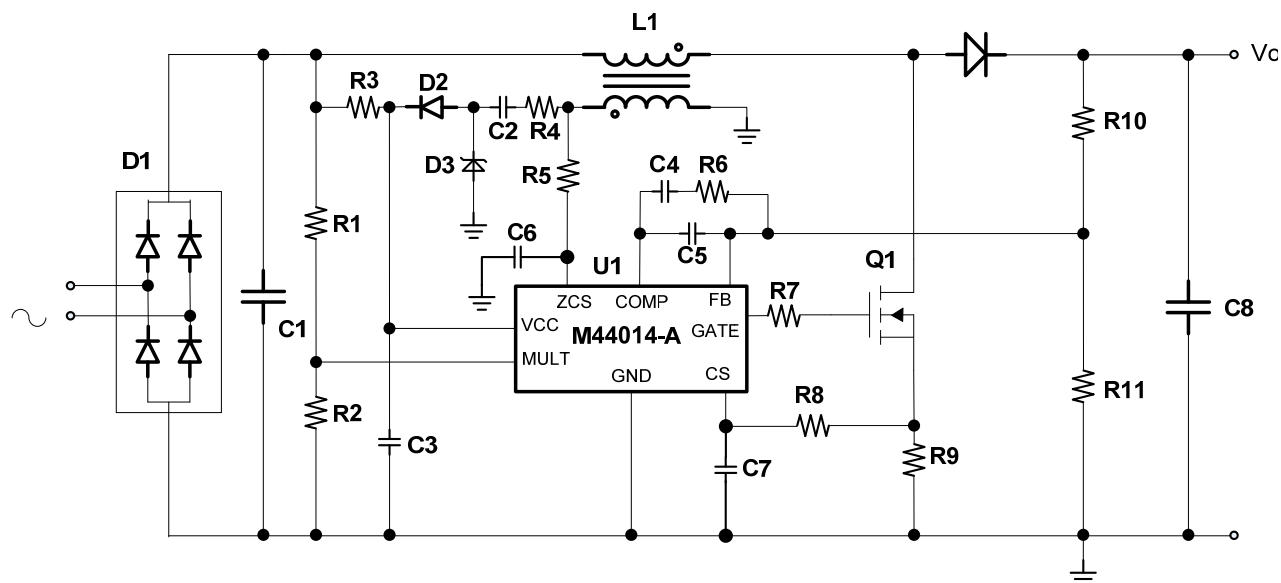
- Boundary Conduction Mode PFC Controller for Pre-Regulator
- Zero-Crossing Compensation to Minimum THD of AC Input Current
- Precise Adjustable Output Over-Voltage Protection
- Ultra-Low (15 $\mu$ A) Start-Up Current
- Low Quiescent Current (0.46mA) at OVP Condition
- On-Chip Filter on Current Sense Pin
- Enable/Disable and Open-Loop Protection Function on FB
- -750/+800mA Peak Gate-Drive Current

## APPLICATIONS

- Offline Adaptors
- Electronic Ballasts
- LLC Front End
- Other PFC Pre-Regulators

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## TYPICAL APPLICATION



## ORDERING INFORMATION

| Part Number  | Package | Top Marking |
|--------------|---------|-------------|
| MP44014-AGS* | SOIC-8  | See Below   |

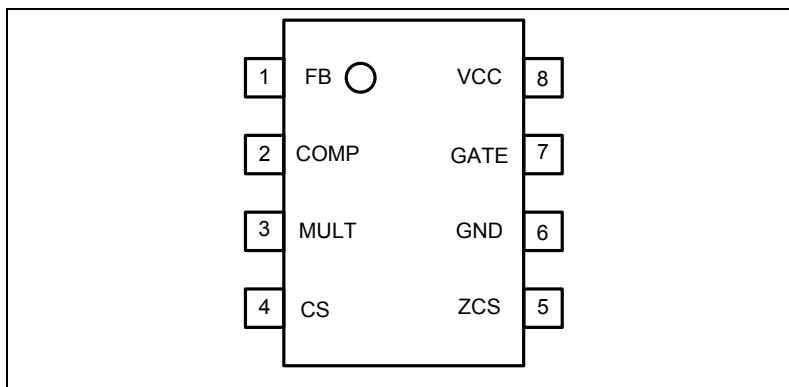
\* For Tape & Reel, add suffix –Z (e.g. MP44014-AGS–Z).

## TOP MARKING

**M44014-A**  
**LLLLLLLL**  
**MPSYWW**

M44014-A: Product Code of MP44014-AGS  
 LLLLLLLL: Lot Number  
 MPS: MPS Prefix  
 Y: Year Code  
 WW: Week Code

## PACKAGE REFERENCE



## ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup>

|                                                                      |                     |
|----------------------------------------------------------------------|---------------------|
| Supply Voltage VCC .....                                             | -0.5V to Self Limit |
| Zero Current Sensing (ZCS) .                                         | -0.3V to Self Limit |
| Other Analog Inputs and Outputs                                      | -0.3V to 6.5V       |
| ZCS Max. Current .....                                               | -2.0mA to 10mA      |
| Continuous Power Dissipation (T <sub>A</sub> = +25°C) <sup>(2)</sup> |                     |
| SOIC8 .....                                                          | 1.4W                |
| Junction Temperature .....                                           | 150°C               |
| Lead Temperature (Solder).....                                       | 260°C               |
| Storage Temperature.....                                             | -55°C to +150°C     |

## Recommended Operating Conditions <sup>(3)</sup>

|                                               |                 |
|-----------------------------------------------|-----------------|
| Supply Voltage VCC .....                      | 13.8V to 21V    |
| Operating Junction Temp. (T <sub>J</sub> ). - | -40°C to +125°C |

## Thermal Resistance <sup>(4)</sup>

|              | $\theta_{JA}$ | $\theta_{JC}$ |
|--------------|---------------|---------------|
| SOIC-8 ..... | 90            | 45 ... °C/W   |

### Notes:

- 1) Exceeding these ratings may damage the device.
- 2) The maximum allowable power dissipation is a function of the maximum junction temperature T<sub>J</sub>(MAX), the junction-to-ambient thermal resistance  $\theta_{JA}$ , and the ambient temperature T<sub>A</sub>. The maximum allowable continuous power dissipation at any ambient temperature is calculated by  $D(MAX) = (T_J(MAX) - T_A) / \theta_{JA}$ . Exceeding the maximum allowable power dissipation produces an excessive die temperature, causing the regulator to go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
- 3) The device is not guaranteed to function outside of its operating conditions.
- 4) Measured on JESD51-7, 4-layer PCB.

## ELECTRICAL CHARACTERISTICS

VCC = 15V, T<sub>J</sub> = -40°C~+125°C, unless otherwise noted

| Parameter                        | Symbol                               | Condition                                                           | Min   | Typ  | Max   | Units |
|----------------------------------|--------------------------------------|---------------------------------------------------------------------|-------|------|-------|-------|
| Supply Voltage                   |                                      |                                                                     |       |      |       |       |
| Operating Range                  | V <sub>CC</sub>                      | After Turn-On                                                       | 10.7  |      | 21    | V     |
| Turn-On Threshold                | V <sub>CC_on</sub>                   |                                                                     | 11    | 12.4 | 13.8  | V     |
| Turn-Off Threshold               | V <sub>CC_off</sub>                  |                                                                     | 8.7   | 9.8  | 10.8  | V     |
| Hysteresis                       | V <sub>CC_hys</sub>                  |                                                                     | 2.0   |      | 3.1   | V     |
| Zener Voltage                    | V <sub>Z</sub>                       | I <sub>CC</sub> =20mA                                               | 22    | 25   | 28    | V     |
| Supply Current                   |                                      |                                                                     |       |      |       |       |
| Start-Up Current                 | I <sub>startup</sub>                 | V <sub>CC</sub> =11V                                                |       | 15   | 40    | μA    |
| Quiescent Current                | I <sub>q</sub>                       | No Switch                                                           |       | 2.5  | 3.2   | mA    |
| Operating Current                | I <sub>cc</sub>                      | F <sub>s</sub> =70kHz, C <sub>LOAD</sub> =1nF                       |       | 3.5  | 4.5   | mA    |
| Quiescent Current                | I <sub>q</sub>                       | During OVP (Either Static or Dynamic)<br>or V <sub>FB</sub> ≤150mV  |       | 0.46 | 0.7   | mA    |
| Multiplier                       |                                      |                                                                     |       |      |       |       |
| Input Bias Current               | I <sub>MULT</sub>                    | V <sub>MULT</sub> =3V                                               |       |      | -1    | μA    |
| Linear Operation Range           | V <sub>MULT</sub>                    |                                                                     | 0     |      | 3     | V     |
| Output Max. Slope                | ΔV <sub>CS</sub> /ΔV <sub>MULT</sub> | V <sub>MULT</sub> =0~0.6V<br>V <sub>COMP</sub> =Upper Clamp         | 1.0   | 1.10 |       | V/V   |
| Gain <sup>(5)</sup>              | K                                    | V <sub>MULT</sub> =1V, V <sub>COMP</sub> =4V, T <sub>J</sub> = 25°C | 0.32  | 0.38 | 0.45  | 1/V   |
| Error Amplifier                  |                                      |                                                                     |       |      |       |       |
| Feedback Voltage                 | V <sub>FB</sub>                      | T <sub>J</sub> = 25°C                                               | 2.465 | 2.5  | 2.535 | V     |
| Feedback Voltage Line Regulation | V <sub>FB_LR</sub>                   | V <sub>CC</sub> =10.7V to 21V                                       |       | 2    | 5     | mV    |
| Feedback Bias Current            | I <sub>FB</sub>                      | V <sub>FB</sub> =0~2.6V                                             |       |      | 1     | μA    |
| Source Current                   | I <sub>COMP_source</sub>             | V <sub>COMP</sub> =4V, V <sub>FB</sub> =2.4V, T <sub>J</sub> = 25°C | -2.7  | -4.7 | -6.9  | mA    |
| Sink Current                     | I <sub>COMP_sink</sub>               | V <sub>COMP</sub> =4V, V <sub>FB</sub> =2.6V, T <sub>J</sub> = 25°C | 3     | 5    |       | mA    |
| Upper Clamp Voltage              | V <sub>COMP_H</sub>                  | V <sub>FB</sub> =2V, I <sub>source</sub> =-0.5mA                    | 5.3   | 6    | 6.6   | V     |
| Lower Clamp Voltage              | V <sub>COMP_L</sub>                  | V <sub>FB</sub> =3V, I <sub>sink</sub> =0.5mA                       | 2.0   | 2.15 | 2.3   | V     |
| FB_Disable Threshold             | V <sub>FB_DIS</sub>                  |                                                                     | 150   | 200  | 250   | mV    |
| FB_EN Hysteresis                 | V <sub>FB_EN_hys</sub>               |                                                                     |       | 250  |       | mV    |
| Current-Sense Comparator         |                                      |                                                                     |       |      |       |       |
| Input Bias Current               | I <sub>CS</sub>                      | V <sub>CS</sub> =6.5V                                               |       |      | -1    | μA    |
| Turn-Off Delay                   | T <sub>DT</sub>                      | V <sub>MULT</sub> =0.2V                                             |       | 175  |       | ns    |
| LEB Time                         | T <sub>LEB</sub>                     | V <sub>MULT</sub> =0.2V                                             | 40    | 80   | 130   | ns    |
| Current-Sense Clamp Voltage      | V <sub>CS_Clamp</sub>                |                                                                     | 1.06  | 1.14 | 1.22  | V     |
| Current-Sense Offset             | V <sub>CS_Offset</sub>               | V <sub>MULT</sub> =0V                                               |       | 25   |       | mV    |
|                                  |                                      | V <sub>MULT</sub> =2.5V                                             |       | 6    |       | mV    |
| Zero Current Sensor              |                                      |                                                                     |       |      |       |       |
| Upper Clamp Voltage              | V <sub>ZCSclamp_H</sub>              | I <sub>ZCS</sub> =2.5mA                                             | 7.2   | 7.8  | 8.7   | V     |

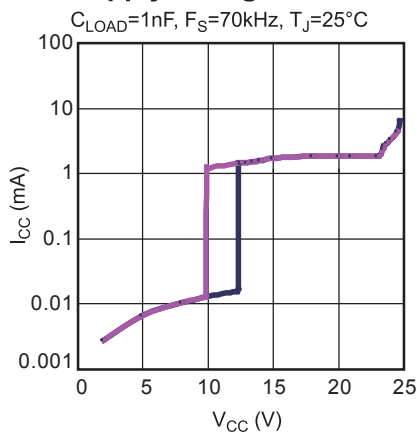
**ELECTRICAL CHARACTERISTICS** *(continued)*VCC = 15V, T<sub>A</sub> = T<sub>J</sub> = -40°C~+125°C, unless otherwise noted.

| Parameter                      | Symbol                   | Condition                                                    | Min  | Typ   | Max  | Units |
|--------------------------------|--------------------------|--------------------------------------------------------------|------|-------|------|-------|
| Lower Clamp Voltage            | V <sub>ZCSclamp_L</sub>  | I <sub>ZCS</sub> =-2.5mA                                     | -0.3 | -0.15 | 0    | V     |
| Zero-Current Sensing Threshold | V <sub>ZCS_H</sub>       | V <sub>ZCS</sub> Rising                                      |      | 1.4   |      | V     |
|                                | V <sub>ZCS_L</sub>       | V <sub>ZCS</sub> Falling                                     |      | 0.7   |      | V     |
| Re-Start Current After Disable | I <sub>ZCS_res</sub>     |                                                              | 55   | 85    |      | μA    |
| <b>Re-Starter</b>              |                          |                                                              |      |       |      |       |
| Re-Start Time                  | T <sub>start</sub>       |                                                              | 80   | 175   | 280  | μs    |
| <b>Over-Voltage</b>            |                          |                                                              |      |       |      |       |
| Dynamic OVP Current            | I <sub>OVP</sub>         |                                                              | 23.5 | 27    | 30.5 | μA    |
| Hysteresis                     | I <sub>OVP_Hys</sub>     |                                                              |      | 20    |      | μA    |
| Static OVP Threshold           | V <sub>OVP</sub>         |                                                              | 2.1  | 2.25  | 2.4  | V     |
| <b>Gate Driver</b>             |                          |                                                              |      |       |      |       |
| Output High Voltage            | V <sub>OH</sub>          | I <sub>GDsource</sub> =20mA                                  | 11.5 | 12.5  |      | V     |
| Output Low Voltage             | V <sub>OL</sub>          | I <sub>GDsink</sub> =200mA                                   |      | 0.9   | 1.9  | V     |
| Voltage Fall Time              | T <sub>f</sub>           |                                                              |      | 30    | 70   | ns    |
| Voltage Rise Time              | T <sub>r</sub>           |                                                              |      | 40    | 80   | ns    |
| Max. Output Drive Voltage      | V <sub>D_max</sub>       |                                                              | 12   | 13.5  | 15.5 | V     |
| Source Current Capability      | I <sub>Gate_source</sub> |                                                              |      | -750  |      | mA    |
| Sink Current Capability        | I <sub>Gate_sink</sub>   |                                                              |      | 800   |      | mA    |
| UVLO Saturation Voltage        | V <sub>Saturation</sub>  | VCC=0 to VCC <sub>ON</sub> ,<br>I <sub>Gate_sink</sub> =10mA |      |       | 0.3  | V     |

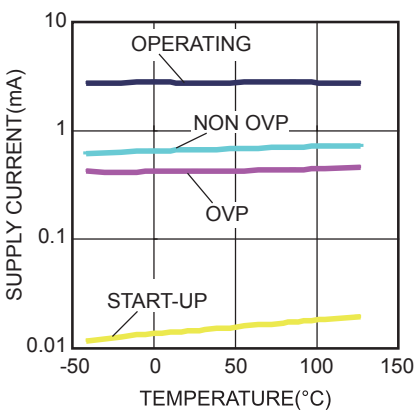
**Note:**5) The multiplier output is given by: V<sub>CS</sub>=K·V<sub>MUTL</sub>·(V<sub>COMP</sub>-2.5)

# TYPICAL PERFORMANCE CHARACTERISTICS

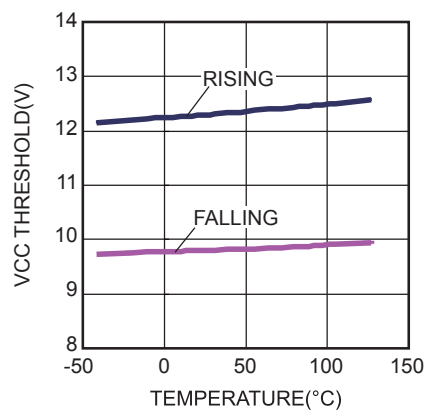
Supply Current vs. Supply Voltage



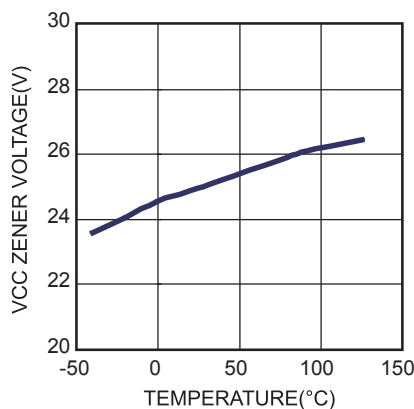
Supply Current vs.  $T_J$



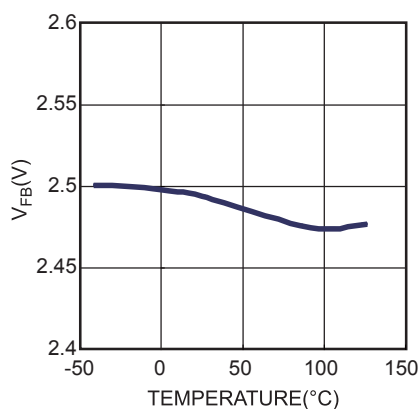
Start-Up & UVLO vs.  $T_J$



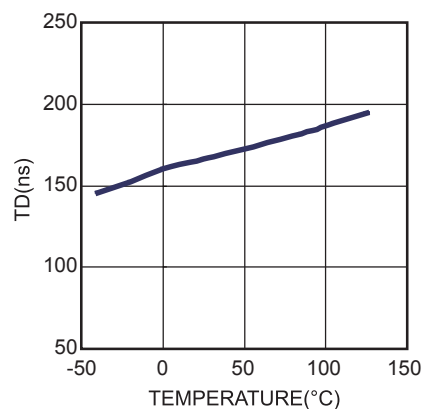
VCC Zener Voltage vs.  $T_J$



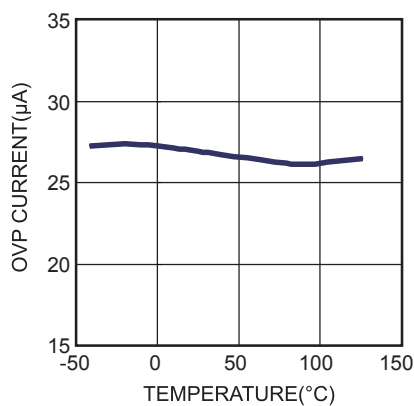
Feedback Reference vs.  $T_J$



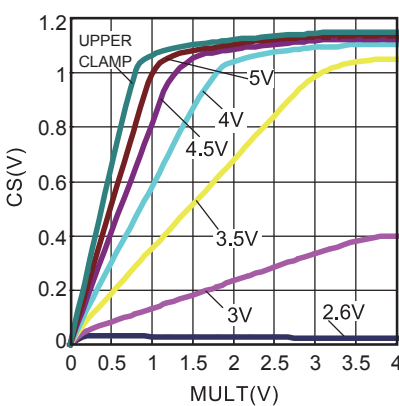
Delay-to-Output vs.  $T_J$



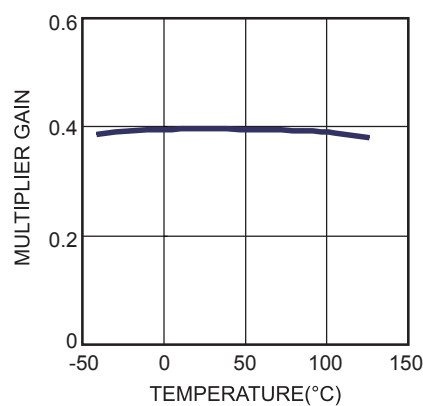
OVP Current vs.  $T_J$



Multiplier Characteristic

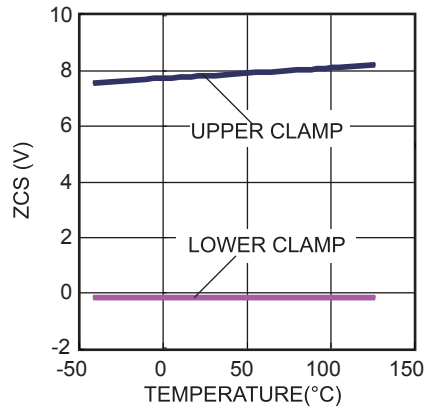


Multiplier Gain vs.  $T_J$

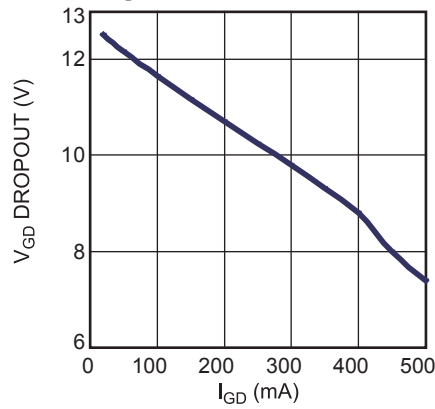


## TYPICAL PERFORMANCE CHARACTERISTICS (*continued*)

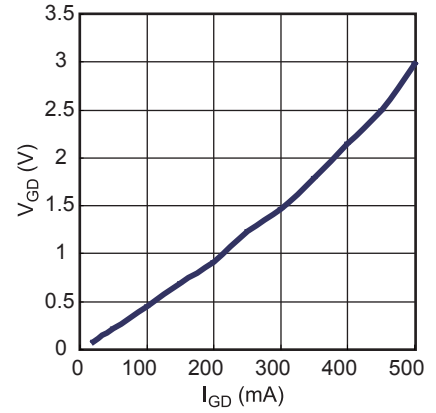
ZCS Clamp Levels vs.  $T_J$



Gate-Drive Output  
High Saturation

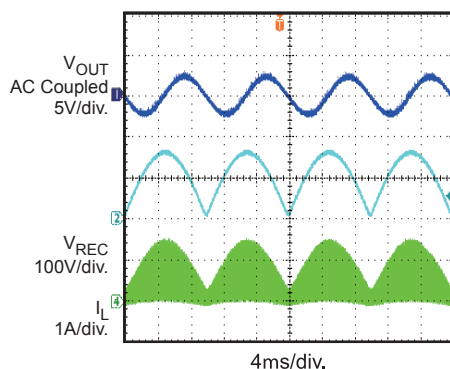


Gate-Drive Output  
Low Saturation

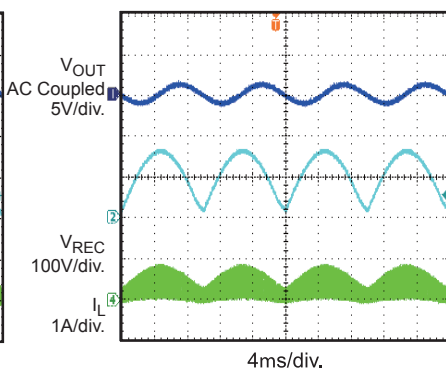


# TYPICAL PERFORMANCE CHARACTERISTICS (continued)

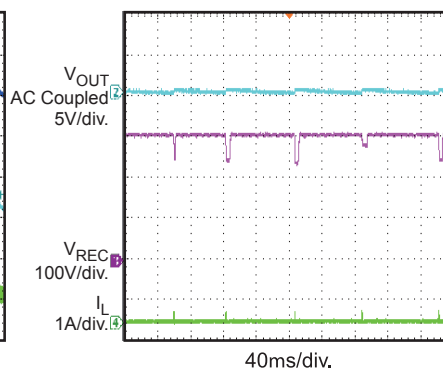
Steady State

 $P_{OUT} = 100W$ 


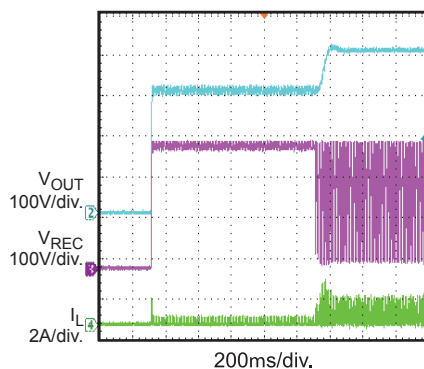
Steady State

 $P_{OUT} = 50W$ 


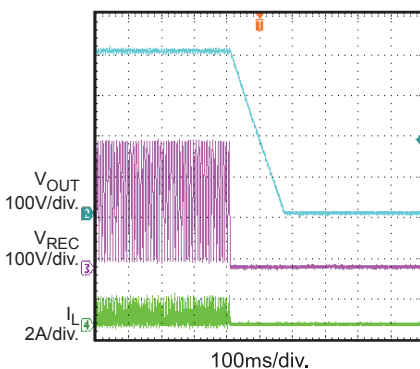
Steady State

 $P_{OUT} = 0W$ 


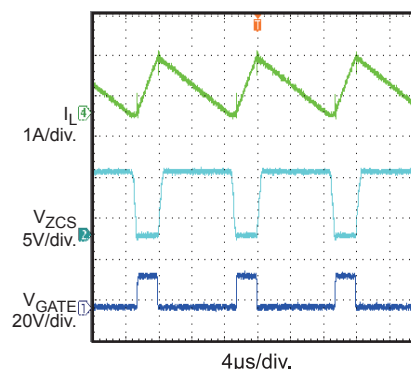
Start-Up



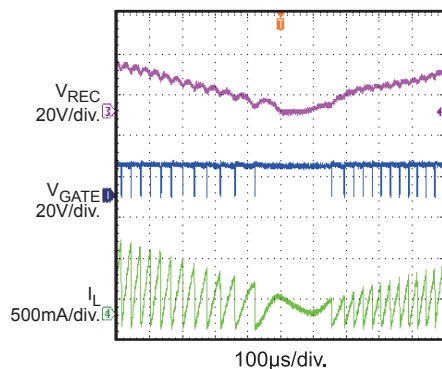
Shutdown



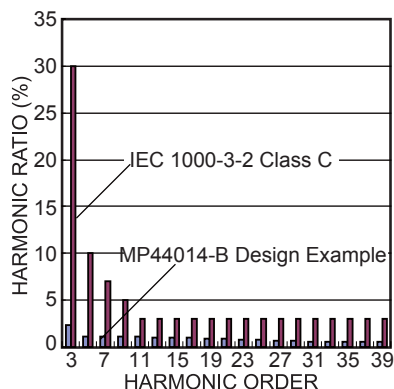
Zero-Current Sensing



Zero-Crossing Compensation

 $V_{AC} = 85V$ 


Harmonics



## PIN FUNCTIONS

| Pin # | Name | Description                                                                                                                                                                                                                                                                                                            |
|-------|------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1     | FB   | Feedback. The output voltage is fed into FB through a resistor divider. Also, FB can be used as an enable and disable pin.                                                                                                                                                                                             |
| 2     | COMP | Output of the Error Amplifier. A compensation network is connected between COMP and FB.                                                                                                                                                                                                                                |
| 3     | MULT | Input of the Multiplier. Connect MULT to the rectified main voltage via a resistor divider to provide the sinusoidal reference for the current control loop.                                                                                                                                                           |
| 4     | CS   | Current Sense. The current through the MOSFET is fed into CS via a resistor. The resulting voltage on CS is compared with the output of the internal multiplier to get an internal sinusoidal-shaped reference, which determines the MOSFETs turn-on signal. The on-chip RC filter reduces high-frequency noise on CS. |
| 5     | ZCS  | The Zero-Crossing Current-Sensing Input of the Inductor. A negative-transition edge triggers the MOSFETs turn-on signal.                                                                                                                                                                                               |
| 6     | GND  | Ground.                                                                                                                                                                                                                                                                                                                |
| 7     | GATE | Gate Driver Output. The high output current of the gate driver is able to drive the low-cost power MOSFET. The high-level voltage of GATE is clamped to 12V in case GATE is supplied with a high VCC.                                                                                                                  |
| 8     | VCC  | Supply Voltage of Both the Signal Path of the IC and the Gate Driver. A bypass capacitor from VCC to ground is needed to reduce noise.                                                                                                                                                                                 |

## FUNCTIONAL BLOCK DIAGRAM

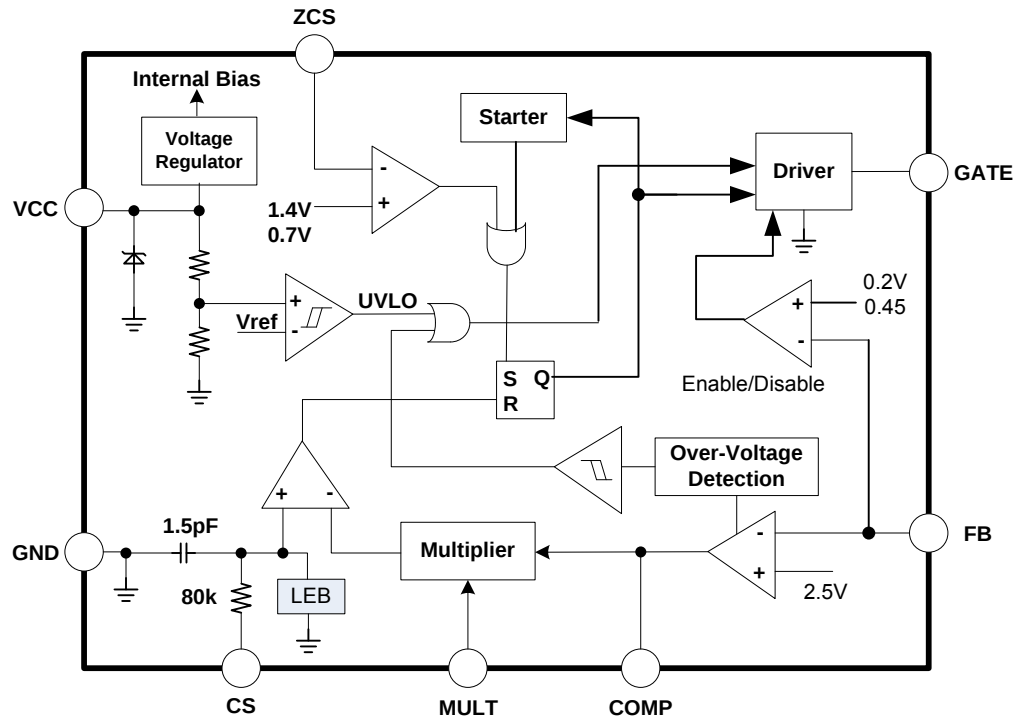


FIGURE 1. Functional Block Diagram



below 0.7V. Once the voltage falls below 0.7V, the MP44014-A turns on the MOSFET. The 7.8V high clamp protects ZCS. The internal 175 $\mu$ s timer generates a signal to turn on the MOSFET if the driver signal has been low for more than 175 $\mu$ s. This also allows the MOSFET to turn on during the start-up period since no signal is generated from ZCS.

### Zero-Crossing Compensation

The MP44014-A offers a 30mV voltage offset for the multiplier output near the zero-crossing of the line voltage. This forces the circuit to process more energy at the bottom of the line voltage. With this function, the THD of the current is reduced.

To prevent redundant energy, this offset is reduced as the instantaneous line voltage increases. Therefore, the offset is negligible near the top of the line voltage.

### Power Factor Correction

The MP44014-A senses the inductor current through CS and compares it to the sinusoidal-shaped signal (which is generated from the output of the multiplier). When the external power MOSFET turns on, the inductor current rises linearly. When the peak current hits

the sinusoidal-shaped signal, the external power MOSFET begins to turn off, and the diode turns on. Also, the inductor current begins to fall. When the inductor current reaches zero, the power MOSFET begins to turn on again, which causes the inductor current to start rising again. The power circuit works in boundary conduction mode, and the envelope of the inductor current is sinusoidal-shaped. The average input current is half of the peak current, so the average input current is sinusoidal-shaped as well (see Fig. 3). A high-power factor can be achieved through this control method.

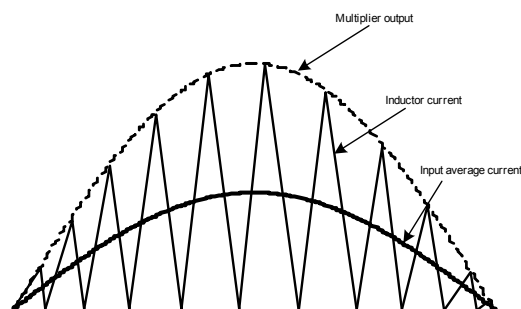


FIGURE 3. Inductor Current Waveform

The control flow chart of the MP44014-A is shown in Fig. 4.

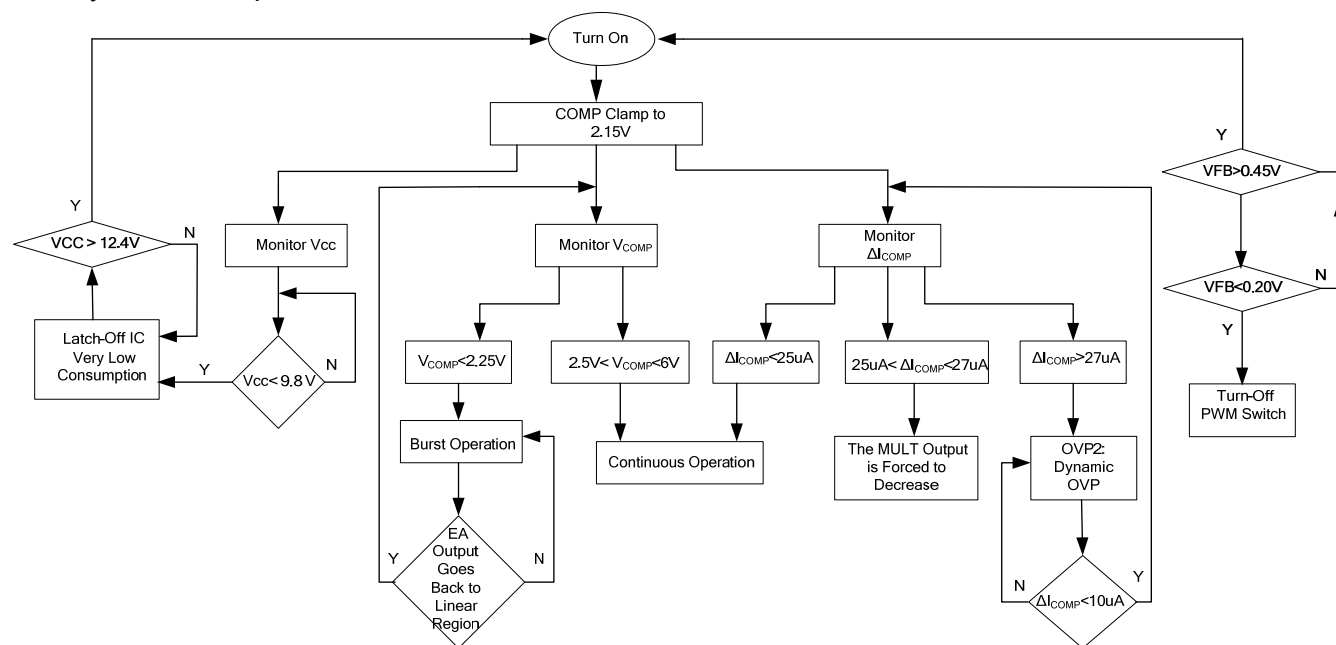
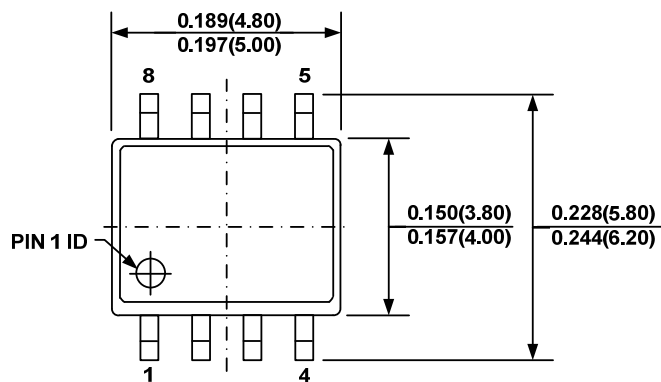


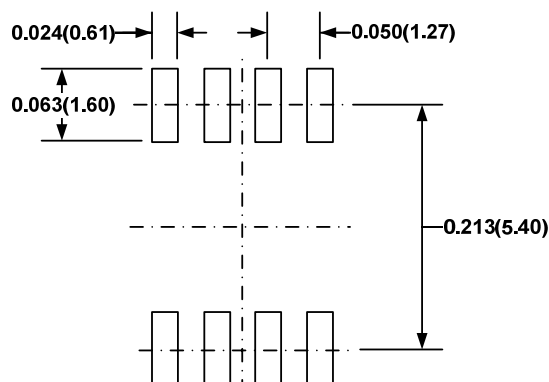
FIGURE 4. Control Flow Chart

# PACKAGE INFORMATION

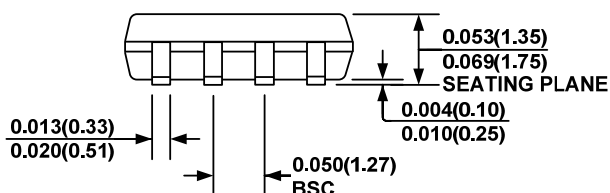
## SOIC-8



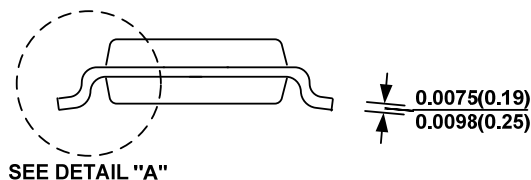
**TOP VIEW**



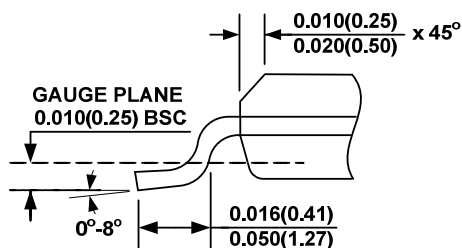
**RECOMMENDED LAND PATTERN**



**FRONT VIEW**



**SIDE VIEW**



**DETAIL "A"**

### NOTE:

- 1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
- 2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
- 4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
- 5) DRAWING CONFORMS TO JEDEC MS-012, VARIATION AA.
- 6) DRAWING IS NOT TO SCALE.

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