



Micro Commercial Components



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Features

- Surface Mount SOT-23 Package
- Capable of 350mWatts of Power Dissipation
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Marking: 4D

Electrical Characteristics @ 25°C Unless Otherwise Specified

Symbol	Parameter	Min	Max	Units
OFF CHARACTERISTICS				
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage* ($I_C = -1.0\text{mA}$, $I_B = 0$)	-400		Vdc
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage ($I_C = -100\mu\text{A}$, $I_E = 0$)	-400		Vdc
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage ($I_E = -100\mu\text{A}$, $I_C = 0$)	-5		Vdc
I_C	Collector Current-Continuous	-100		mA
I_{CBO}	Collector Cutoff Current ($V_{CB} = -400\text{Vdc}$, $I_E = 0$)		-100	nA
I_{EBO}	Emitter Cutoff Current ($V_{EB} = -4\text{Vdc}$, $I_C = 0$)		-100	nA
T_J	Junction Temperature	150		°C
T_{stg}	Storage Temperature	-55 to +150		°C

ON CHARACTERISTICS

h_{FE}	DC Current Gain* ($I_C = -1.0\text{mA}$, $V_{CE} = -10\text{Vdc}$) ($I_C = -10\text{mA}$, $V_{CE} = -10\text{Vdc}$) ($I_C = -50\text{mA}$, $V_{CE} = -10\text{Vdc}$) ($I_C = -100\text{mA}$, $V_{CE} = -10\text{Vdc}$)	70 80 40 40	300	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage ($I_C = -10\text{mA}$, $I_B = -1.0\text{mA}$) ($I_C = -50\text{mA}$, $I_B = -5\text{mA}$)		-0.2 -0.3	Vdc
$V_{BE(sat)}$	Base-Emitter Saturation Voltage ($I_C = -50\text{mA}$, $I_B = -1.0\text{mA}$)		-0.75	Vdc

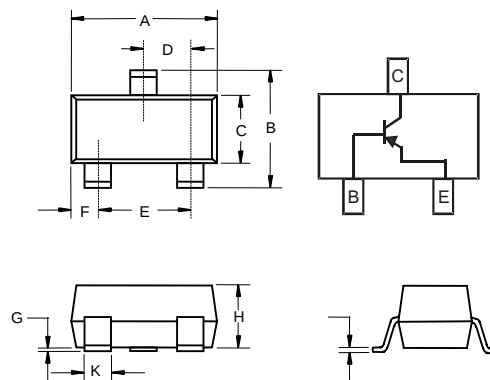
SMALL-SIGNAL CHARACTERISTICS

f_T	Current Gain-Bandwidth Product ($I_C = -10\text{mA}$, $V_{CE} = -20\text{Vdc}$)	50		MHz
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MMBTA94

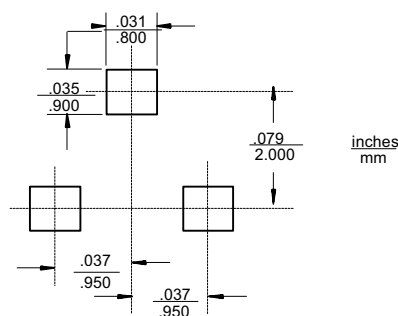
PNP Silicon High Voltage Transistor

SOT-23

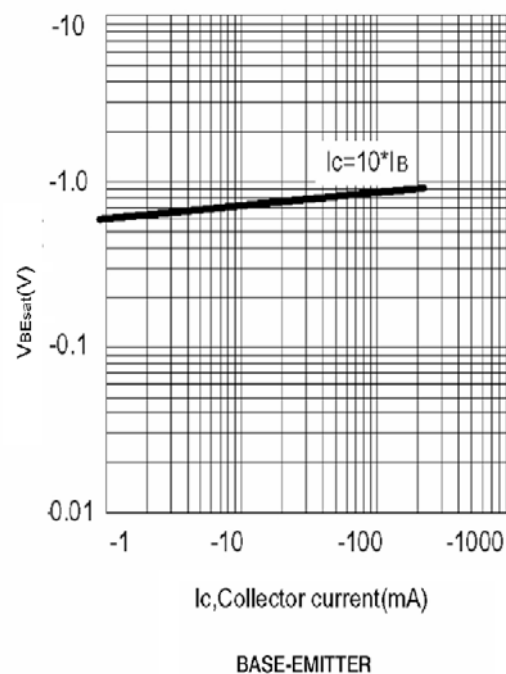


DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.110	.120	2.80	3.04	
B	.083	.098	2.10	2.64	
C	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
E	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
H	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

Suggested Solder Pad Layout



Graph of DC current gain (h_{FE}) versus DC collector current (I_c) for the 2N3866 JFET. The y-axis is logarithmic, ranging from 1 to 1000. The x-axis is logarithmic, ranging from -1 to -1000 mA. A single curve is shown for $V_{CE} = -10V$, peaking at approximately 120 at $I_c = -10$ mA and decreasing to about 50 at $I_c = -100$ mA.



Ordering Information :

Device	Packing
Part Number-TP	Tape&Reel; 3Kpcs/Reel

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