RoHS Compliant
HALOGEN-FREE

MagnaChip

MDFS10N60D

N-Channel MOSFET 600V, 10A, 0.75Ω

General Description

These N-channel MOSFET are produced using advanced MagnaChip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

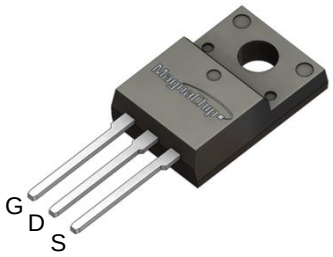
These devices are suitable device for SMPS, high Speed switching and general purpose applications.

Features

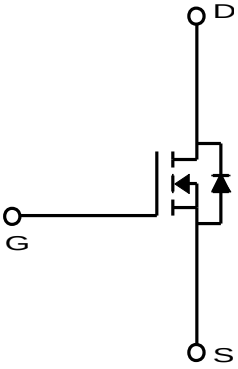
- V_{DS} = 600V
- I_D = 10A @ V_{GS} = 10V
- $R_{DS(ON)}$ ≤ 0.75Ω @ V_{GS} = 10V

Applications

- Power Supply
- PFC
- High Current, High Speed Switching



TO-220FT



Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current	T_C=25°C	I_D	10.0*	A
	T_C=100°C		6.33*	A
Pulsed Drain Current(1)		I_{DM}	40*	A
Power Dissipation	T_C=25°C	P_D	48.6	W
	Derate above 25 °C		0.39	W/°C
Repetitive Avalanche Energy(1)		E_{AR}	4.86	mJ
Peak Diode Recovery dv/dt(3)		dv/dt	4.5	V/ns
Single Pulse Avalanche Energy(4)		E_{AS}	580	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

* Id limited by maximum junction temperature

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient(1)	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance, Junction-to-Case(1)	$R_{\theta JC}$	2.57	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDFS10N60DTH	-55~150°C	TO-220FT	Tube	Halogen Free

Electrical Characteristics (Ta =25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D = 250μA, V_{GS} = 0V	600	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2.0	-	4.0	
Drain Cut-Off Current	I_{DSS}	V_{DS} = 600V, V_{GS} = 0V	-	-	1	μA
Gate Leakage Current	I_{GSS}	V_{GS} = ±30V, V_{DS} = 0V	-	-	100	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	V_{GS} = 10V, I_D = 5.0A		0.62	0.75	Ω
Forward Transconductance	g_{fs}	V_{DS} = 30V, I_D = 5.0A	-	13	-	S
Dynamic Characteristics						
Total Gate Charge	Q_g	V_{DS} = 480V, I_D = 10A, V_{GS} = 10V(2)	-	42.9	-	nC
Gate-Source Charge	Q_{gs}		-	7.7	-	
Gate-Drain Charge	Q_{gd}		-	18.7	-	
Input Capacitance	C_{iss}	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	-	1595	-	pF
Reverse Transfer Capacitance	C_{rss}		-	20	-	
Output Capacitance	C_{oss}		-	168	-	
Turn-On Delay Time	$t_{d(on)}$	V_{GS} = 10V, V_{DS} = 300V, I_D = 10A(2), R_G = 25Ω	-	22	-	ns
Rise Time	t_r		-	26	-	
Turn-Off Delay Time	$t_{d(off)}$		-	160	-	
Fall Time	t_f		-	43.5	-	
Drain-Source Body Diode Characteristics						
Maximum Continuous Drain to Source Diode Forward Current	I_S		-	-	10	A
Source-Drain Diode Forward Voltage	V_{SD}	I_S = 10A, V_{GS} = 0V	-	-	1.4	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 10A, di/dt = 100A/μs(3)	-	319	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	2.65	-	μC

Note :

1. Pulse width is based on $R_{\theta JC}$ & $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width ≤300us, duty cycle≤2%, pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
3. I_{SD} ≤10.0A, di/dt≤200A/us, V_{DD}≤BVDSS, Starting T_J=25°C
4. L=10.7mH, I_{AS}=10.0A, V_{DD}=50V, R_g =25Ω, Starting T_J=25°C,

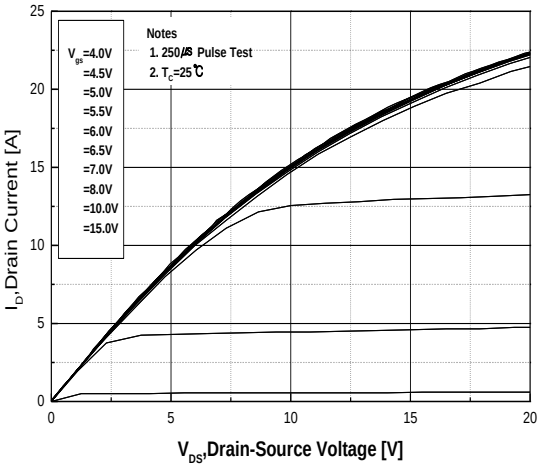


Fig.1 On-Region Characteristics

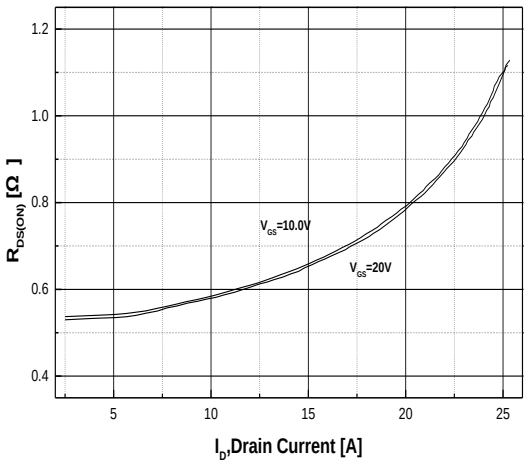


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

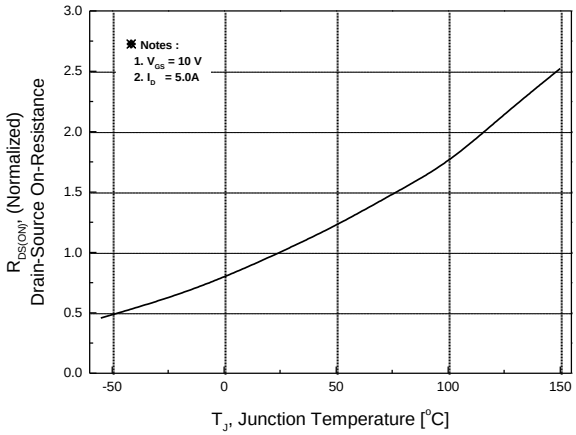


Fig.3 On-Resistance Variation with Temperature

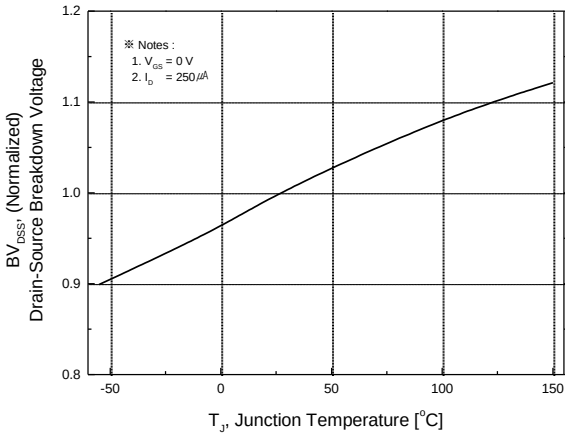


Fig.4 Breakdown Voltage Variation vs. Temperature

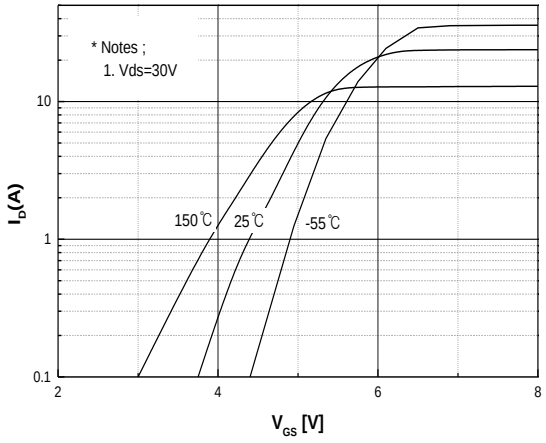


Fig.5 Transfer Characteristics

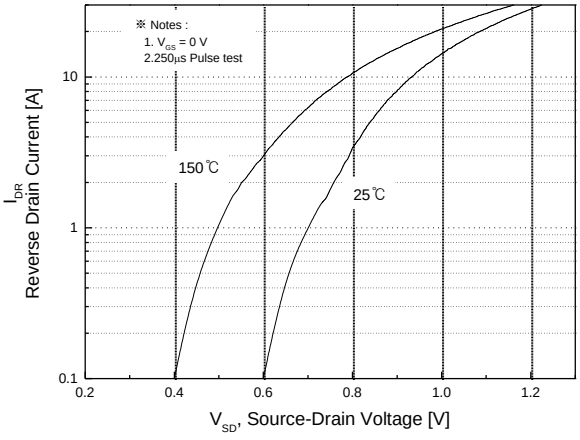


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

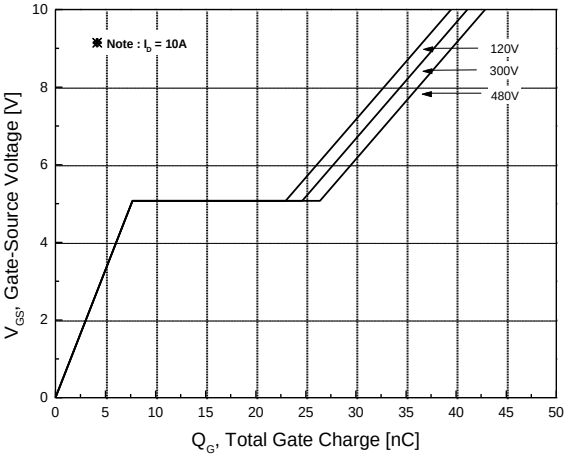


Fig.7 Gate Charge Characteristics

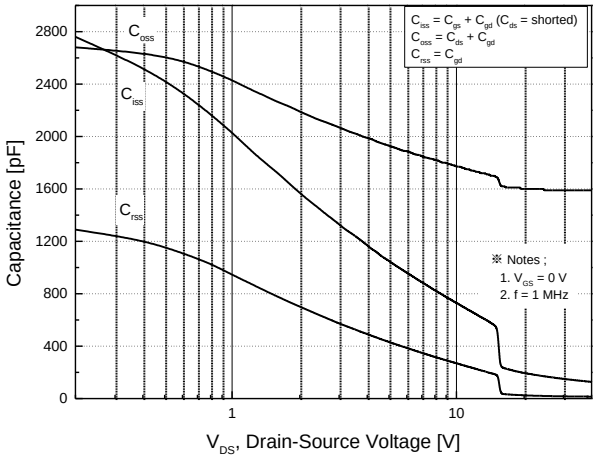


Fig.8 Capacitance Characteristics

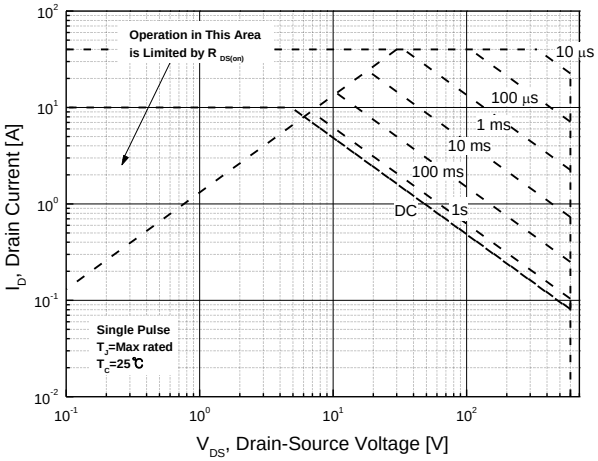


Fig.9 Maximum Safe Operating Area

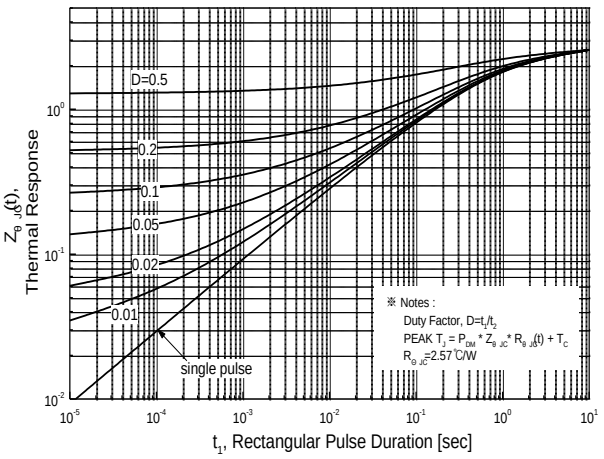


Fig.10 Transient Thermal Response Curve

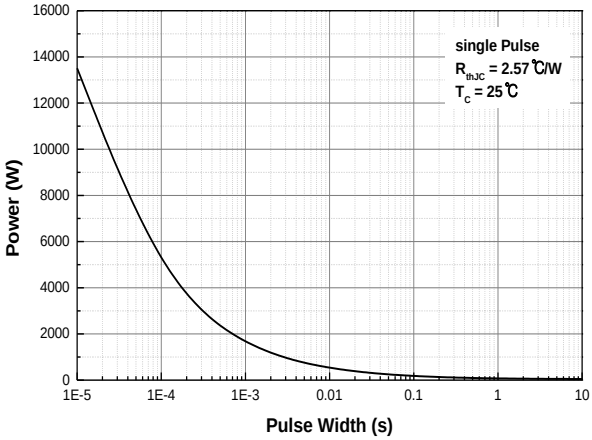


Fig.11 Single Pulse Maximum Power Dissipation

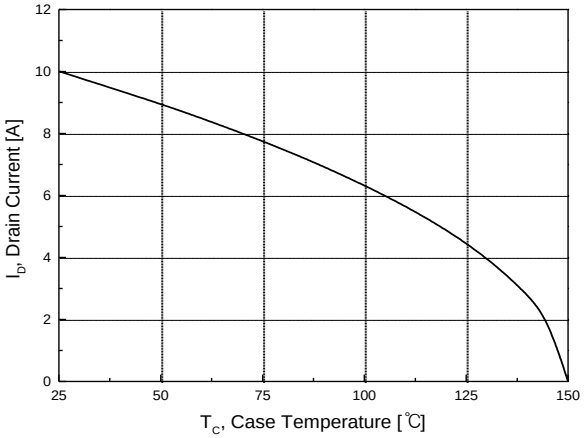
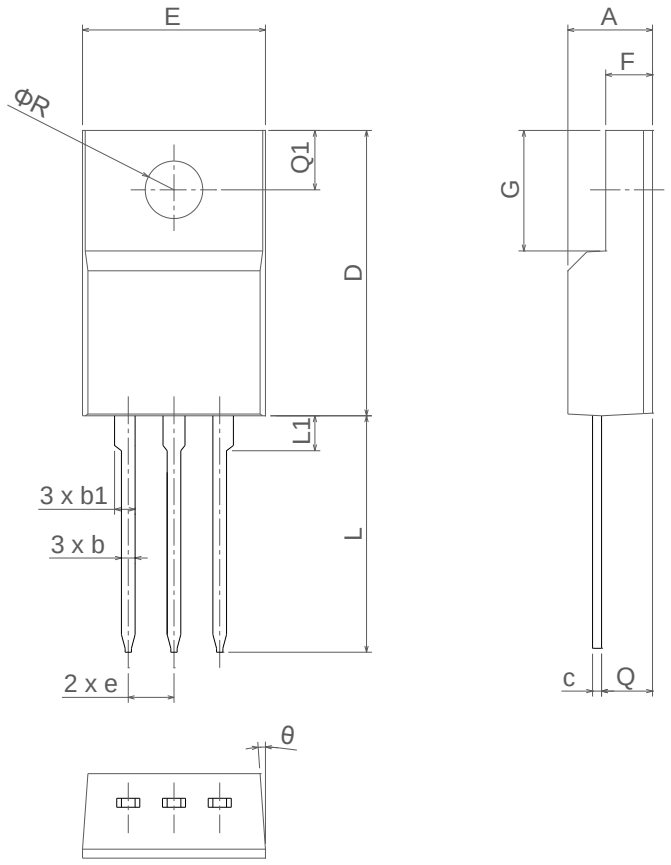


Fig.15 Maximum Drain Current vs. Case Temperature

Physical Dimensions

3 Leads, TO-220FT



Note: PKG Body Sizes exclude Mold Flash & Gate Burrs

[Unit:mm]

Symbol	Dimension [mm]		
	Min	Nom	Max
A	4.30	-	4.70
b	0.54	-	0.84
b1	0.99	-	1.29
c	0.49	-	0.79
D	14.70	-	15.30
E	9.70	-	10.30
e	2.29	-	2.79
F	2.50	-	2.90
G	6.70	-	7.10
L	12.50	-	13.50
L1	1.80	-	-
Q	2.50	-	2.70
Q1	2.70	-	3.30
ΦR	3.05	-	3.40
θ	(5°)		

DISCLAIMER:

The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.

MagnaChip reserves the right to change the specifications and circuitry without notice at any time. MagnaChip does not consider responsibility for use of any circuitry other than circuitry entirely included in a MagnaChip product. MagnaChip is a registered trademark of MagnaChip Semiconductor Ltd.