

MCP48CXBXX Silicon Errata and Data Sheet Clarification

The MCP48CXBXX family devices that you have received conform functionally to the current Device Data Sheet (DS20006160A), except for the anomalies described in this document.

The issues described here will be addressed in future revisions of the MCP48CXBXX silicon.

The silicon issues discussed in this document are for devices listed in [Table 1](#).

TABLE 1: AFFECTED PART NUMBERS

Part Number
MCP48CVB01
MCP48CVB11
MCP48CVB21
MCP48CVB02
MCP48CVB12
MCP48CVB22
MCP48CMB01
MCP48CMB11
MCP48CMB21
MCP48CMB02
MCP48CMB12
MCP48CMB22

Silicon Errata Issues

Note: This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current.

1. Module: SPI Communication

Powering up the device with $\overline{CS}$ tied to V_{SS} prevents the SPI communication module from starting.

Work around

Before starting any communication with MCP48CXBXX, the (SPI) Master device must ensure the serial interface is reset, during or after the start-up procedure, by setting the $\overline{CS}$ pin level to V_{IH} .

Affected Silicon Revisions

A2							
X							

2. Module: SPI Communication

Continuous Read or Write commands addressing registers from 0x10 to 0x1F do not work.

Work around

None.

Continuous read or write operations must not be used for nonvolatile register locations. Using these commands on an address range from 0x0C to 0x1F will return CMDERR (in case of write) or may return bad data (in case of read).

Affected Silicon Revisions

A2							
X							

Data Sheet Clarifications

The following typographic corrections and clarifications are to be noted for the latest version of the device data sheet (DS20006160A):

None.

MCP48CXBXX

APPENDIX A: DOCUMENT REVISION HISTORY

Rev. A Document (03/2019)

- Initial Release of this Document.

APPENDIX B: SILICON REVISION HISTORY

The following table indicates how to determine the revision of the MCP48CXBXX device. The revision information can be determined by the Year and Week Code of the manufacture printed on the device.

**TABLE B -1: SILICON REVISION/DEVICE
MARKING**

Silicon Revision	YYWWNNN		Comments
	Start Date	End Date	
Rev. A2	1909NNN	—	
Legend: "N" is any alphanumeric character.			

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ISBN: 978-1-5224-4336-0

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