



MCH6102/MCH6202

Bipolar Transistor

(-30V, (-)1.5A, Low $V_{CE(sat)}$, (PNP)NPN Single MCPH6

ON Semiconductor®

<http://onsemi.com>

Applicaitons

- Relay drivers, lamp drivers, motor drivers, flash

Features

- Adoption of MBIT processes
- Low collector-to-emitter saturation voltage
- Ultrasmall package facilitates miniaturization in end products (mounting height : 0.85mm)
- High allowable power dissipation
- Large current capacity
- High-speed switching

Specifications () : MCH6102

Absolute Maximum Ratings at $T_a=25^{\circ}\text{C}$

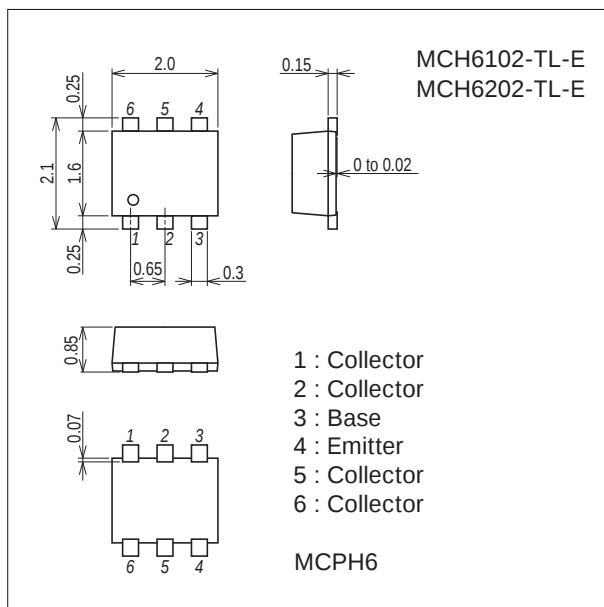
Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		(-30)40	V
Collector-to-Emitter Voltage	V_{CEO}		(-30)	V
Emitter-to-Base Voltage	V_{EBO}		(-5)	V

Continued on next page.

Package Dimensions

unit : mm (typ)

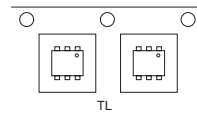
7022A-007



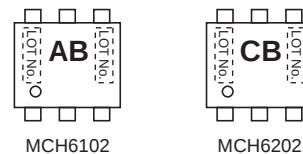
Product & Package Information

- Package : MCPH6
- JEITA, JEDEC : SC-88, SC-70-6, SOT-363
- Minimum Packing Quantity : 3,000 pcs./reel

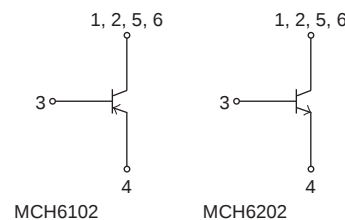
Packing Type: TL



Marking



Electrical Connection



MCH6102/MCH6202

Continued from preceding page.

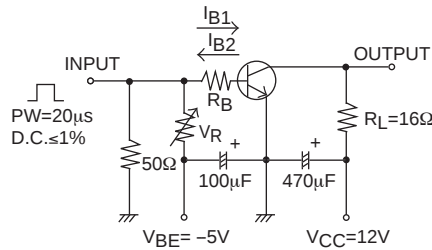
Parameter	Symbol	Conditions	Ratings	Unit
Collector Current	I_C		(-)1.5	A
Collector Current (Pulse)	I_{CP}		(-)3	A
Base Current	I_B		(-)300	mA
Collector Dissipation	P_C	When mounted on ceramic substrate (600mm ² ×0.8mm)	1.0	W
Junction Temperature	T_J		150	°C
Storage Temperature	T_{stg}		-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Electrical Characteristics at $T_a=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I_{CBO}	$V_{CB}=(-)30\text{V}$, $I_E=0\text{A}$			(-)0.1	μA
Emitter Cutoff Current	I_{EBO}	$V_{EB}=(-)4\text{V}$, $I_C=0\text{A}$			(-)0.1	μA
DC Current Gain	h_{FE}	$V_{CE}=(-)2\text{V}$, $I_C=(-)100\text{mA}$	200		560	
Gain-Bandwidth Product	f_T	$V_{CE}=(-)10\text{V}$, $I_C=(-)300\text{mA}$		(450)500		MHz
Output Capacitance	C_{ob}	$V_{CB}=(-)10\text{V}$, $f=1\text{MHz}$		(9)8		pF
Collector-to-Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C=(-)750\text{mA}$, $I_B=(-)15\text{mA}$		(-250)150	(-375)225	mV
Base-to-Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C=(-)750\text{mA}$, $I_B=(-)15\text{mA}$		(-)0.85	(-)1.2	V
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=(-)10\mu\text{A}$, $I_E=0\text{A}$	(-30)40			V
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=(-)1\text{mA}$, $R_{BE}=\infty$	(-)30			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=(-)10\mu\text{A}$, $I_C=0\text{A}$	(-)5			V
Turn-ON Time	t_{on}	See specified Test Circuit.		(37)35		ns
Storage Time	t_{stg}			(115)205		ns
Fall Time	t_f			(26)32		ns

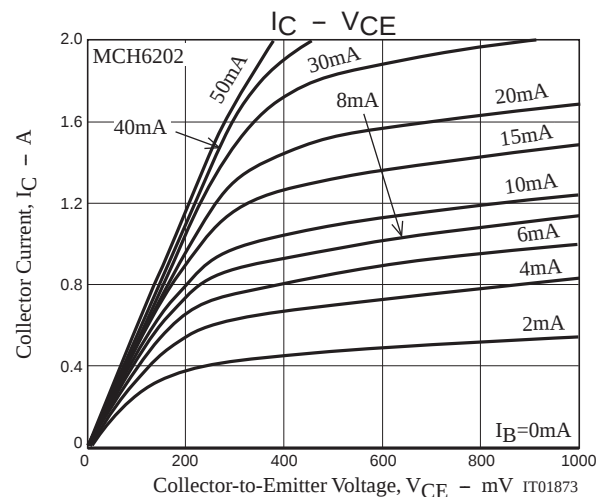
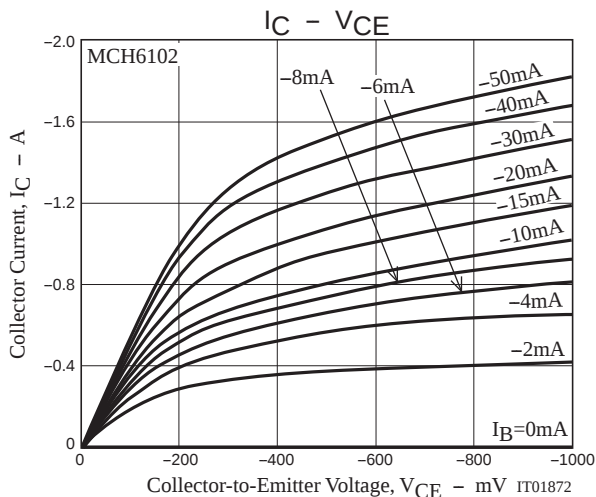
Switching Time Test Circuit

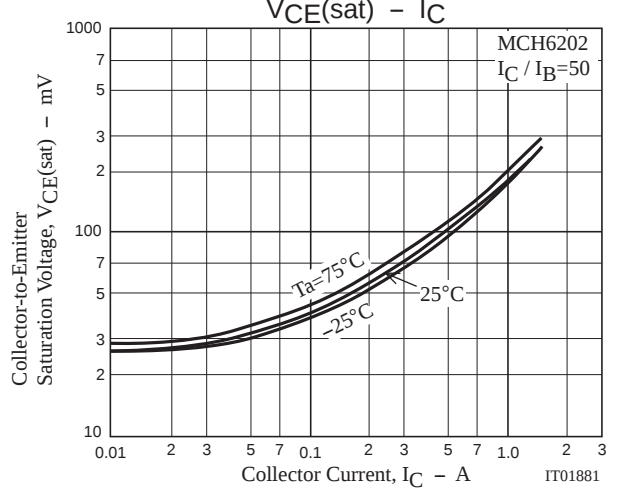
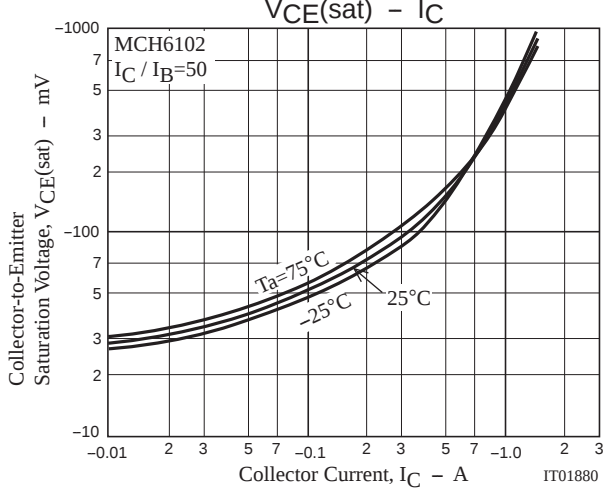
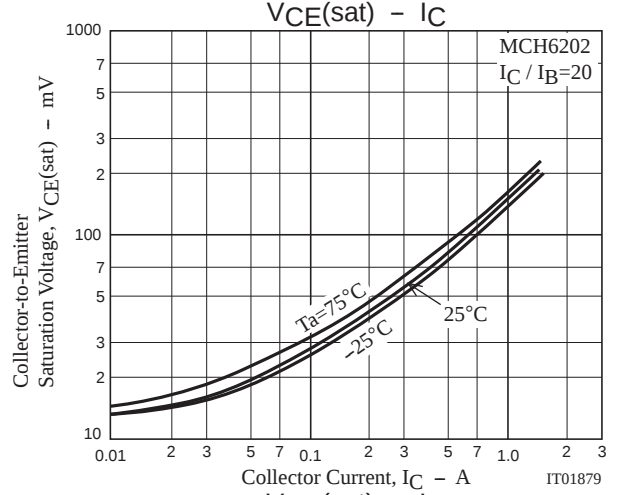
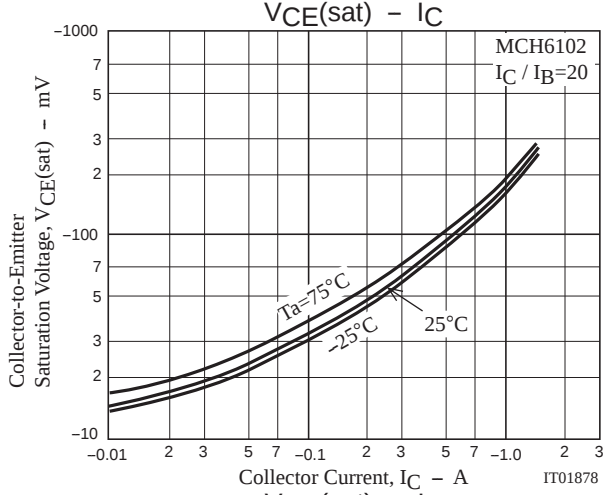
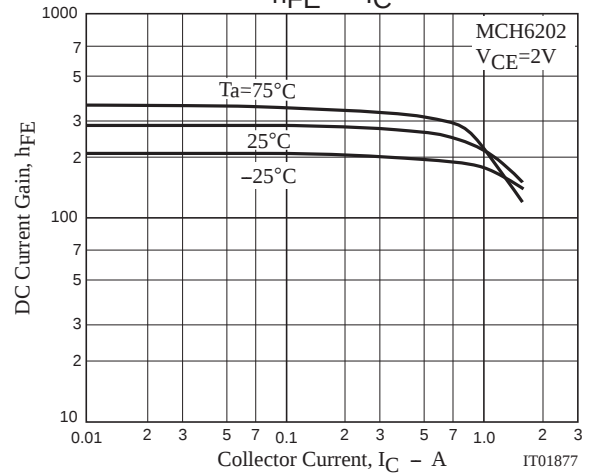
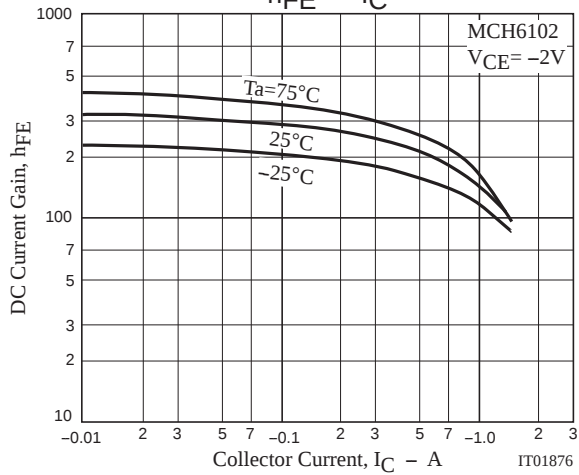
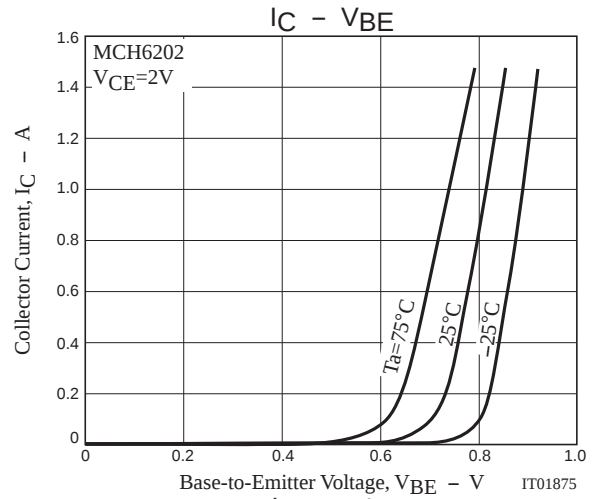
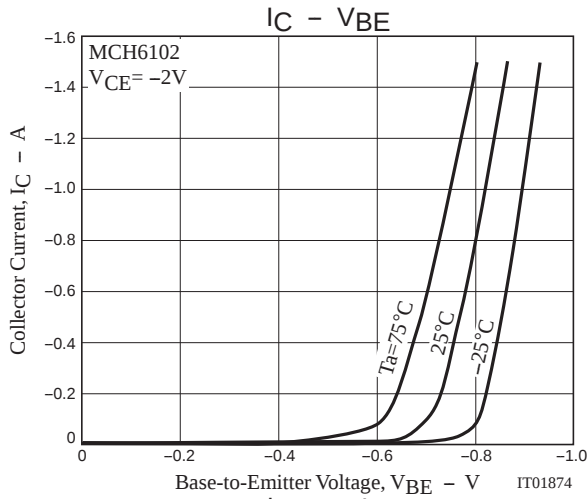


$I_C=20I_{B1}=-20I_{B2}=750\text{mA}$
For PNP, the polarity is reversed.

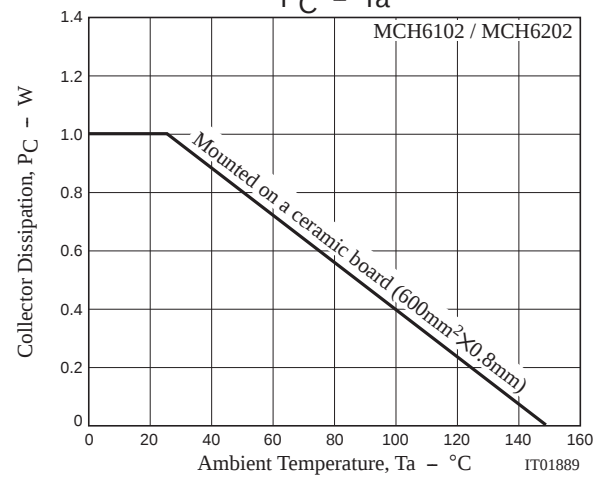
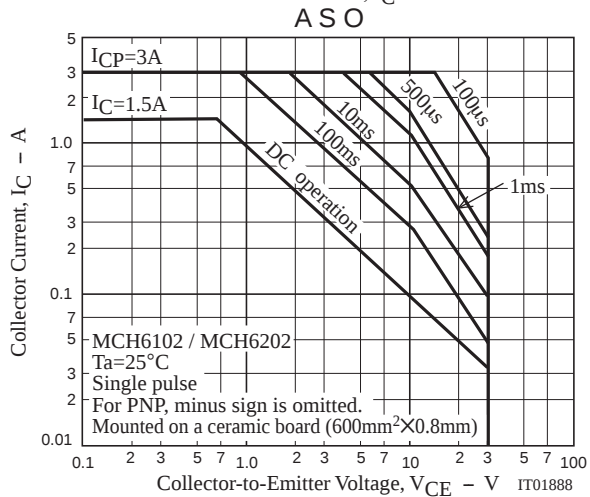
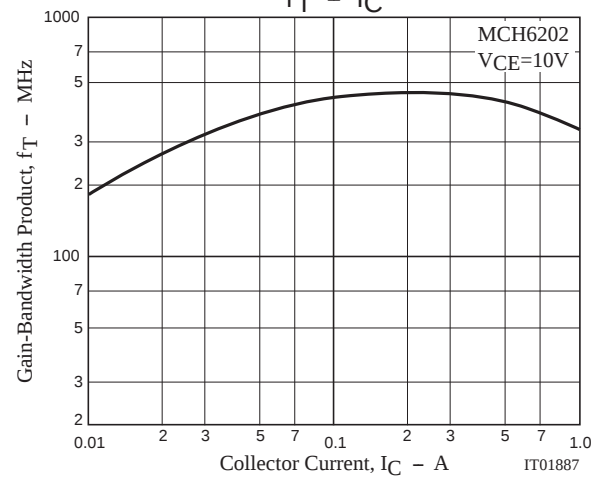
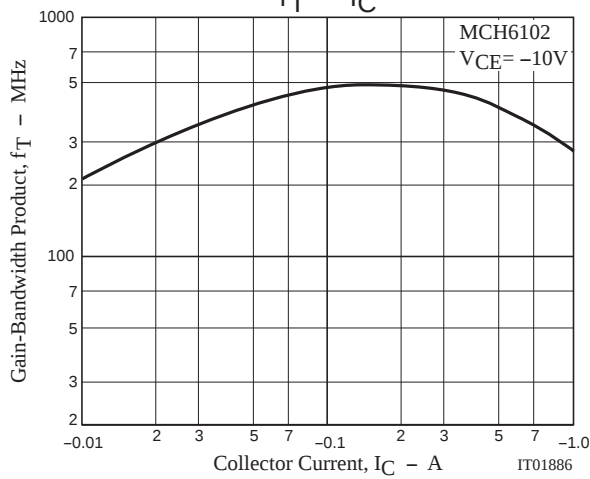
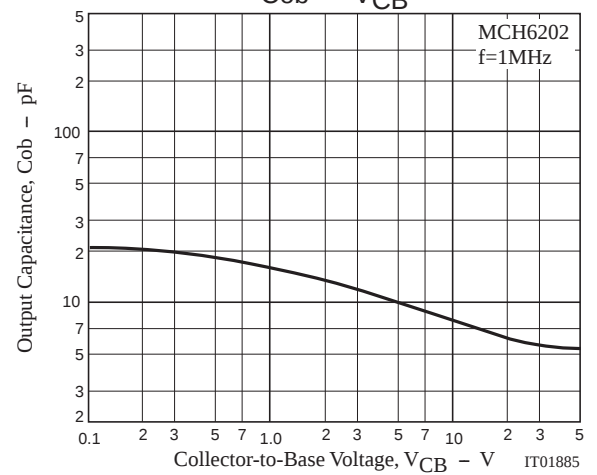
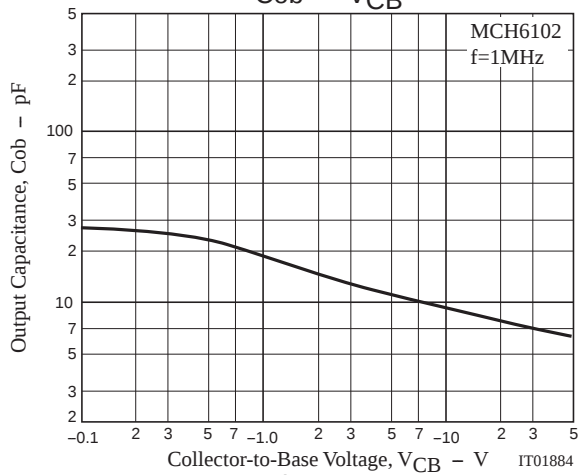
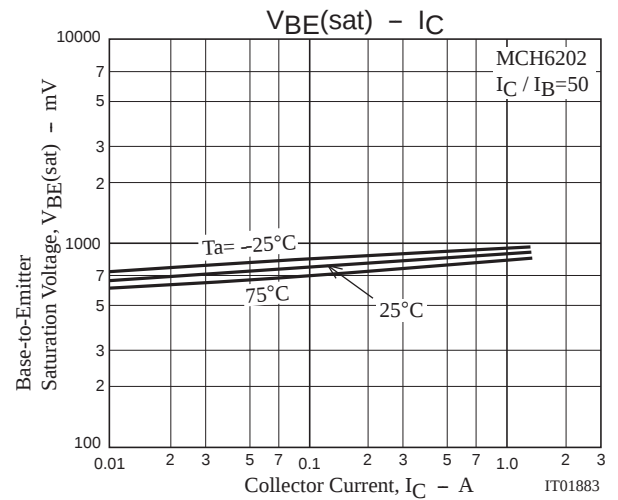
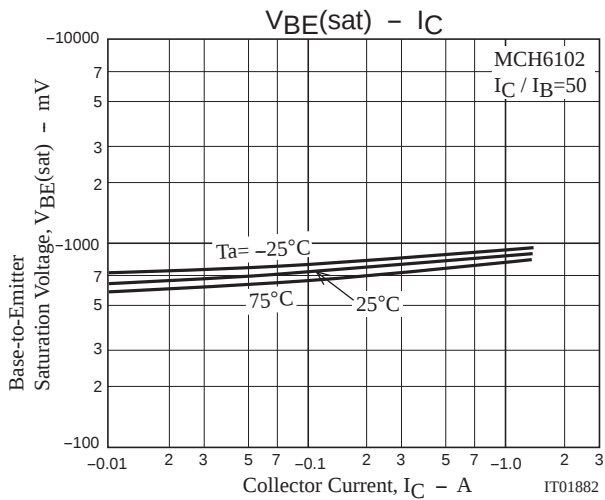
Ordering Information

Device	Package	Shipping	memo
MCH6102-TL-E	MCPH6	3,000pcs./reel	Pb Free
MCH6202-TL-E	MCPH6	3,000pcs./reel	





MCH6102/MCH6202



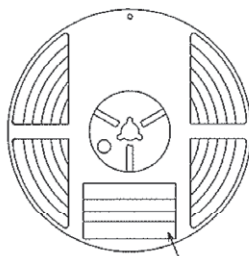
Embossed Taping Specification

MCH6102-TL-E, MCH6202-TL-E

1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
MCPH6	MCP4	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

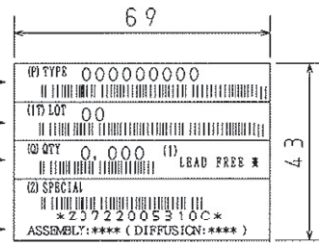
Packing method



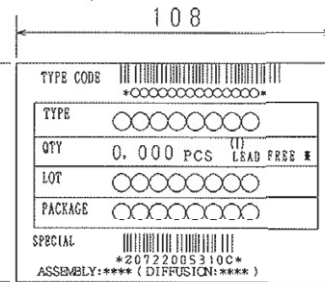
Reel label

Type No.
LOT No.
Quantity
Origin

Reel label, Inner box label
(unit:mm)



Outer box label
It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.



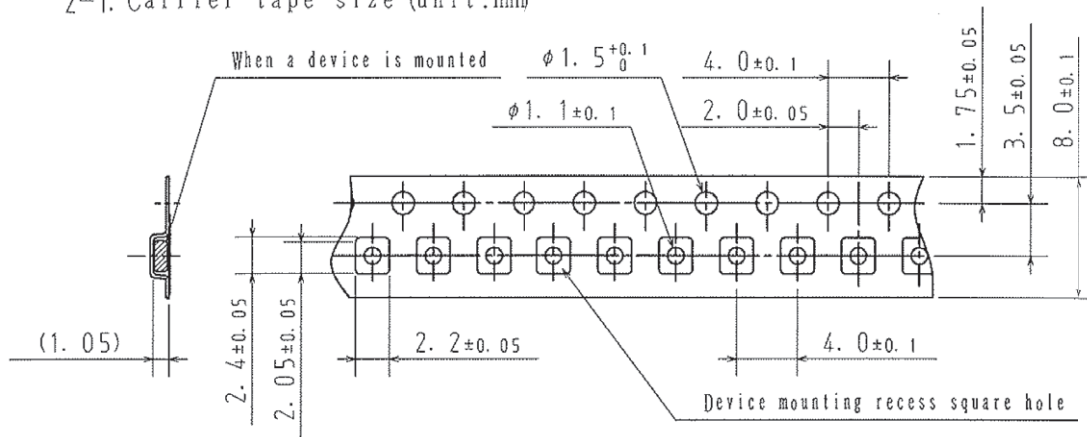
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

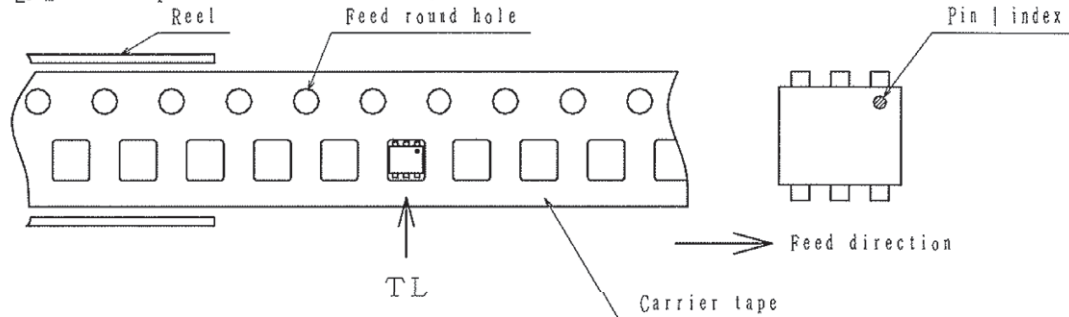
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



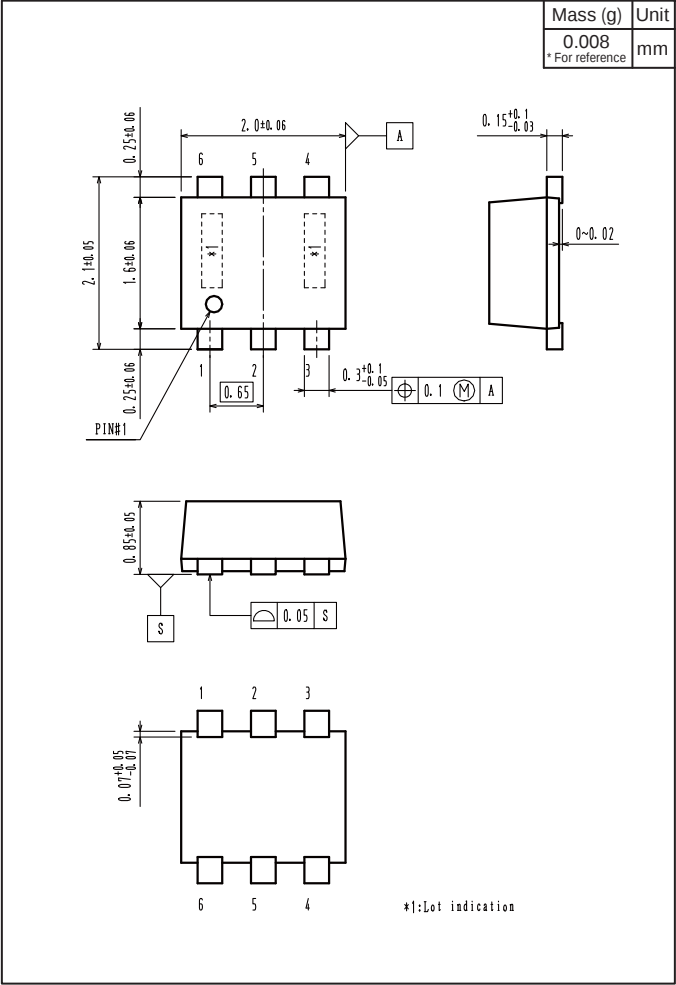
2-2. Device placement direction



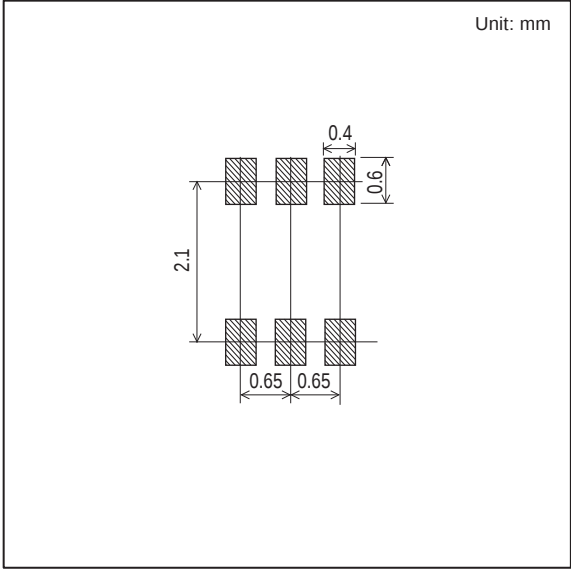
Those with pin 1 index on the feed hole side.....TL

Outline Drawing

MCH6102-TL-E, MCH6202-TL-E



Land Pattern Example



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.