19-3887; Rev 0; 2/06

EVALUATION KIT AVAILABLE

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

General Description

The MAX9741 stereo Class D audio power amplifier provides Class AB amplifier performance with Class D efficiency, conserving board space and eliminating the need for a bulky heatsink. Using a high-efficiency Class D architecture, it delivers 12W continuous output power into 8Ω loads. Proprietary modulation and switching schemes render the traditional Class D EMI suppression output filter unnecessary.

The MAX9741 offers two modulation schemes: a fixed-frequency mode (FFM), and a spread-spectrum mode (SSM) that reduces EMI-radiated emissions. The device utilizes a fully differential architecture, a full bridged output, and offers comprehensive click-and-pop suppression.

The MAX9741 features high 80dB PSRR, low 0.1% THD+N, and SNR in excess of 100dB. Short-circuit and thermal-overload protection prevent the device from being damaged during a fault condition. The MAX9741 is available in a 56-pin TQFN (8mm x 8mm x 0.8mm) package. The MAX9741 is specified over the extended -40°C to +85°C temperature range.

Features

- **Low-EMI Class D Amplifier**
- **Spread-Spectrum Mode Reduces EMI**
- **Passes FCC EMI Limits with Ferrite Bead Filters with 0.5m Cables**
- **12W+12W Continuous Output Power into 8Ω**
- **Low 0.1% THD+N**
- **High PSRR (80dB at 1kHz)**
- **10V to 25V Single-Supply Operation**
- **Differential Inputs Minimize Common-Mode Noise**
- **Pin-Selectable Gain Reduces Component Count**
- **Industry-Leading Click-and-Pop Suppression**
- **Short-Circuit and Thermal-Overload Protection**
- **Available in Thermally Efficient, Space-Saving 56-Pin TQFN (8mm x 8mm x 0.8mm) Package**

Applications

LCD/PDP TVs

CRT TVs

PC Speakers

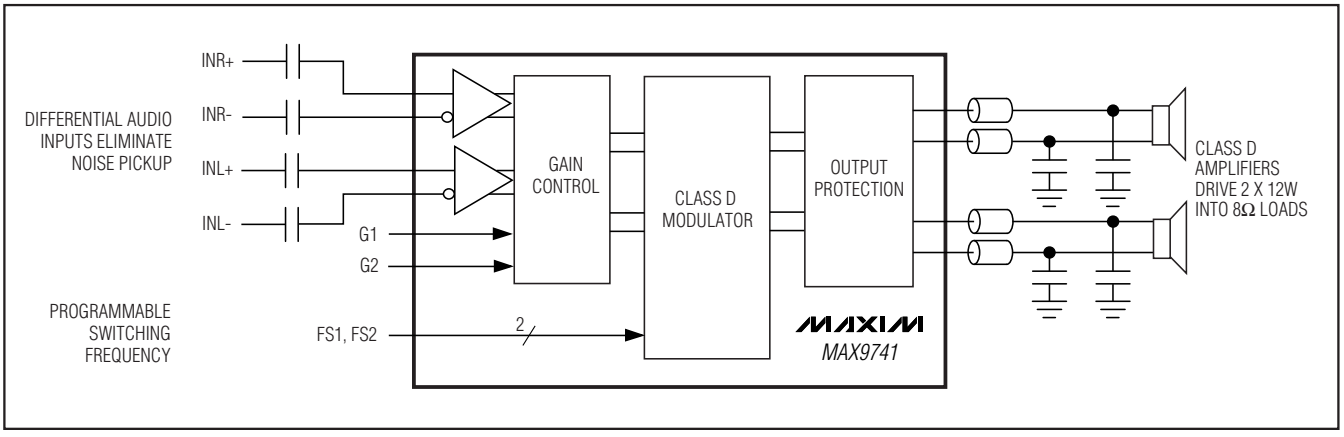
Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	PKG CODE
MAX9741ETN+	-40°C to +85°C	56 TQFN-EP*	T5688-3

+Denotes lead-free package.

**EP = Exposed paddle.*

Simplified Block Diagram



Pin Configuration appears at end of data sheet.

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

ABSOLUTE MAXIMUM RATINGS

(All voltages referenced to GND.)
V_{DD} to PGND, AGND 30V
OUTR_, OUTL_, C1N.....-0.3V to (V_{DD} + 0.3V)
C1P.....(V_{DD} - 0.3V) to (CHOLD + 0.3V)
CHOLD.....(V_{DD} - 0.3V) to +40V
SHDN, FS_, G_ .....-6.3V to 8V
All Other Pins to GND.....-0.3V to +12V
Duration of OUTR_/OUTL_
Short Circuit to GND, V_{DD}.....Continuous
Continuous Input Current (V_{DD}, PGND) 2A
Continuous Input Current (all other pins).....±20mA
Thermal Limits (Note 1)
Continuous Power Dissipation (T_A = +70°C)
Single-Layer PC Board
56-Pin TQFN (derate 28.6mW/°C above +70°C) 2.29W
θ_{JA} ..... 35°C/W
θ_{JC}..... 0.6°C/W
Continuous Power Dissipation (T_A = +70°C)
Multiple-Layer PC Board
56-Pin TQFN (derate 47.6mW/°C above +70°C) 3.81W
θ_{JA}..... 21°C/W
θ_{JC}..... 0.6°C/W
Junction Temperature.....+150°C
Operating Temperature Range .....-40°C to +85°C
Storage Temperature Range.....-65°C to +150°C
Lead Temperature (soldering, 10s) .....+300°C

Note 1: Thermal performance of this device is highly dependant on PC board layout. See the *Applications Information* for more detail.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{DD} = 18V, GND = PGND = 0V, $\overline{SHDN} \geq V_{IH}$, A_V = 16dB, C_{SS} = C_{IN} = 0.47µF, C_{REG} = 0.01µF, C1 = 100nF, C2 = 1µF, FS1 = FS2 = GND (f_S = 670kHz), R_L connected between OUTL+ and OUTL- and OUTR+ and OUTR-, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
GENERAL							
Supply Voltage Range	V_{DD}	Inferred from PSRR test		10		25	V
Quiescent Current	I_{DD}	R_L = Open			26	37	mA
Shutdown Current	I_{SHDN}				0.2	1.5	µA
Turn-On Time	t_{ON}	C_{SS} = 470nF			100		ms
		C_{SS} = 180nF			50		
Amplifier Output Resistance in Shutdown		$\overline{SHDN}$ = GND		150	320		kΩ
Input Impedance	R_{IN}	A_V = 13dB		35	53	80	kΩ
		A_V = 16dB		30	45	65	
		A_V = 19.1dB		23	36	55	
		A_V = 29.6dB		10	14.3	22	
Voltage Gain	A_V	G1 = L, G2 = L		29.4	29.6	29.8	dB
		G1 = L, G2 = H		18.9	19.1	19.3	
		G1 = H, G2 = L		12.8	13	13.2	
		G1 = H, G2 = H		15.9	16	16.3	
Gain Matching		Between channels			0.5		%
Output Offset Voltage	V_{OS}				±5	±30	mV
Common-Mode Rejection Ratio	CMRR	f_{IN} = 1kHz, input referred			60		dB
Power-Supply Rejection Ratio (Note 3)	PSRR	V_{DD} = 10V to 25V		48	83		dB
		200mV_{P-P} ripple	f_{RIPPLE} = 1kHz		80		
			f_{RIPPLE} = 20kHz		60		

ELECTRICAL CHARACTERISTICS (continued)

(V_{DD} = 18V, GND = PGND = 0V, $\overline{SHDN} \geq V_{IH}$, A_V = 16dB, C_{SS} = C_{IN} = 0.47µF, C_{REG} = 0.01µF, C1 = 100nF, C2 = 1µF, FS1 = FS2 = GND (f_S = 670kHz), R_L connected between OUTL+ and OUTL- and OUTR+ and OUTR-, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS			MIN	TYP	MAX	UNITS
Continuous Output Power (Notes 4, 5)	P_{CONT}	V_{DD} = 18V, THD+N = 10%, f = 1kHz	R_L = 8Ω			12		W
			R_L = 4Ω			6.5		
		V_{DD} = 24V, THD+N = 10%, f = 1kHz	R_L = 8Ω			11		
			R_L = 4Ω			5		
		V_{DD} = 12V, THD+N = 10%, f = 1kHz	R_L = 8Ω			8		
			R_L = 4Ω			8.5		
Total Harmonic Distortion Plus Noise	THD+N	f_{IN} = 1kHz, either FFM or SSM, R_L = 8Ω, P_{OUT} = 4W				0.1		%
Signal-to-Noise Ratio	SNR	R_L = 8Ω, P_{OUT} = 4W, f = 1kHz BW = 22Hz to 22kHz	Unweighted	FFM		95.8		dB
				SSM		91.8		
			A-weighted	FFM		99.1		
				SSM		95.7		
Crosstalk		Left to right, right to left, 8Ω load, f_{IN} = 10kHz				65		dB
Oscillator Frequency	f_{OSC}	FS1 = L, FS2 = L			560	670	800	kHz
		FS1 = L, FS2 = H				930		
		FS1 = H, FS2 = L				470		
		FS1 = H, FS2 = H (spread-spectrum mode)				670 ±7%		
Efficiency (Note 4)	η	V_{DD} = 12V, R_L = 8Ω, P_{OUT} = 8W				78		%
		V_{DD} = 18V, R_L = 8Ω, P_{OUT} = 10W				78		
Regulator Output	V_{REG}					6		V
DIGITAL INPUTS ($\overline{SHDN}$, FS_, G_)								
Input Thresholds		V_{IH}			2.5			V
		V_{IL}					0.8	
Input Leakage Current							±1	µA

Note 2: All devices are 100% production tested at +25°C. All temperature limits are guaranteed by design.

Note 3: PSRR is specified with the amplifier inputs connected to GND through C_{IN}.

Note 4: Testing performed with a resistive load in series with an inductor to simulate an actual speaker load. For R_L = 8Ω, L = 68µH. For R_L = 12Ω, L = 100µH. For R_L = 16Ω, L = 120µH.

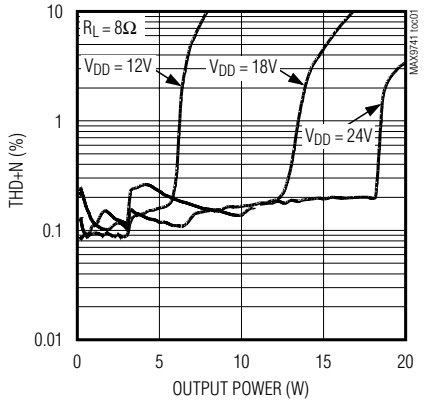
Note 5: Output power measured at T_A = +25°C, with a soak time of 15 minutes.

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

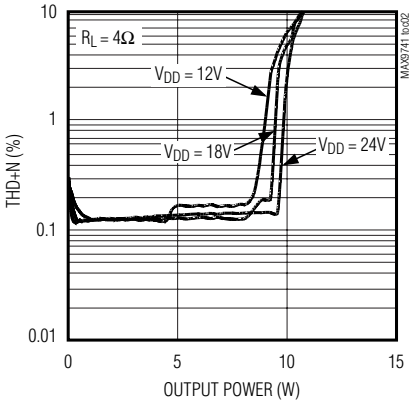
Typical Operating Characteristics

(V_{DD} = 18V, R_L = 8Ω, f_{IN} = 1kHz, 33µH with 4Ω, 68µH with 8Ω, part in SSM mode, 136µH with 16Ω, measurement BW = 22Hz to 22kHz, unless otherwise noted.)

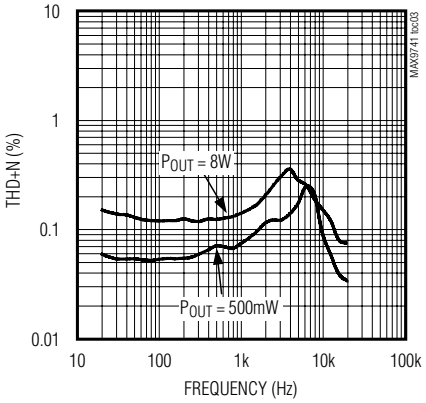
TOTAL HARMONIC DISTORTION PLUS NOISE vs. OUTPUT POWER



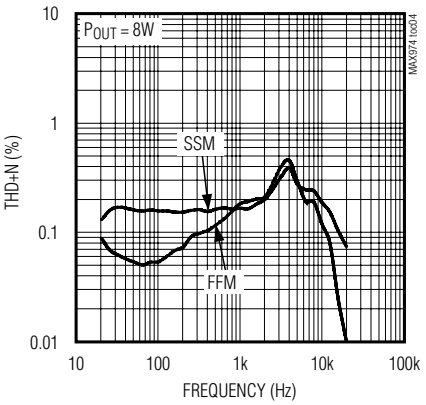
TOTAL HARMONIC DISTORTION PLUS NOISE vs. OUTPUT POWER



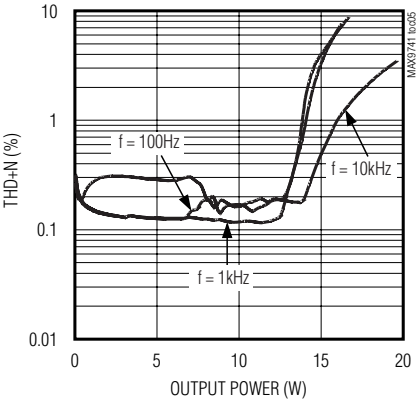
TOTAL HARMONIC DISTORTION PLUS NOISE vs. FREQUENCY



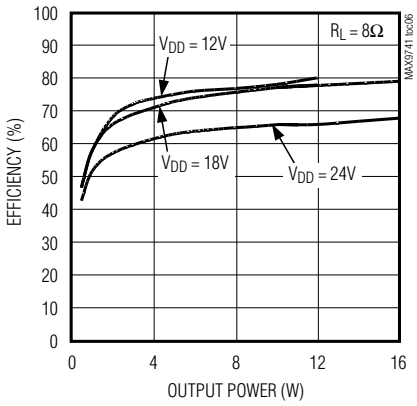
TOTAL HARMONIC DISTORTION PLUS NOISE vs. FREQUENCY



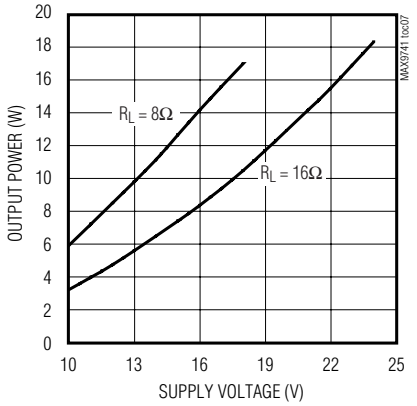
TOTAL HARMONIC DISTORTION PLUS NOISE vs. OUTPUT POWER



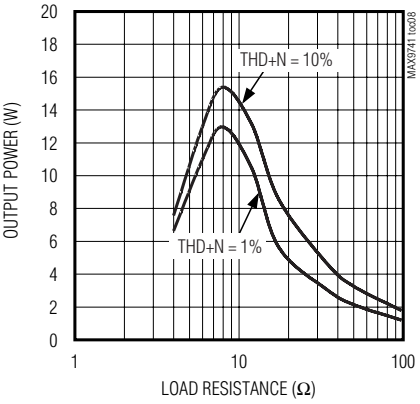
EFFICIENCY vs. OUTPUT POWER



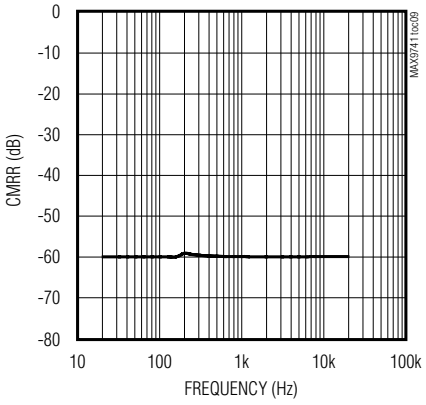
OUTPUT POWER vs. SUPPLY VOLTAGE



OUTPUT POWER vs. LOAD RESISTANCE



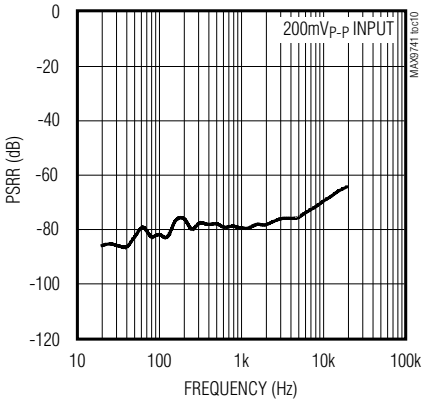
COMMON-MODE REJECTION RATIO vs. FREQUENCY



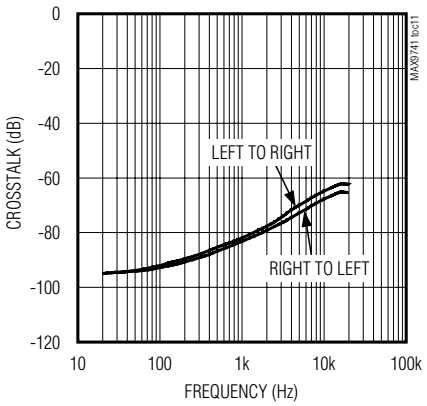
Typical Operating Characteristics (continued)

(V_{DD} = 18V, R_L = 8Ω, f_{IN} = 1kHz, 33µH with 4Ω, 68µH with 8Ω, part in SSM mode, 136µH with 16Ω, measurement BW = 22Hz to 22kHz, unless otherwise noted.)

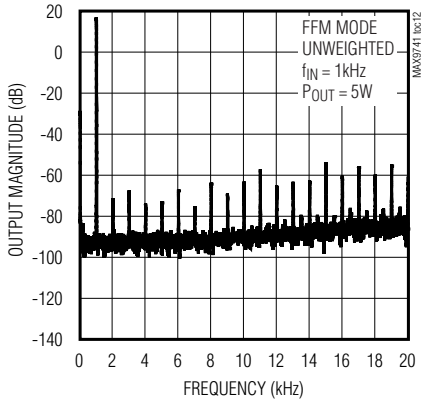
POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



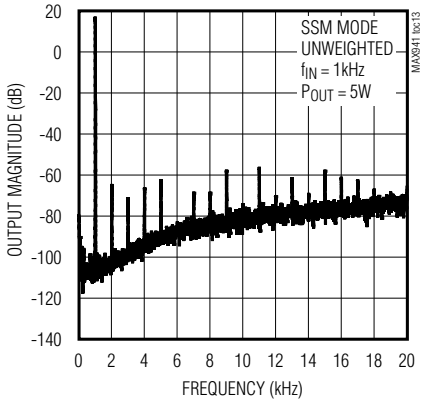
CROSSTALK vs. FREQUENCY



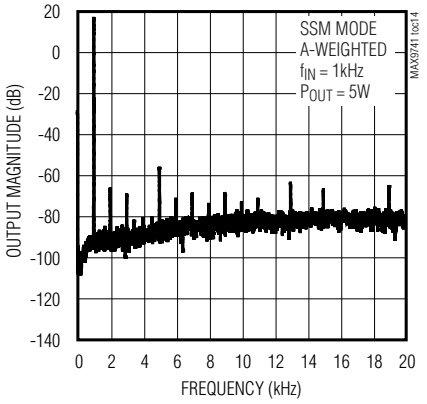
OUTPUT FREQUENCY SPECTRUM



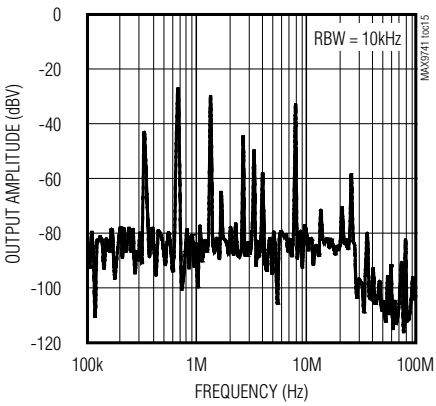
OUTPUT FREQUENCY SPECTRUM



OUTPUT FREQUENCY SPECTRUM



WIDEBAND OUTPUT SPECTRUM (FFM MODE)

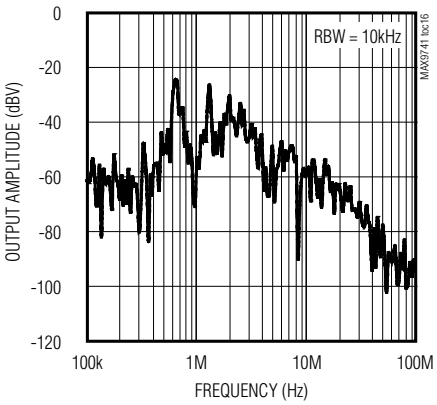


12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

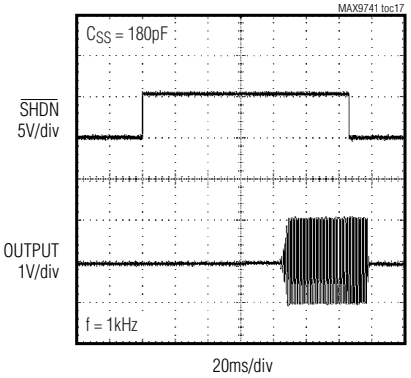
Typical Operating Characteristics (continued)

(V_{DD} = 18V, R_L = 8Ω, f_{IN} = 1kHz, 33µH with 4Ω, 68µH with 8Ω, part in SSM mode, 136µH with 16Ω, measurement BW = 22Hz to 22kHz, unless otherwise noted.)

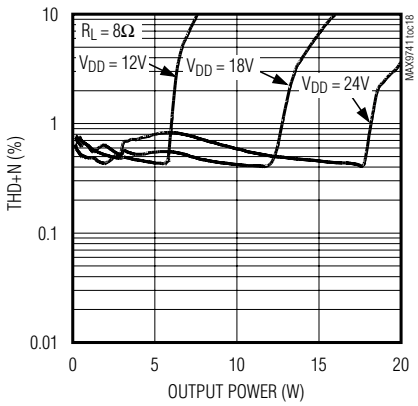
WIDEBAND OUTPUT SPECTRUM (SSM MODE)



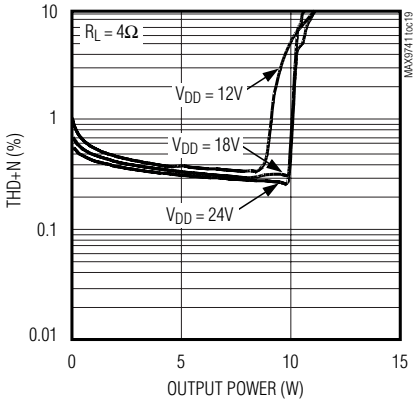
TURN-ON/TURN-OFF RESPONSE



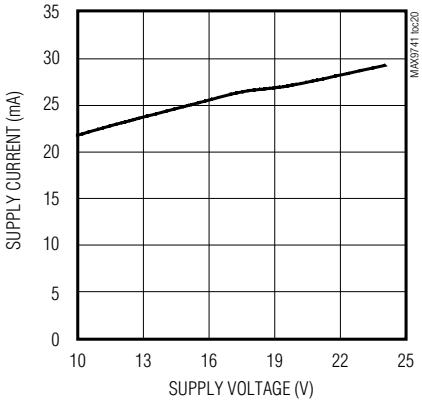
TOTAL HARMONIC DISTORTION PLUS NOISE vs. OUTPUT POWER WITH FERRITE BEAD FILTER



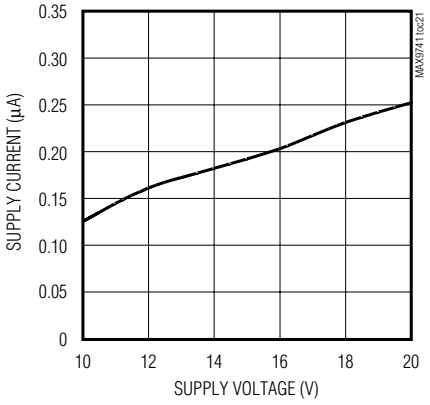
TOTAL HARMONIC DISTORTION PLUS NOISE vs. OUTPUT POWER WITH FERRITE BEAD FILTER



SUPPLY CURRENT vs. SUPPLY VOLTAGE



SHUTDOWN SUPPLY CURRENT vs. SUPPLY VOLTAGE



Pin Description

PIN	NAME	FUNCTION
1, 4, 7, 11–15, 19, 21, 23, 25, 28, 33–36, 39, 42, 43, 44, 49, 50, 55, 56	N.C.	No Connection. Not internally connected.
2, 3, 40, 41	PGND	Power Ground
5, 6, 37, 38	V_{DD}	Power-Supply Input
8	C1N	Charge-Pump Flying Capacitor Negative Terminal
9	C1P	Charge-Pump Flying Capacitor Positive Terminal
10	CHOLD	Charge-Pump Hold Capacitor. Connect a 1µF capacitor from CHOLD to V_{DD}.
16	INL-	Left-Channel Negative Input
17	INL+	Left-Channel Positive Input
18	$\overline{SHDN}$	Active-Low Shutdown. Connect $\overline{SHDN}$ to GND to disable the device. Connect to V_{DD} for normal operation.
20	SS	Soft-Start. Connect a 0.47µF capacitor from SS to GND to enable soft-start feature.
22	AGND	Analog Ground
24	REG	Internal Regulator Output. Bypass with a 0.01µF capacitor to PGND.
26	INR-	Right-Channel Negative Input
27	INR+	Right-Channel Positive Input
29	G1	Gain-Select Input 1
30	G2	Gain-Select Input 2
31	FS1	Frequency-Select Input 1
32	FS2	Frequency-Select Input 2
45, 46	OUTR-	Right-Channel Negative Audio Output
47, 48	OUTR+	Right-Channel Positive Audio Output
51, 52	OUTL-	Left-Channel Negative Audio Output
53, 54	OUTL+	Left-Channel Positive Audio Output
—	EP	Exposed Paddle. Connect to GND.

Detailed Description

The MAX9741 low-EMI, Class D audio power amplifier features several improvements to switch-mode amplifier technology. This device offers Class AB performance with Class D efficiency, while occupying minimal board space. A unique modulation scheme and spread-spectrum switching mode create a compact, flexible, low-noise, efficient audio power amplifier. The differential input architecture reduces common-mode noise pickup, and can be used without input-coupling capacitors. The device can also be configured as a single-ended input amplifier.

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

Operating Modes

Fixed-Frequency Modulation (FFM) Mode

The MAX9741 features three FFM modes with different switching frequencies (Table 1). In FFM mode, the frequency spectrum of the Class D output consists of the fundamental switching frequency and its associated harmonics (see the Wideband Output Spectrum graph in the *Typical Operating Characteristics*). The MAX9741 allows the switching frequency to be changed by ±35%, should the frequency of one or more of the harmonics fall in a sensitive band. This can be done at any time and does not affect audio reproduction.

Table 1. Operating Modes

FS1	FS2	SWITCHING MODE (kHz)
L	L	670
L	H	930
H	L	470
H	H	670 ±7%

Spread-Spectrum Modulation (SSM) Mode

A unique, proprietary spread-spectrum mode flattens the wideband spectral components, improving EMI emissions that may be radiated by the speaker and cables. This mode is enabled by setting FS1 = FS2 = H. In SSM mode, the switching frequency varies randomly by ±7% around the center frequency (670kHz). The modulation scheme remains the same, but the period of the triangle waveform changes from cycle to cycle. Instead of a large amount of spectral energy present at multiples of the switching frequency, the energy is now spread over a bandwidth that increases with frequency. Above a few megahertz, the wideband spectrum looks like white noise for EMI purposes.

Efficiency

Efficiency of a Class D amplifier is attributed to the region of operation of the output stage transistors. In a Class D amplifier, the output transistors act as current-steering switches and consume negligible additional power.

The theoretical best efficiency of a linear amplifier is 78%; however, that efficiency is only exhibited at peak output powers. Under normal operating levels (typical music reproduction levels), efficiency falls below 30%, whereas the MAX9741 still exhibits > 78% efficiency under the same conditions (Figure 1).

Shutdown

A shutdown mode reduces power consumption and extends battery life. Driving $\overline{SHDN}$ low places the device in low-power (0.2µA) shutdown mode. Connect $\overline{SHDN}$ to a logic-high for normal operation.

Click-and-Pop Suppression

Comprehensive click-and-pop suppression eliminates audible transients on startup and shutdown. While in shutdown, the H-bridge is pulled to GND through 320kΩ. During startup, or power-up, the input amplifiers are muted and an internal loop sets the modulator bias voltages to the correct levels, preventing clicks and pops when the H-bridge is subsequently enabled. Following startup, a soft-start function gradually unmutes the input amplifiers. The value of the soft-start capacitor has an impact on the click/pop levels. For optimum performance, C_{SS} should be 470nF with a voltage rating of at least 7V.

Mute Function

The MAX9741 features a clickless/popless mute mode. When the device is muted, the outputs stop switching, muting the speaker. Mute only affects the output stage and does not shut down the device. To mute the MAX9741, drive SS to GND by using a MOSFET pulldown (Figure 2). Driving SS to GND during the power-up/down or shutdown/turn-on cycle optimizes click-and-pop suppression.

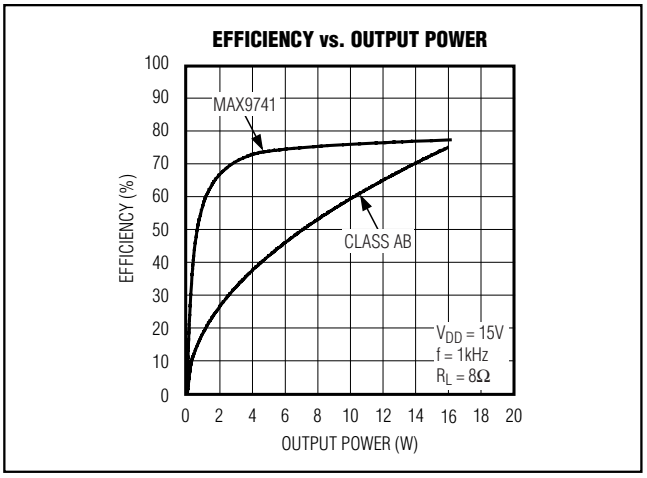


Figure 1. MAX9741 Efficiency vs. Class AB Efficiency

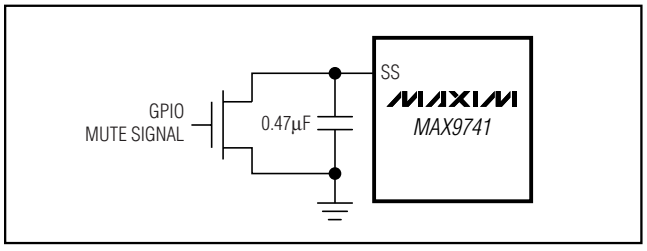


Figure 2. MAX9741 Mute Circuit

Internal Regulator

The MAX9741 has an internal linear regulator, REG, used to power the internal analog circuitry. The voltage at REG is nominally 6V. Bypass REG to AGND with a 10nF capacitor, rated for at least 10V. REG is turned off in shutdown.

Applications Information

Class D Amplifier Outputs

Class D amplifiers differ from analog amplifiers such as Class AB in that their output waveform is composed of high-frequency pulses from ground to the supply rail. When viewed with an oscilloscope the audio signal will not be seen; instead, the high-frequency pulses dominate. To evaluate the output of a Class D amplifier requires taking the difference from the positive and negative outputs, then lowpass filtering the difference to recover the amplified audio signal.

Ferrite Bead Output Filters

The MAX9741's low-EMI output switching method reduces the output filtering requirements when compared to pure PWM Class D amplifiers. The outputs will contain both differential and common-mode noise at the switching frequency and its harmonics. In many applications, a simple ferrite bead filter (see the *Simplified Block Diagram*) will allow the amplifier to pass FCC EMI limits. Ferrite beads offer significant cost and size reductions when compared to conventional inductors. The ferrite bead type and capacitor value can be adjusted to tune the rejection to match the speaker cable length.

Actual EMI test results for the MAX9741 are shown in Figure 3. This shows the MAX9741, tested in a 10m anechoic EMC chamber. The MAX9741 test conditions were: SSM mode, 0.5m cables on each side, 16dB gain, 18V supply voltage, both channels playing pink noise at 4W per channel into 8Ω shielded speakers.

The graph of Figure 3 indicates peak readings. Actual quasi peak readings per EN55022B specification will be lower due to Maxim's proprietary SSM mode. Table 2 lists select values, indicating the peak reading, the quasi-peak reading, and the actual margin to EN55022B specification.

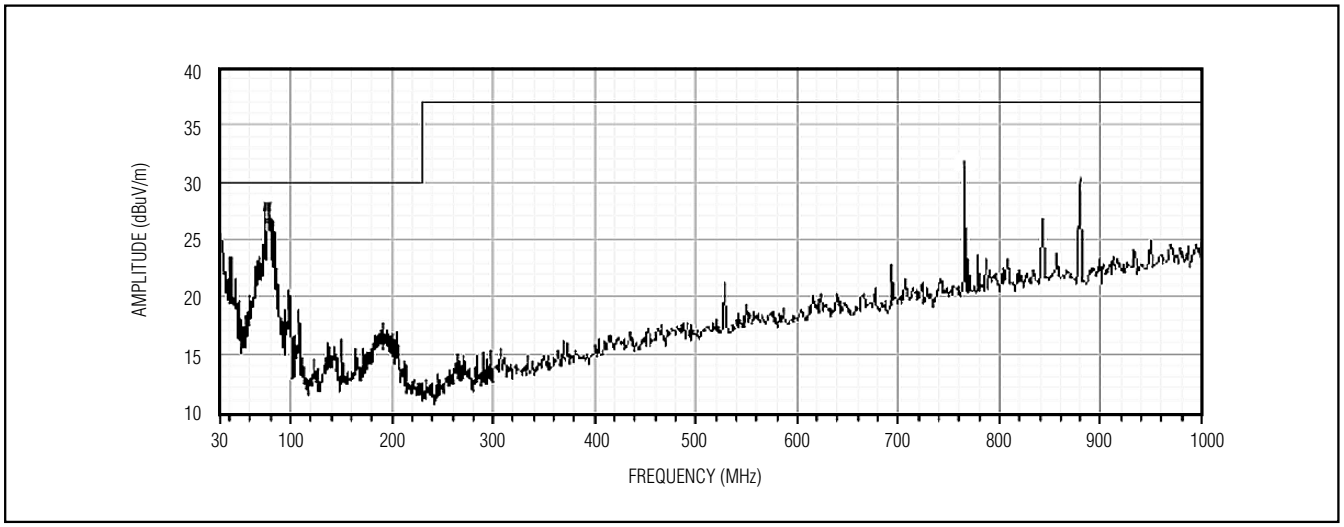


Figure 3. EMI Measurement of MAX9741 in 10m Anechoic Chamber

Table 2. Peak and Quasi-Peak EMI Readings

FREQUENCY (MHz)	PRELIMINARY PEAK READING (dBµV/m)	QUASI PEAK READING (dBµV/m)	EN55022B LIMIT (dBµV/m)	ACTUAL MARGIN (dBµV/m)
75.38	28.1	18.3	30.0	11.7
78.57	28.0	21.9	30.0	-8.1
83.18	26.6	20.6	30.0	-9.4

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

Ferrite beads are available from many manufacturers. Table 3 lists some manufacturers who make ferrite beads and other products suitable for use with Class D amplifiers.

Although they offer a low cost and small size, ferrite bead filters slightly increase distortion and slightly reduce efficiency. If the audio performance of the ferrite bead filters does not meet the system requirements, then a full inductor/capacitor (LC) filter should be considered.

Inductor/Capacitor Output Filters

Using a full inductor and capacitor (LC) output filter provides significant attenuation of the fundamental switching energy.

Select inductors rated for the expected RMS current load. For example, if using a Class D amplifier up to 10W into 8Ω, the inductor should be rated for 1.25A RMS or more. Furthermore, the inductor should maintain a constant inductance value across the expected current range. Inductors which change in value as a function of current will cause harmonic distortion.

The output capacitors can also affect audio performance. Ceramic capacitors are often selected for their size and cost advantage, but they cause distortion. If the application constraints dictate ceramic capacitors, selecting higher voltage rating and larger package size mitigates some of the shortcomings. Best performance is obtained with plastic film capacitors, but these are larger and more expensive.

Filterless Operation

In some cases, a Class D amplifier can be used without an output filter. The intrinsic inductance of the loudspeaker stores energy from the high-speed PWM pulses, converting these into power in the audible frequency range. Filterless operation requires the Class D amplifier to be very close to the speaker. Distances greater than a few centimeters must be evaluated for EMC compliance.

Gain Selection

Table 4 shows the suggested gain settings to attain a maximum output power from a given peak input voltage and given load.

Output Offset

Unlike a Class AB amplifier, the output offset voltage of Class D amplifiers does not noticeably increase quiescent current draw when a load is applied. This is due to the power conversion of the Class D amplifier. For example, an 8mVDC offset across an 8Ω load results in 1mA extra current consumption in a Class AB device. In the Class D case, an 8mV offset into 8Ω equates to an additional power drain of 8µW. Due to the high efficiency of the Class D amplifier, this represents an additional quiescent current draw of: 8µW / (V_{DD} / 100 × η), which is in the order of a few microamps.

Input Amplifier

Differential Input

The MAX9741 features a differential input structure, making them compatible with many CODECs, and offering improved noise immunity over a single-ended input amplifier. In devices such as PCs, noisy digital signals can be picked up by the amplifier's input traces. The signals appear at the amplifiers' inputs as common-mode noise. A differential input amplifier amplifies the difference of the two inputs, any signal common to both inputs is canceled.

Table 3. Filter Component Suppliers

SUPPLIER	PRODUCT	WEBSITE
Murata	Ferrite beads, capacitors	www.murata.com
Taiyo Yuden	Ferrite beads, capacitors	www.t-yuden.com
TDK	Ferrite beads, capacitors	www.tdk.co.jp/tetop01
Fairrite	Ferrite beads	www.fair-rite.com
Coilcraft	Inductors	www.coilcraft.com
Sumida	Inductors	www.sumida.com
Panasonic	Inductors	www.panasonic.com/industrial/components

Table 4. Gain Settings

G1	G2	GAIN (dB)
0	0	29.6
0	1	19.1
1	0	13
1	1	16

Single-Ended Input

The MAX9741 can be configured as single-ended input amplifiers by capacitively coupling either input to GND and driving the other input (Figure 4).

Component Selection

Input Filter

An input capacitor, C_{IN}, in conjunction with the input impedance of the MAX9741, forms a highpass filter that removes the DC bias from an incoming signal. The AC-coupling capacitor allows the amplifier to bias the signal to an optimum DC level. Assuming zero-source impedance, the -3dB point of the highpass filter is given by:

$$f_{-3dB} = \frac{1}{2\pi\ R_{IN}\ C_{IN}}$$

Choose C_{IN} so f_{-3dB} is well below the lowest frequency of interest. Setting f_{-3dB} too high affects the low-frequency response of the amplifier. Use capacitors with dielectrics that have low-voltage coefficients, such as tantalum or aluminum electrolytic. Capacitors with high-voltage coefficients, such as ceramics, may result in increased distortion at low frequencies.

Charge-Pump Capacitor Selection

Use capacitors with an ESR less than 100mΩ for optimum performance. Low-ESR ceramic capacitors minimize the output resistance of the charge pump. For best performance over the extended temperature range, select capacitors with an X7R dielectric.

Flying Capacitor (C1)

The value of the flying capacitor (C1) affects the load regulation and output resistance of the charge pump. A C1 value that is too small degrades the device's ability to provide sufficient current drive. Increasing the value of C1 improves load regulation and reduces the charge-pump output resistance to an extent. Above 1µF, the on-resistance of the switches and the ESR of C1 and C2 dominate.

Hold Capacitor (C2)

The output capacitor value and ESR directly affect the ripple at CHOLD. Increasing C2 reduces output ripple. Likewise, decreasing the ESR of C2 reduces both ripple and output resistance. Lower capacitance values can be used in systems with low maximum output power levels.

Sharing Input Sources

In certain systems, a single audio source can be shared by multiple devices (speaker and headphone amplifiers).

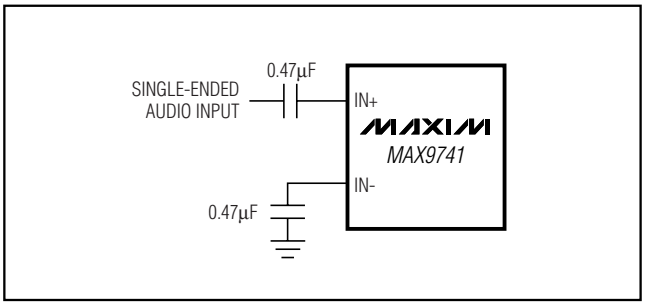


Figure 4. Single-Ended Input

When sharing inputs, it is common to mute the unused device, rather than completely shutting it down, preventing the unused device inputs from distorting the input signal. Mute the MAX9741 by driving SS low through an open-drain output or MOSFET. Driving SS low turns off the Class D output stage, but does not affect the input bias levels of the MAX9741. Be aware that during normal operation, the voltage at SS can be up to 7V, depending on the MAX9741 supply.

Supply Bypassing/Layout

Proper power-supply bypassing ensures low-distortion operation. For optimum performance, bypass V_{DD} to PGND with a 0.1µF or greater capacitor as close to each V_{DD} pin as possible. In some applications, a 0.1µF capacitor in parallel with a larger value, low-ESR ceramic or aluminum electrolytic capacitor provides good results. A low-impedance, high-current power-supply connection to V_{DD} is assumed. Additional bulk capacitance should be added as required depending on the application and power-supply characteristics. AGND and PGND should be star connected to system ground. Refer to the MAX9741 Evaluation Kit for layout guidance.

Class D Amplifier Thermal Considerations

Class D amplifiers provide much better efficiency and thermal performance than a comparable Class AB amplifier. However, the system's thermal performance must be considered with realistic expectations and consideration of many parameters. This application note examines Class D amplifiers using general examples to illustrate good design practices.

Continuous Sine Wave vs. Music

When a Class D amplifier is evaluated in the lab, often a continuous sine wave is used as the signal source. While this is convenient for measurement purposes, it represents a worst-case scenario for thermal loading on the amplifier. It is not uncommon for a Class D amplifier to enter thermal shutdown if driven near maximum output power with a continuous sine wave.

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

Audio content, both music and voice, has a much lower RMS value relative to its peak output power. Figure 5 shows a sine wave and an audio signal in the time domain. Both are measured for RMS value by the oscilloscope. Although the audio signal has a slightly higher peak value than the sine wave, its RMS value is almost half that of the sine wave. Therefore, while an audio signal may reach similar peaks as a continuous sine wave, the actual thermal impact on the Class D amplifier is highly reduced. If the thermal performance of a system is being evaluated, it is important to use actual audio signals instead of sine waves for testing. If sine waves must be used, the thermal performance will be less than the system's actual capability.

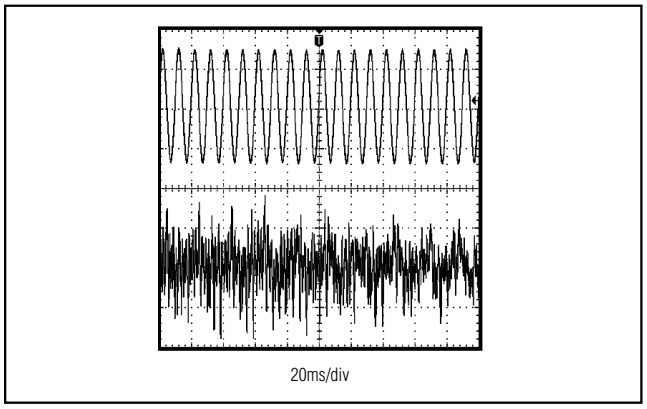


Figure 5. RMS Comparison of Sine Wave vs. Audio Signal

PC Board Thermal Considerations

The exposed pad is the primary route of heat away from the IC. With a bottom-side exposed pad, the PC board and its copper becomes the primary heatsink for the Class D amplifier. Solder the exposed pad to a large copper polygon. Add as much copper as possible from this polygon to any adjacent pin on the Class D amplifier as well as to any adjacent components, provided these connections are at the same potential. These copper paths must be as wide as possible. Each of these paths contributes to the overall thermal capabilities of the system.

The copper polygon to which the exposed pad is attached should have multiple vias to the opposite side of the PC board, where they connect to another copper polygon. Make this polygon as large as possible within the system's constraints for signal routing.

Additional improvements are possible if all the traces from the device are made as wide as possible. Although the IC pins are not the primary thermal path out of the package, they do provide a small amount. The total improvement would not exceed approximately 10%, but it could make the difference between acceptable performance and thermal problems.

With a bottomside exposed pad, the lowest resistance thermal path is on the bottom of the PC board. The topside of the IC is not a significant thermal path for the device.

Thermal Calculations

The die temperature of a Class D amplifier can be estimated with some basic calculations. For example, the die temperature is calculated for the below conditions:

- T_A = +40°C
- P_{OUT} = 10W (5W + 5W)
- Efficiency (η) = 78%
- θ_{JA} = 21°C/W

First, the Class D amplifier's power dissipation must be calculated.

$$P_{DISS} = \frac{P_{OUT}}{\eta} - P_{OUT} = \frac{10W}{78\%} - 10W = 2.82W$$

Then the power dissipation is used to calculate the die temperature, T_C, as follows:

$$T_C = T_A + P_{DISS} \times \theta_{JA} = 40°C + 2.82W \times 21°C/W = 99.2°C$$

Load Impedance

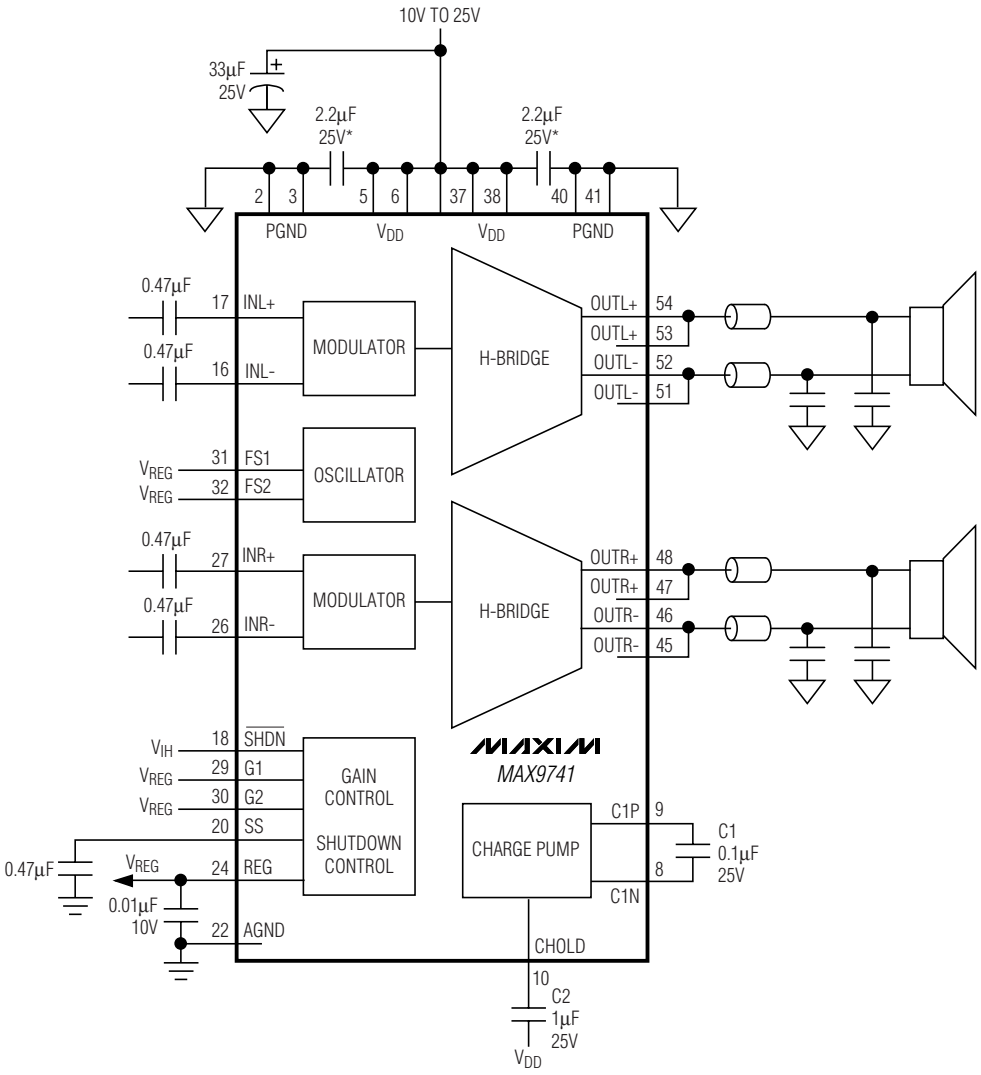
The on-resistance of the MOSFET output stage in Class D amplifiers affects both the efficiency and the peak-current capability. Reducing the peak current into the load reduces the I^2R losses in the MOSFETs, increasing efficiency. To keep the peak currents lower, choose the highest impedance speaker which can still deliver the desired output power within the voltage swing limits of the Class D amplifier and its supply voltage.

Optimize MAX9741 Efficiency with Load Impedance and Supply Voltage

To optimize efficiency, load the output stage with 12Ω to 16Ω speakers. The MAX9741 exhibits highest efficiency performance when driving higher load impedance (see the *Typical Operating Characteristics*). If a 12Ω to 16Ω load is not available, select a lower supply voltage when driving 4Ω to 10Ω loads.

For best performance, choose a speaker impedance to complement the required output power and the available supply voltage. For example, if operating from a 24V supply and a peak output of 10W per channel is desired, using 12Ω speakers provides the best audio performance and power efficiency. The amplifier outputs are short-circuit protected at approximately 2A. Selecting a higher impedance driver helps prevent exceeding the current limit.

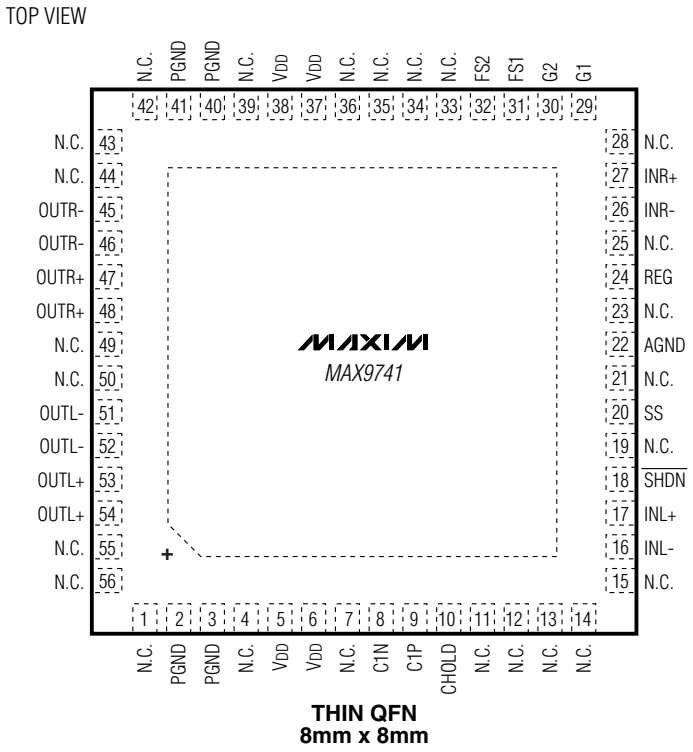
Application Circuit



LOGIC INPUTS SHOWN FOR A_V = 16dB (SSM).
V_{IN} = LOGIC-HIGH > 2.5V.
*CAPACITOR VOLTAGE RATINGS MAY BE REDUCED WHEN OPERATING WITH REDUCED SUPPLY VOLTAGES.

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

Pin Configuration



THIN QFN
8mm x 8mm

Chip Information

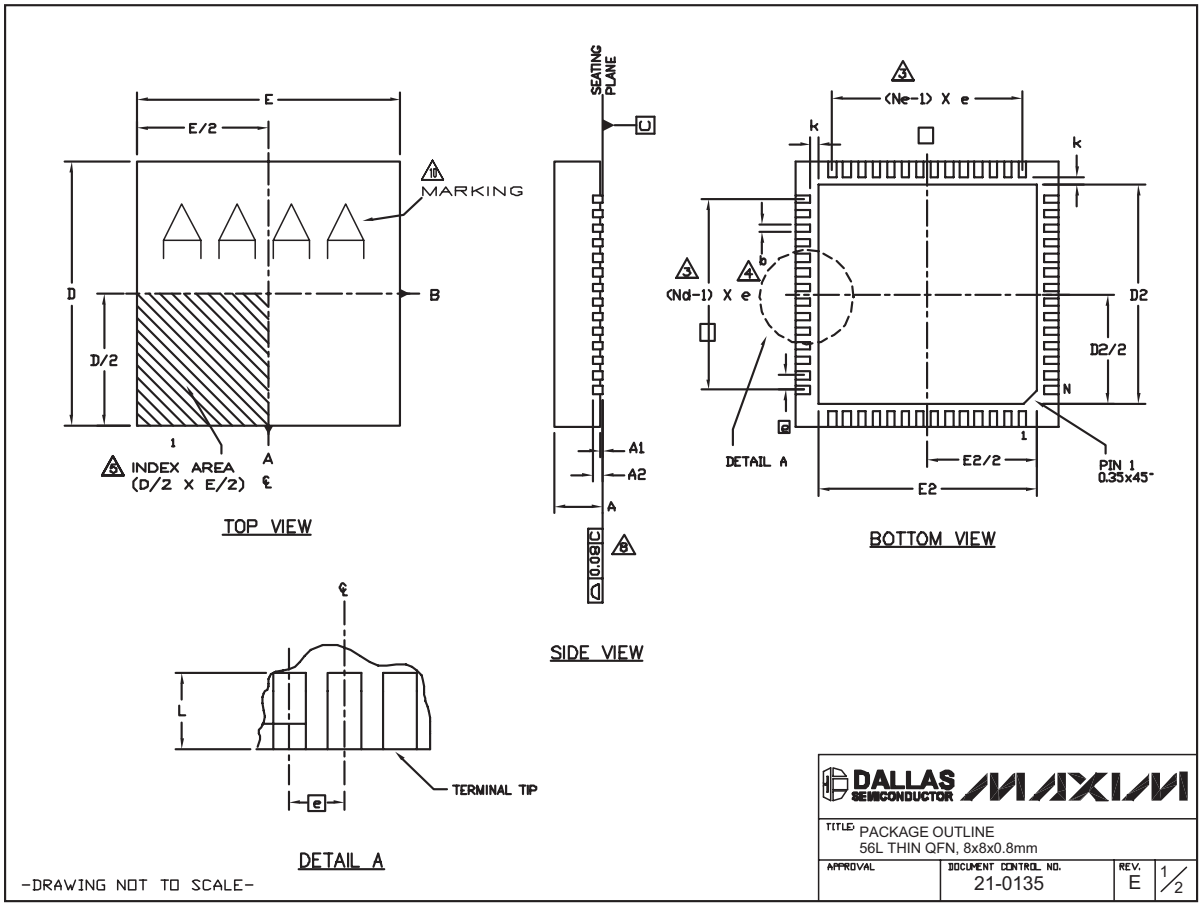
TRANSISTOR COUNT: 4630

PROCESS: BiCMOS

12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



12W+12W, Low-EMI, Spread-Spectrum, Stereo, Class D Amplifier

Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to **www.maxim-ic.com/packages**.)

NOTES:

1. DIE THICKNESS ALLOWABLE IS 0.225mm MAXIMUM (0.009 INCHES MAXIMUM).
2. DIMENSIONING & TOLERANCES CONFORM TO ASME Y14.5M. - 1994.
3. N IS THE NUMBER OF TERMINALS.
   Nd IS THE NUMBER OF TERMINALS IN X-DIRECTION &
   Ne IS THE NUMBER OF TERMINALS IN Y-DIRECTION.
4. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25mm FROM TERMINAL TIP.
5. THE PIN #1 IDENTIFIER MUST BE LOCATED ON THE TOP SURFACE OF THE PACKAGE WITHIN HATCHED AREA AS SHOWN.
   EITHER AN INDENTATION MARK OR INK/LASER MARK IS ACCEPTABLE.
6. ALL DIMENSIONS ARE IN MILLIMETERS.
7. PACKAGE WARPAGE MAX 0.01mm.
8. APPLIES TO EXPOSED PAD AND TERMINALS.
   EXCLUDES INTERNAL DIMENSION OF EXPOSED PAD.
9. MEETS JEDEC MO220.
10. MARKING IS FOR PACKAG ORIENTATION REFERENCE ONLY
11. NUMBER OF LEADS ARE FOR REFERENCE ONLY

SYMBOL	56L 8x8			NOTE
	MIN.	NOM.	MAX.	
A	0.70	0.75	0.80	
b	0.20	0.25	0.30	4
D	7.90	8.00	8.10	
E	7.90	8.00	8.10	
e	0.50 BSC			
N	56			3
Nd	14			3
Ne	14			3
L	0.30	0.40	0.50	
A1	0.00	0.02	0.05	
A2	0.20 REF			
k	0.25	--	--	

PKG. CODE	EXPOSED PAD VARIATION						JEDEC	DOWN BONDS ALLOWED
	D2			E2				
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.		
T5688-2	6.50	6.65	6.70	6.50	6.65	6.70	WLLD-5	YES
T5688-3	6.50	6.65	6.70	6.50	6.65	6.70	WLLD-5	NO

-DRAWING NOT TO SCALE-

DALLAS SEMICONDUCTOR MAXIM

TITLE: PACKAGE OUTLINE
56L THIN QFN, 8x8x0.8mm

APPROVAL	DOCUMENT CONTROL NO.	REV.	
	21-0135	E	2/2

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