19-1934; Rev 2; 11/05

MAXIM

Dual 256-Tap, Low-Drift, Digital Potentiometers in 10-µMAX

General Description

The MAX5403/MAX5404/MAX5405 is a family of dual linear taper digital potentiometers. Each device has one 3-terminal potentiometer and one 2-terminal variable resistor (Figure 1). The MAX5403/MAX5404/MAX5405 operate from +2.7V to +5.5V single-supply voltages and use an ultra-low supply current of 0.1µA. These devices also provide glitchless switching between resistors taps as well as a convenient power-on reset (POR) that sets the wiper to the midscale position at power-up. The potentiometer consists of a fixed resistor with a wiper contact that is digitally controlled through a 3-wire serial interface and has 256-tap points. It performs the same function as a discrete potentiometer or variable resistor.

These parts are ideal for applications requiring digitally controlled resistors. Three resistance values are available: 10kΩ (MAX5403), 50kΩ (MAX5404), and 100kΩ (MAX5405). A nominal resistor-temperature coefficient of 35ppm/°C end-to-end and 5ppm/°C ratiometric make the MAX5403/MAX5404/MAX5405 ideal for applications requiring low temperature-coefficient variable resistors, such as adjustable-gain circuit configurations.

The MAX5403/MAX5404/MAX5405 are available in a 10-pin µMAX® package. Each device is guaranteed over the extended industrial temperature range (-40°C to +85°C).

Applications

Mechanical Potentiometer Replacement

Low-Drift Programmable Gain Amplifier (PGA)

Volume Control

LCD Screen Adjustment

Adjustable Voltage Reference

Programmable Filters, Delays, Time Constant

Impedance Matching

SPI is a registered trademark of Motorola, Inc.

µMAX is a registered trademark of Maxim Integrated Products, Inc.

Features

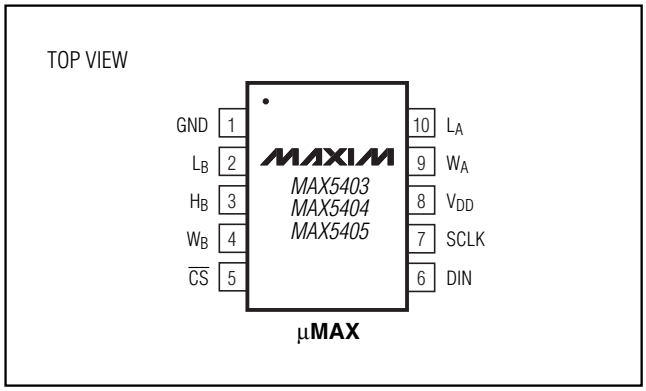
- **10-Pin µMAX Small Footprint Package**
- **256-Tap Positions**
- **Ultra Low 0.1µA Supply Current**
- **+2.7V to +5.5V Single-Supply Operation**
- **Low End-To-End Temperature Coefficient: 35ppm/°C**
- **Low Ratiometric Temperature Coefficient: 5ppm/°C**
- **Power-On Reset: Wiper Goes to Midscale (Position 128)**
- **Glitchless Switching Between Resistor Taps**
- **3-Wire SPI™ -Interface Compatible**
- **10kΩ/50kΩ/100kΩ Resistor Values**

Ordering Information

PART	PIN-PACKAGE	R (kΩ)	PKG CODE
MAX5403EUB	10 µMAX	10	U10C-4
MAX5404EUB	10 µMAX	50	U10C-4
MAX5405EUB	10 µMAX	100	U10C-4

Note: *All devices operate over the -40°C to +85°C temperature range.*

Pin Configuration



For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Dual 256-Tap, Low-Drift, Digital Potentiometers in 10-µMAX

ABSOLUTE MAXIMUM RATINGS

V_{DD} to GND .....-0.3V to +6V
DIN, SCLK, $\overline{CS}$ .....-0.3V to +6V
H_X, L_X, W_X to GND .....-0.3V to (V_{DD} + 0.3)
Maximum Continuous Current Into H_X, L_X, and W_X .....±1mA
Continuous Power Dissipation (T_A = +70°C)
10-Pin µMAX (derate 5.6mW/°C above +70°C) 444mW
Operating Temperature Range .....-40°C to +85°C
Junction Temperature.....+150°C
Storage Temperature Range .....-65°C to +150°C
Lead Temperature (soldering, 10s) .....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{DD} = +5V, unless otherwise noted. V_H = V_{DD}, V_L = 0, T_A = T_{MIN} to T_{MAX}. Typical values are at V_{DD} = +5V, T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
DC PERFORMANCE (Voltage Divider Mode)							
Resolution	N			8			Bits
Integral Nonlinearity (Notes 1, 2)	INL					±1/2	LSB
Differential Nonlinearity (Notes 1, 2)	DNL					±1/2	LSB
End-to-End Resistor Tempco	TC_R				35		ppm/°C
Ratiometric Resistor Tempco					5		ppm/°C
Full-Scale Error		MAX5403			-8		LSB
Zero-Scale Error		MAX5403			+8		LSB
Full-Scale Error		MAX5404			-1.6		LSB
Zero-Scale Error		MAX5404			+1.6		LSB
Full-Scale Error		MAX5405			-0.8		LSB
Zero-Scale Error		MAX5405			+0.8		LSB
DC PERFORMANCE (Variable Resistor Mode)							
Resolution	N			8			Bits
Integral Nonlinearity (Notes 1, 3)	INL	V_{DD} = +5V	MAX5403 (Pot A)			±2.5	LSB
			MAX5403 (Pot B)			±1	
			MAX5404			±1	
			MAX5405			±1	
		V_{DD} = +3V	MAX5403 (Pot A)			±4.5	
			MAX5403 (Pot B)			±3	
			MAX5404			±1.5	
			MAX5405			±1.5	
Differential Nonlinearity (Notes 1, 3)	DNL	V_{DD} = +5V				±1/2	LSB
		V_{DD} = +3V					
DC PERFORMANCE (Resistor Characteristics)							
Wiper Resistance (Note 4)	R_W	V_{DD} = +5V			275		Ω
		V_{DD} = +3V				550	
Wiper Capacitance	C_W	MAX5403			50		pF
		MAX5404/MAX5405			30		

ELECTRICAL CHARACTERISTICS (continued)

(V_{DD} = +5V, unless otherwise noted. $V_H = V_{DD}$, V_L = 0, $T_A = T_{MIN}$ to T_{MAX}. Typical values are at V_{DD} = +5V, T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
End-to-End Resistance	R_{HL}	MAX5403		7.5	10	12.5	kΩ
		MAX5404		37.5	50	62.5	
		MAX5405		75	100	125	
DIGITAL INPUTS							
Input High Voltage	V_{IH}			0.7 × V_{DD}			V
Input Low Voltage	V_{IL}					0.3 × V_{DD}	V
Input Leakage Current						±1.0	µA
Input Capacitance					5		pF
TIMING CHARACTERISTICS (ANALOG)							
Wiper-Settling Time	t_S	MAX5403			100		ns
		MAX5404			325		
		MAX5405			650		
TIMING CHARACTERISTICS (DIGITAL) (Note 5)							
Maximum SCLK Frequency				10			MHz
SCLK Clock Period	t_{CP}			100			ns
SCLK Pulse Width High	t_{CH}			40			ns
SCLK Pulse Width Low	t_{CL}			40			ns
$\overline{CS}$ Fall to SCLK Rise Setup Time	t_{CSS}			40			ns
SCLK Rise to $\overline{CS}$ Rise Hold Time	t_{CSH}			0			ns
DIN Setup Time	t_{DS}			40			ns
DIN Hold Time	t_{DH}			0			ns
SCLK Rise to $\overline{CS}$ Fall Delay	t_{CS0}			10			ns
$\overline{CS}$ Rise to SCLK Rise Hold	t_{CS1}			40			ns
$\overline{CS}$ Pulse Width High	t_{CSW}			100			ns
POWER SUPPLIES							
Supply Voltage	V_{DD}			2.7		5.5	V
Supply Current	I_{DD}	$\overline{CS}$ = SCLK = DIN = V_{DD}	V_{DD} = +5V		0.8	5	µA
			V_{DD} = +2.7V		0.1		µA

Note 1: Linearity is defined in terms of the H_X to L_X code-dependent resistance.

Note 2: The DNL and INL are measured with the potentiometer configured as a voltage divider with $H_X = V_{DD}$ and L_X = 0. The wiper terminal is unloaded and measured with an ideal voltmeter.

Note 3: The DNL and INL are measured with the potentiometer configured as a variable resistor. For the 3-terminal potentiometer (Pot A), H_A is unconnected and L_A = 0. For the 2-terminal potentiometer (Pot B), only L_B = 0. At V_{DD} = +5V, the wiper terminal is driven with a source current of 400µA for the 10kΩ configuration, 80µA for the 50kΩ configuration, and 40µA for the 100kΩ configuration. At V_{DD} = +3V, 200µA/40µA/20µA for 10kΩ/50kΩ/100kΩ configuration respectively.

Note 4: The wiper resistance is the worst value measured by injecting into W_X, a current $I_W = V_{DD} / R_{HL}$.

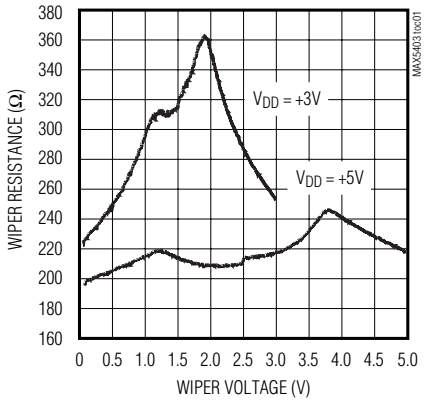
Note 5: Digital timing is guaranteed by design.

Dual 256-Tap, Low-Drift, Digital Potentiometers in 10-µMAX

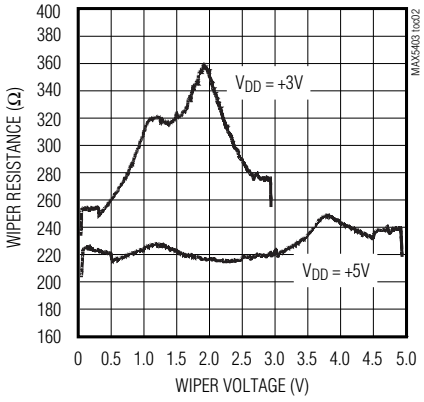
Typical Operating Characteristics

(V_{DD} = +5.0V, T_A = +25°C, unless otherwise noted.)

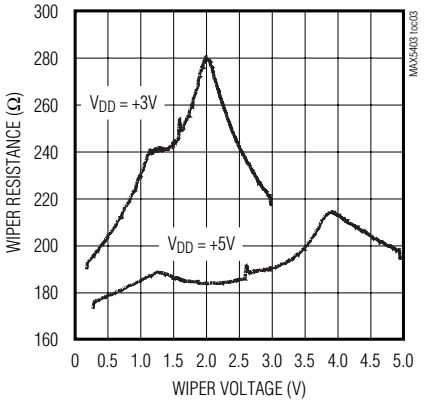
WIPER RESISTANCE vs. WIPER VOLTAGE (50kΩ)



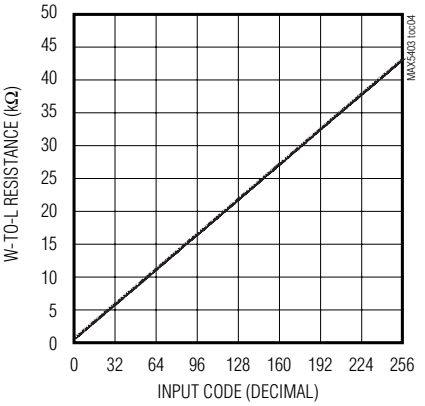
WIPER RESISTANCE vs. WIPER VOLTAGE (100kΩ)



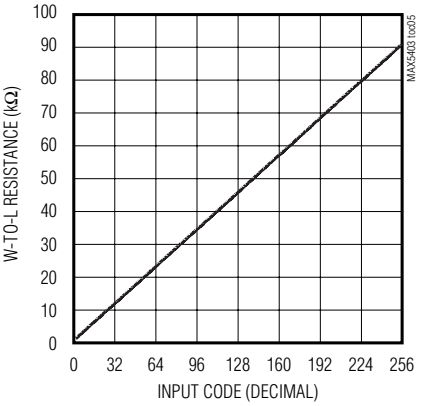
WIPER RESISTANCE vs. WIPER VOLTAGE (10kΩ)



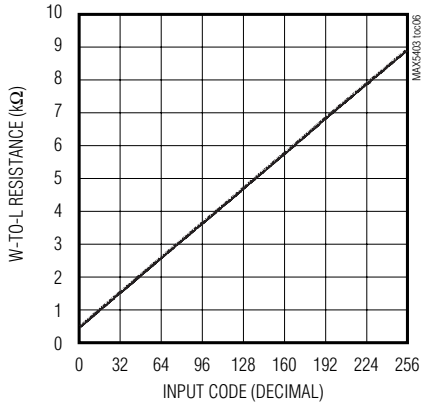
WIPER-TO-END RESISTANCE vs. INPUT CODE (50kΩ)



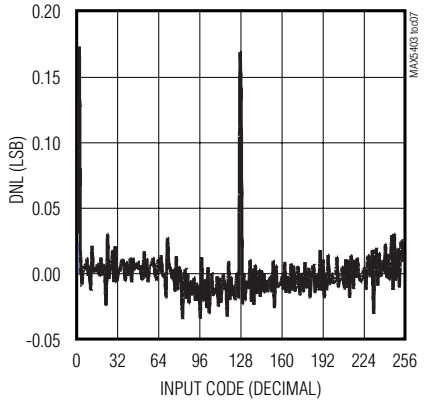
WIPER-TO-END RESISTANCE vs. INPUT CODE (100kΩ)



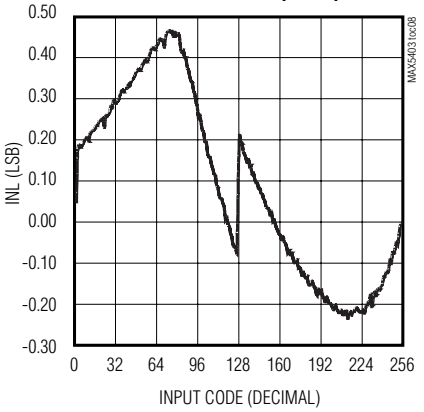
WIPER-TO-END RESISTANCE vs. INPUT CODE (10kΩ)



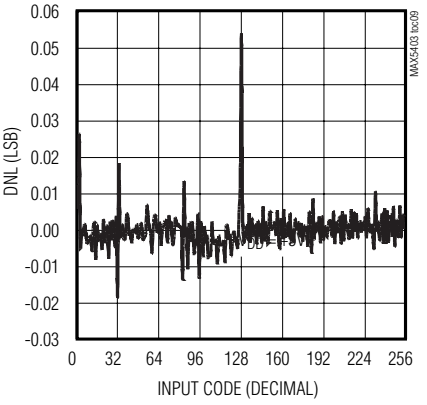
VARIABLE RESISTOR DNL vs. INPUT CODE (10kΩ)



VARIABLE RESISTOR INL vs. INPUT CODE (10kΩ)



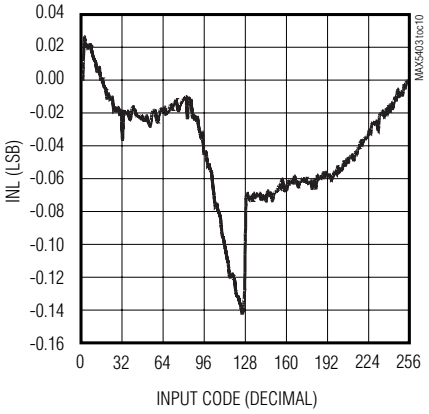
VARIABLE RESISTOR DNL vs. INPUT CODE (50kΩ)



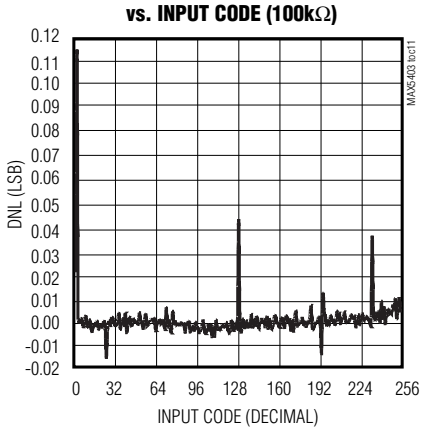
Typical Operating Characteristics (continued)

(V_{DD} = +5.0V, T_A = +25°C, unless otherwise noted.)

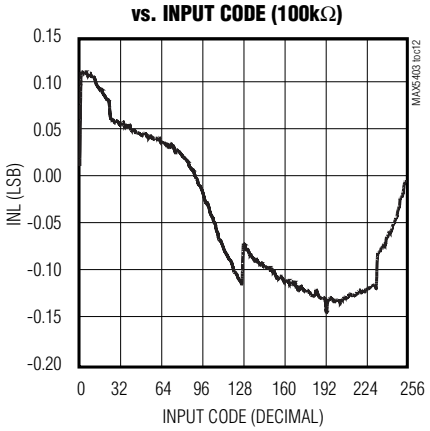
VARIABLE RESISTOR INL vs. INPUT CODE (50kΩ)



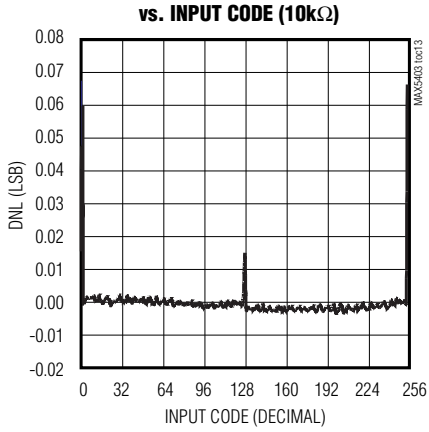
VARIABLE RESISTOR DNL vs. INPUT CODE (100kΩ)



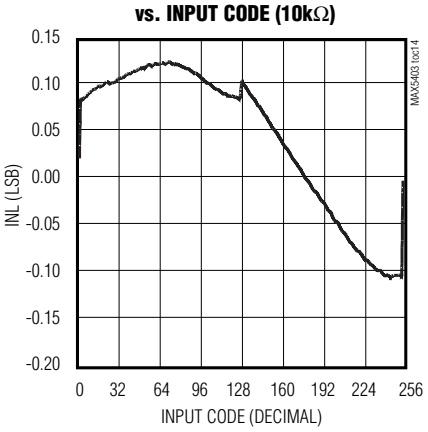
VARIABLE RESISTOR INL vs. INPUT CODE (100kΩ)



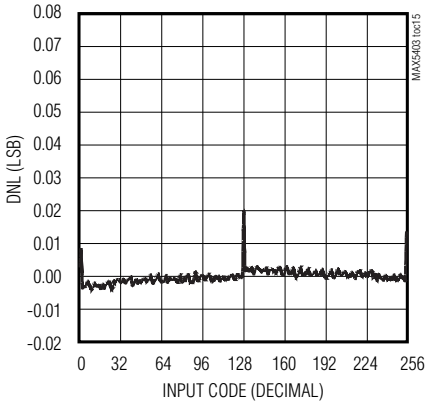
VOLTAGE-DIVIDER DNL vs. INPUT CODE (10kΩ)



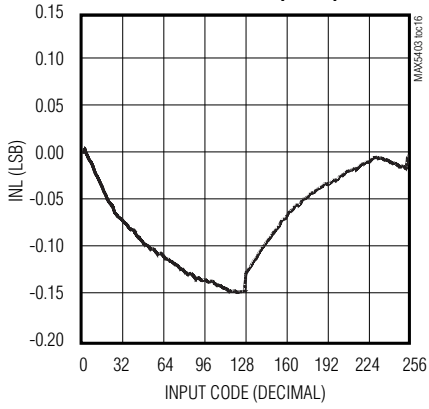
VOLTAGE-DIVIDER INL vs. INPUT CODE (10kΩ)



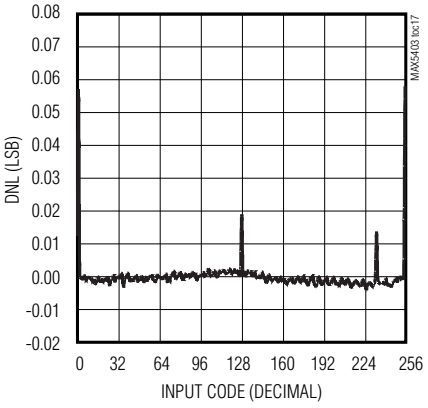
VOLTAGE-DIVIDER DNL vs. INPUT CODE (50kΩ)



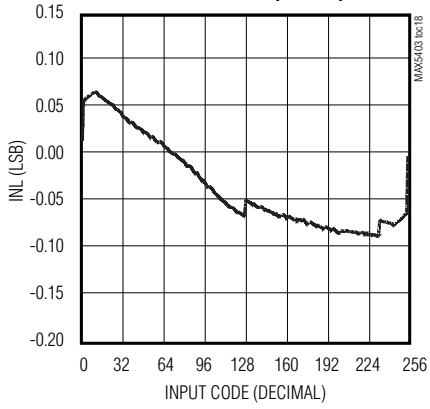
VOLTAGE-DIVIDER INL vs. INPUT CODE (50kΩ)



VOLTAGE-DIVIDER DNL vs. INPUT CODE (100kΩ)



VOLTAGE-DIVIDER INL vs. INPUT CODE (100kΩ)

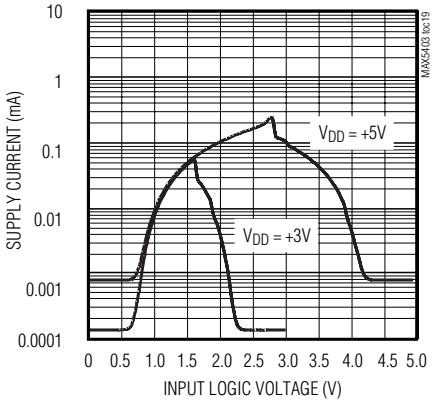


Dual 256-Tap, Low-Drift, Digital Potentiometers in 10-µMAX

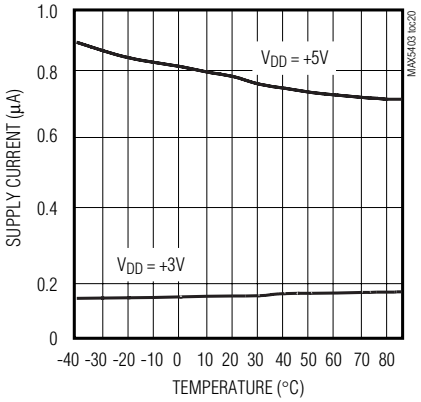
Typical Operating Characteristics (continued)

(V_{DD} = +5.0V, T_A = +25°C, unless otherwise noted.)

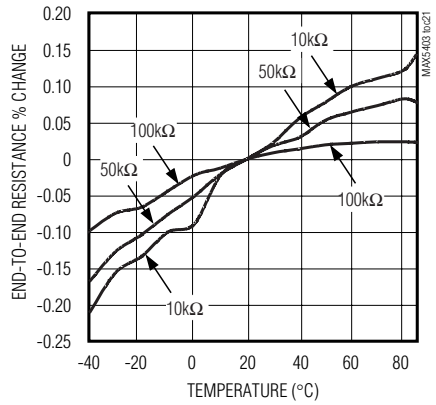
SUPPLY CURRENT vs. LOGIC INPUT VOLTAGE



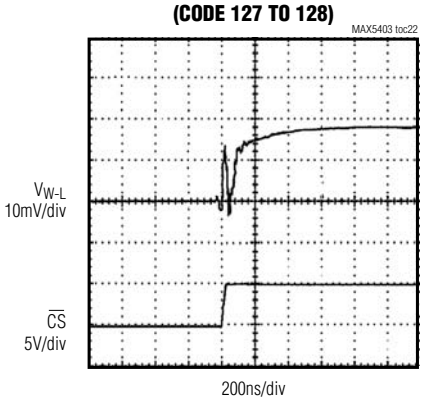
SUPPLY CURRENT vs. TEMPERATURE



END-TO-END RESISTANCE % CHANGE vs. TEMPERATURE



TAP-TO-TAP SWITCHING TRANSIENT (CODE 127 TO 128)



Pin Description

PIN	NAME	FUNCTION
1	GND	Ground
2	L_B	Low Terminal of Resistor B
3	H_B	High Terminal of Resistor B
4	W_B	Wiper Terminal of Resistor B
5	$\overline{CS}$	SPI Chip Select
6	DIN	SPI Serial Data Input
7	SCLK	SPI Clock Input
8	V_{DD}	Power Supply, +2.7V to +5.5V. Connect a 0.1µF capacitor to GND.
9	W_A	Wiper Terminal of Resistor A
10	L_A	Low Terminal of Resistor A

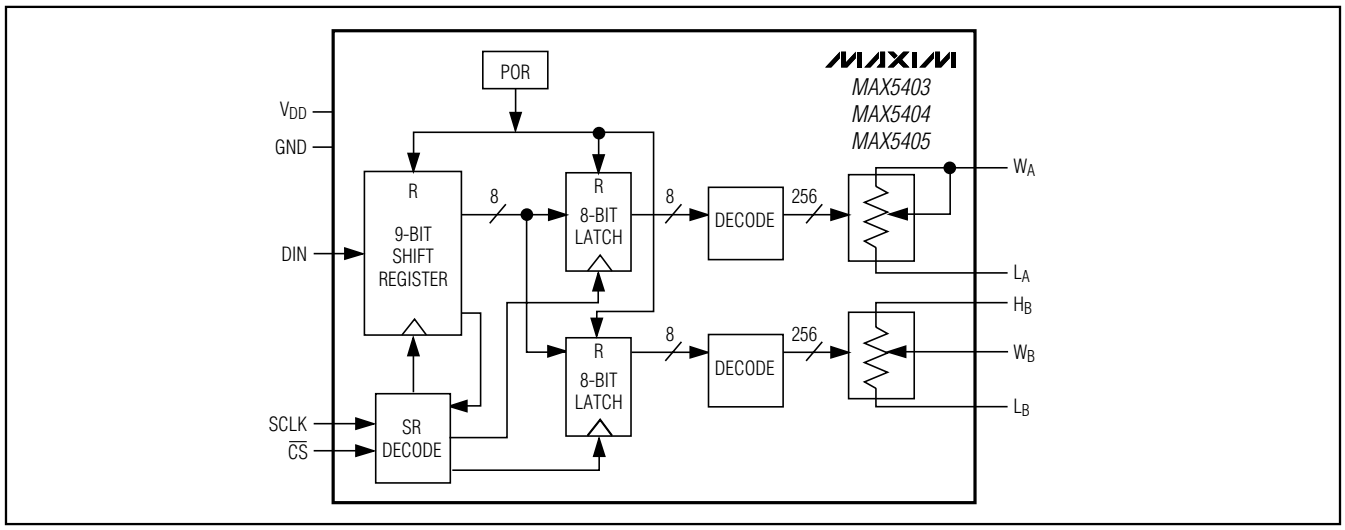


Figure 1. MAX5403/MAX5404/MAX5405 Functional Diagram: 2-Terminal Variable Resistor and 3-Terminal Potentiometers in 10-µMAX Configuration

Detailed Description

Each potentiometer consists of 255 fixed resistors in series between pins H_B and L_B for the 3-terminal potentiometer and between W_A and L_A for the 2-terminal variable resistor (Figure 1). The potentiometer wiper (pin W_X) can be programmed to access any one of the 256 different tap points on the resistor string. The MAX5403/MAX5404/MAX5405 require nine bits to program the wiper position. The 1st bit is an address code, allowing one or the other potentiometers to be selected for programming. The potentiometers are programmed independently of each other.

The MAX5403/MAX5404/MAX5405 use a 3-wire serial data interface to control the wiper tap position. This write-only interface contains three inputs: Chip Select ($\overline{CS}$), Data In (DIN), and Data Clock (SCLK). When $\overline{CS}$ is taken low, data from the DIN pin is synchronously loaded into the serial shift register on each rising edge

Dual 256-Tap, Low-Drift, Digital Potentiometers in 10-µMAX

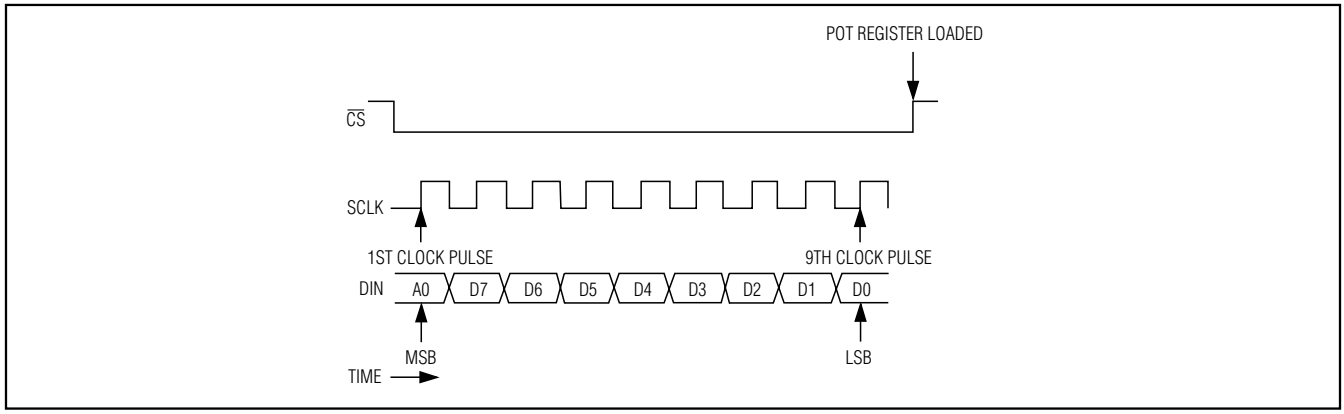


Figure 2. Potentiometer Serial Data Timing Circuit

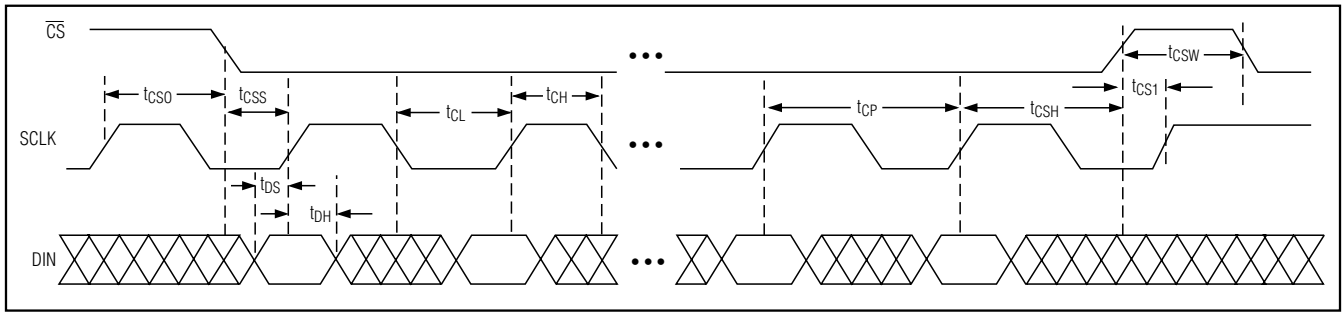


Figure 3. Detailed Serial Interface Timing Diagram

of each SCLK pulse (Figure 2). After all the data bits have been shifted in, they are latched into the appropriate potentiometer control register when $\overline{CS}$ transitions from low to high. Note that if $\overline{CS}$ is not kept low during the entire data stream, the data will be corrupted and the device will need to be reloaded.

The first bit A0 (address bit) is used to address one or the other of the potentiometers for programming. Potentiometer control register A is selected for writing when A0 is 'zero', and potentiometer control register B is selected when A0 is one.

The MAX5403/MAX5404/MAX5405 feature POR circuitry that sets the wiper to the midscale position at power-up.

Applications Information

The MAX5403/MAX5404/MAX5405 are intended for a variety of circuits where accurate, fine-tuning adjustable resistance is required, such as in adjustable voltage or adjustable gain circuit configurations. It is primarily used in either a potentiometer divider or a variable resistor configuration.

Adjustable Current to Voltage Converter

Figure 5 shows the MAX5403/MAX5404/MAX5405 being used with a MAX4250 low-noise op amp to fine tune a current to voltage converter. Pins H_B and W_B of the MAX5403/MAX5404/MAX5405 3-terminal potentiometer (only pin W_A of the 2-terminal variable resistor) are connected to the node between R3 and R2 (pin L_X is connected to ground). Circuit space is minimized due to both devices' packaging.

Adjustable Gain Amplifier

Figure 6 shows how to use the MAX5403/MAX5404/MAX5405 to digitally adjust the gain of a noninverting op amp configuration. In Figure 6a, connect the MAX5403/MAX5404/MAX5405 as a 2-terminal variable resistor in series with a resistor to ground to form the adjustable gain control of a noninverting amplifier.

Similarly, Figure 6b shows how to use the MAX5403/MAX5404/MAX5405 as a 3-terminal potentiometer. In this application the MAX5403/MAX5404/MAX5405 low 5ppm/°C ratiometric tempco allows for a very stable adjustable gain-configuration overtemperature.

ADDRESS	DATA WORD							
B0 (A0)	**B1** (D7)	**B2** (D6)	**B3** (D5)	**B4** (D4)	**B5** (D3)	**B6** (D2)	**B7** (D1)	**B8** (D0)
(MSB) First Bit In								(LSB) Last Bit In

Figure 4. Serial Data Format

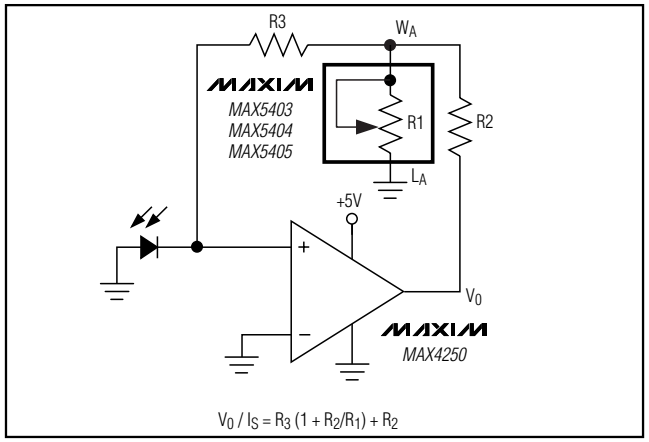


Figure 5. I to V Converter

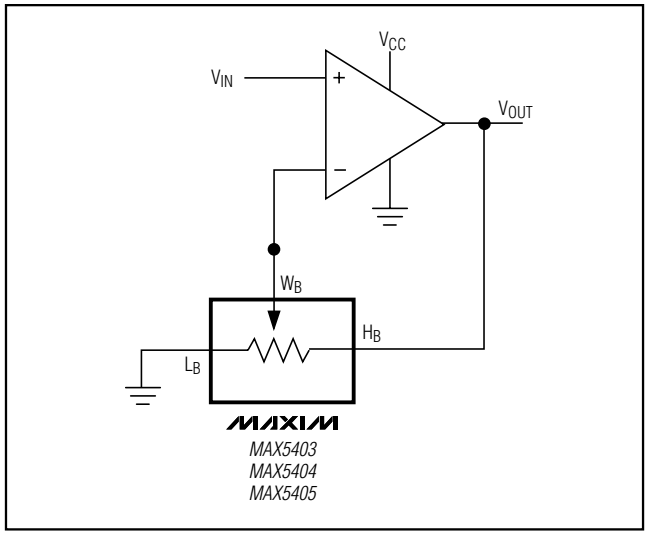


Figure 6b. Adjustable Gain Circuit Using: 3-Terminal Potentiometer

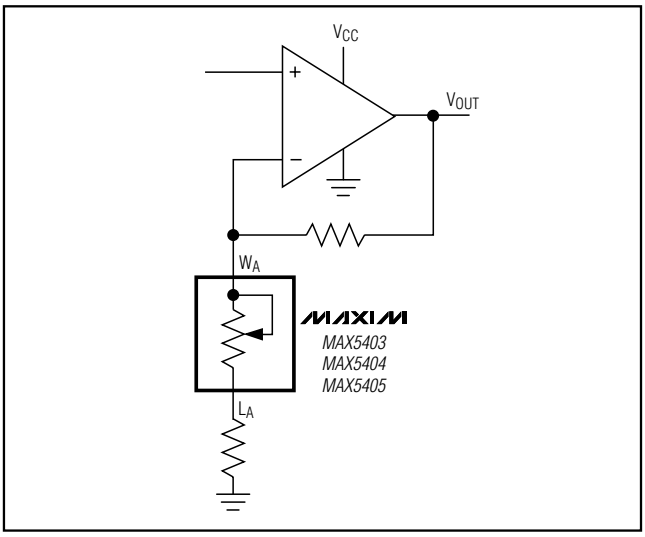


Figure 6a. Adjustable Gain Circuit Using: 2-Terminal Variable Resistor

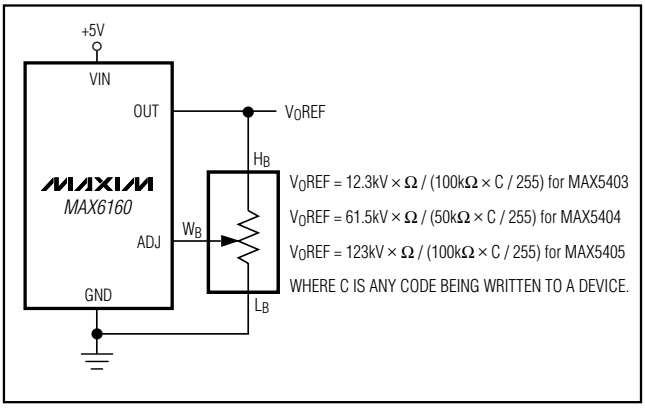


Figure 7a. Adjustable Voltage Reference Using: 3-Terminal Potentiometer

Adjustable Voltage Reference

In Figure 7a, the MAX5403/MAX5404/MAX5405 is shown with the MAX6160 to make an adjustable voltage reference. In this circuit, the H_X pin of the MAX5403/MAX5404/MAX5405 is connected to the OUT pin of the MAX6160, the L_X pin of the MAX5403/MAX5404/MAX5405 is connected to GND, and the W_X pin of the MAX5403/MAX5404/MAX5405 is connected to the ADJ pin of the MAX6160. The MAX5403/MAX5404/MAX5405 allow precise setting of the voltage reference output. A low 5ppm/°C ratiometric tempco allows a very stable, adjustable voltage over temperature.

Dual 256-Tap, Low-Drift, Digital Potentiometers in 10-µMAX

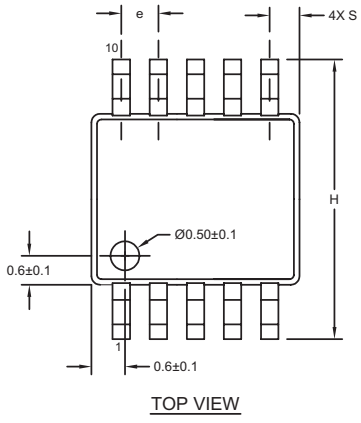
Chip Information

TRANSISTOR COUNT: 8689

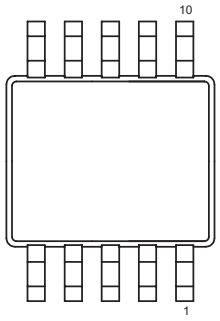
PROCESS: BiCMOS

Package Information

10LUMAX.EPS

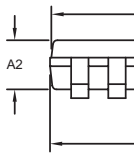


TOP VIEW

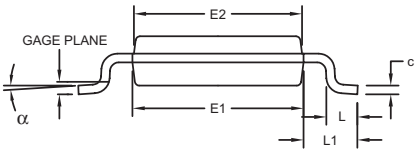


BOTTOM VIEW

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	-	0.043	-	1.10
A1	0.002	0.006	0.05	0.15
A2	0.030	0.037	0.75	0.95
D1	0.116	0.120	2.95	3.05
D2	0.114	0.118	2.89	3.00
E1	0.116	0.120	2.95	3.05
E2	0.114	0.118	2.89	3.00
H	0.187	0.199	4.75	5.05
L	0.0157	0.0275	0.40	0.70
L1	0.037 REF		0.940 REF	
b	0.007	0.0106	0.177	0.270
e	0.0197 BSC		0.500 BSC	
c	0.0035	0.0078	0.090	0.200
S	0.0196 REF		0.498 REF	
α	0°	6°	0°	6°



FRONT VIEW



SIDE VIEW

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15mm (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187C-BA.

DALLAS SEMICONDUCTOR MAXIM

PROPRIETARY INFORMATION

TITLE: PACKAGE OUTLINE, 10L uMAX/uSOP

APPROVAL	DOCUMENT CONTROL NO.	REV.	
	21-0061	I	1/1

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