

MAX31855

Cold-Junction Compensated Thermocouple-to-Digital Converter

General Description

The MAX31855 performs cold-junction compensation and digitizes the signal from a K, J, N, T, or E type thermocouple. (Contact the factory for S and R type thermocouples.) The data is output in a signed 14-bit, SPI™-compatible, read-only format. This converter resolves temperatures to 0.25°C, allows readings as high as +1800°C and as low as -270°C, and exhibits thermocouple accuracy of ±2°C for temperatures ranging from -200°C to +700°C for K-type thermocouples. For full range accuracies and other thermocouple types, see the [Thermal Characteristics](#) specifications.

Applications

Industrial
Appliances
HVAC
Automotive

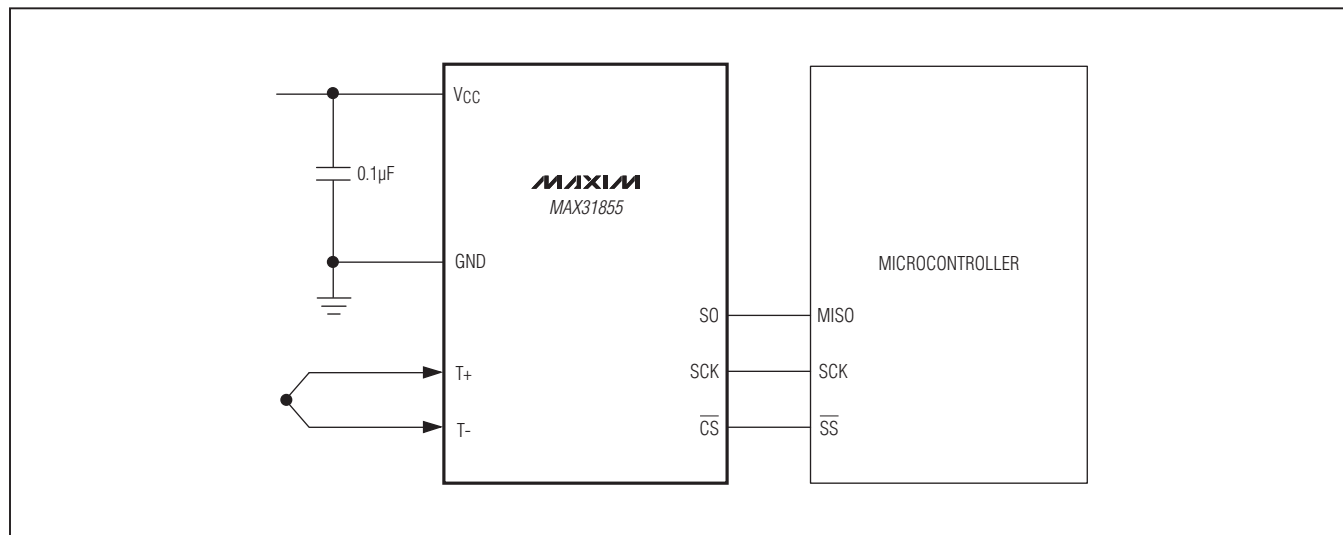
Features

- ◆ Cold-Junction Compensation
- ◆ 14-Bit, 0.25°C Resolution
- ◆ Versions Available for K, J, N, T, and E Type Thermocouples (Contact Factory for S and R Type Availability) (see [Table 1](#))
- ◆ Simple SPI-Compatible Interface (Read-Only)
- ◆ Detects Thermocouple Shorts to GND or VCC
- ◆ Detects Open Thermocouple

[Ordering Information](#) appears at end of data sheet.

For related parts and recommended products to use with this part, refer to: www.maxim-ic.com/MAX31855.related

Typical Application Circuit



SPI is a trademark of Motorola, Inc.

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ABSOLUTE MAXIMUM RATINGS

Supply Voltage Range (V_{CC} to GND).....-0.3V to +4.0V
 All Other Pins..... -0.3V to ($V_{CC} + 0.3$ V)
 Continuous Power Dissipation ($T_A = +70^{\circ}\text{C}$)
 SO (derate 5.9mW/ $^{\circ}\text{C}$ above $+70^{\circ}\text{C}$).....470.6mW
 ESD Protection (All Pins, Human Body Model)..... $\pm 2000\text{kV}$

Operating Temperature Range..... -40°C to $+125^{\circ}\text{C}$
 Junction Temperature $+150^{\circ}\text{C}$
 Storage Temperature Range -65°C to $+150^{\circ}\text{C}$
 Lead Temperature (soldering, 10s) $+300^{\circ}\text{C}$
 Soldering Temperature (reflow) $+260^{\circ}\text{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

PACKAGE THERMAL CHARACTERISTICS (Note 1)

SO

Junction-to-Ambient Thermal Resistance (θ_{JA}) $170^{\circ}\text{C}/\text{W}$
 Junction-to-Case Thermal Resistance (θ_{JC}) $40^{\circ}\text{C}/\text{W}$

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to www.maxim-ic.com/thermal-tutorial.

RECOMMENDED OPERATING CONDITIONS*

($T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Power-Supply Voltage	V_{CC}	(Note 2)	3.0	3.3	3.6	V
Input Logic 0	V_{IL}		-0.3		+0.8	V
Input Logic 1	V_{IH}		2.1		$V_{CC} + 0.3$	V

DC ELECTRICAL CHARACTERISTICS*

($3.0\text{V} \leq V_{CC} \leq 3.6\text{V}$, $T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Power-Supply Current	I_{CC}			900	1500	μA
Thermocouple Input Bias Current		$T_A = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, 100mV across the thermocouple inputs	-100		+100	nA
Power-Supply Rejection				-0.3		$^{\circ}\text{C}/\text{V}$
Power-On Reset Voltage Threshold	V_{POR}	(Note 3)		2	2.5	V
Power-On Reset Voltage Hysteresis				0.2		V
Output High Voltage	V_{OH}	$I_{OUT} = -1.6\text{mA}$	$V_{CC} - 0.4$			V
Output Low Voltage	V_{OL}	$I_{OUT} = 1.6\text{mA}$			0.4	V

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THERMAL CHARACTERISTICS*

(3.0V ≤ V_{CC} ≤ 3.6V, T_A = -40°C to +125°C, unless otherwise noted.) (Note 4)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
MAX31855K Thermocouple Temperature Gain and Offset Error (41.276μV/°C nominal sensitivity) (Note 4)		T _{THERMOCOUPLE} = -200°C to +700°C, T _A = -20°C to +85°C (Note 3)	-2		+2	°C
		T _{THERMOCOUPLE} = +700°C to +1350°C, T _A = -20°C to +85°C (Note 3)	-4		+4	
		T _{THERMOCOUPLE} = -200°C to +1350°C, T _A = -40°C to +125°C (Note 3)	-6		+6	
MAX31855J Thermocouple Temperature Gain and Offset Error (57.953μV/°C nominal sensitivity) (Note 4)		T _{THERMOCOUPLE} = -40°C to +750°C, T _A = -20°C to +85°C (Note 3)	-2		+2	°C
		T _{THERMOCOUPLE} = -40°C to +750°C, T _A = -40°C to +125°C (Note 3)	-4		+4	
MAX31855N Thermocouple Temperature Gain and Offset Error (36.256μV/°C nominal sensitivity) (Note 4)		T _{THERMOCOUPLE} = -200°C to +700°C, T _A = -20°C to +85°C (Note 3)	-2		+2	°C
		T _{THERMOCOUPLE} = +700°C to +1300°C, T _A = -20°C to +85°C (Note 3)	-4		+4	
		T _{THERMOCOUPLE} = -200°C to +1300°C, T _A = -40°C to +125°C (Note 3)	-6		+6	
MAX31855T Thermocouple Temperature Gain and Offset Error (52.18μV/°C nominal sensitivity) (Note 4)		T _{THERMOCOUPLE} = -250°C to +400°C, T _A = -20°C to +85°C (Note 3)	-2		+2	°C
		T _{THERMOCOUPLE} = -250°C to +400°C, T _A = -40°C to +125°C (Note 3)	-4		+4	
MAX31855E Thermocouple Temperature Gain and Offset Error (76.373μV/°C nominal sensitivity) (Note 4)		T _{THERMOCOUPLE} = -40°C to +700°C, T _A = -20°C to +85°C (Note 3)	-2		+2	°C
		T _{THERMOCOUPLE} = +700°C to +900°C, T _A = -20°C to +85°C (Note 3)	-3		+3	
		T _{THERMOCOUPLE} = -40°C to +900°C, T _A = -40°C to +125°C (Note 3)	-5		+5	
Thermocouple Temperature Data Resolution				0.25		°C
Internal Cold-Junction Temperature Error		T _A = -20°C to +85°C (Note 3)	-2		+2	°C
		T _A = -40°C to +125°C (Note 3)	-3		+3	
Cold-Junction Temperature Data Resolution		T _A = -40°C to +125°C		0.0625		°C
Temperature Conversion Time (Thermocouple, Cold Junction, Fault Detection)	t _{CONV}	(Note 5)		70	100	ms
Thermocouple Conversion Power-Up Time	t _{CONV_PU}	(Note 6)	200			ms

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SERIAL-INTERFACE TIMING CHARACTERISTICS*

(See [Figure 1](#) and [Figure 2](#).)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Leakage Current	I_{LEAK}	(Note 7)	-1		+1	μA
Input Capacitance	C_{IN}			8		pF
Serial-Clock Frequency	f_{SCL}				5	MHz
SCK Pulse-High Width	t_{CH}		100			ns
SCK Pulse-Low Width	t_{CL}		100			ns
SCK Rise and Fall Time					200	ns
$\overline{CS}$ Fall to SCK Rise	t_{CSS}		100			ns
SCK to $\overline{CS}$ Hold			100			ns
$\overline{CS}$ Fall to Output Enable	t_{DV}				100	ns
$\overline{CS}$ Rise to Output Disable	t_{TR}				40	ns
SCK Fall to Output Data Valid	t_{DO}				40	ns
$\overline{CS}$ Inactive Time		(Note 3)	200			ns

Note 2: All voltages are referenced to GND. Currents entering the IC are specified positive, and currents exiting the IC are negative.

Note 3: Guaranteed by design; not production tested.

Note 4: Not including cold-junction temperature error or thermocouple nonlinearity.

Note 5: Specification is 100% tested at $T_A = +25^\circ C$. Specification limits over temperature ($T_A = T_{MIN}$ to T_{MAX}) are guaranteed by design and characterization; not production tested.

Note 6: Because the thermocouple temperature conversions begin at V_{POR} , depending on V_{CC} slew rates, the first thermocouple temperature conversion may not produce an accurate result. Therefore, the t_{CONV_PU} specification is required after V_{CC} is greater than V_{CCMIN} to guarantee a valid thermocouple temperature conversion result.

Note 7: For all pins except T+ and T- (see the Thermocouple Input Bias Current parameter in the [DC Electrical Characteristics](#) table).

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Serial-Interface Diagrams

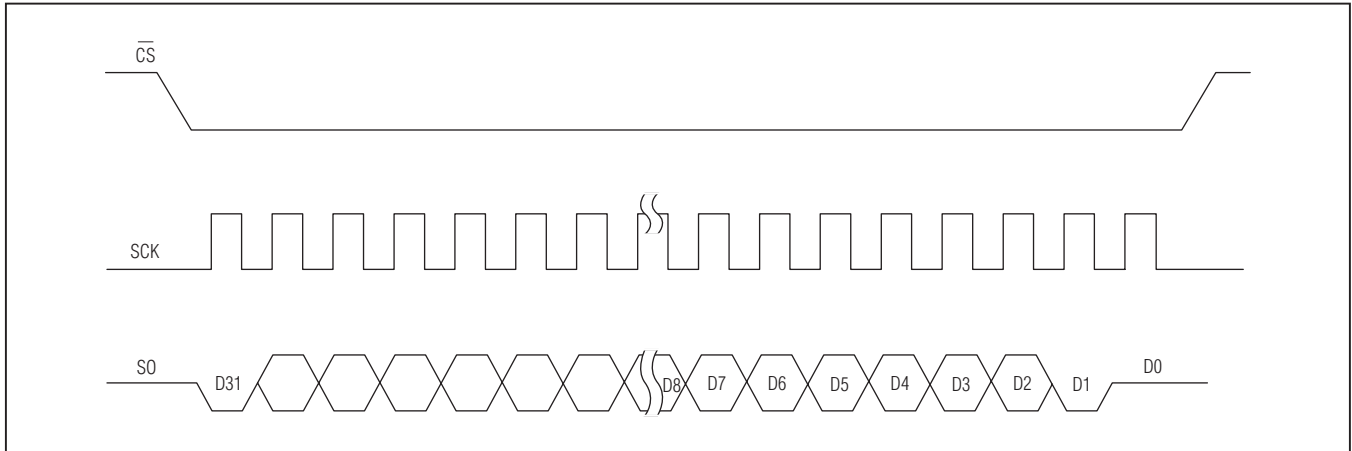


Figure 1. Serial-Interface Protocol

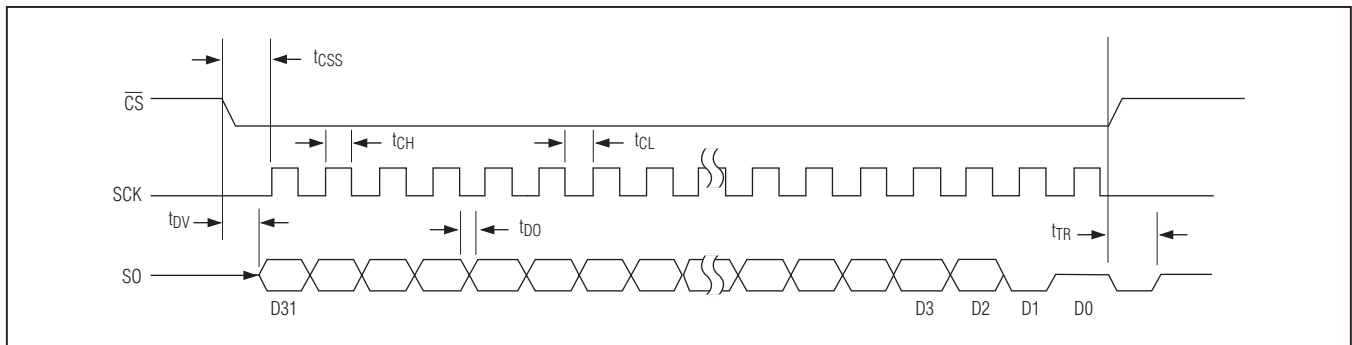
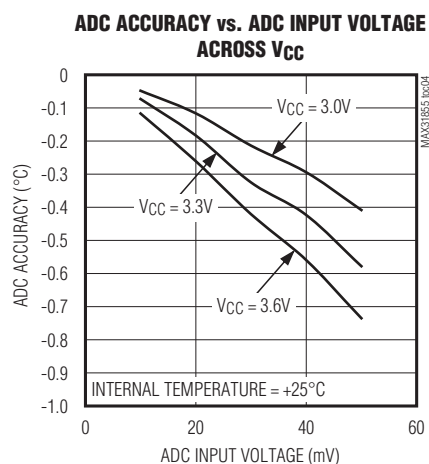
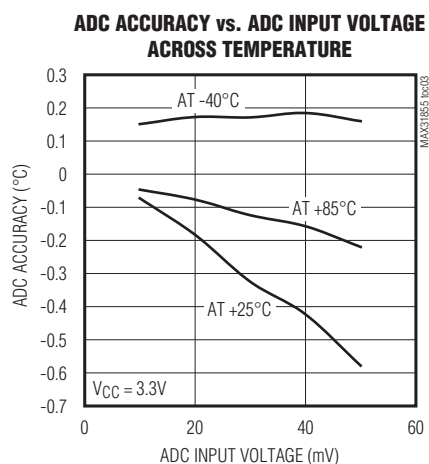
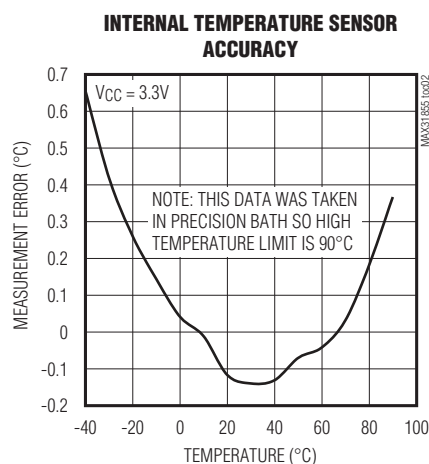
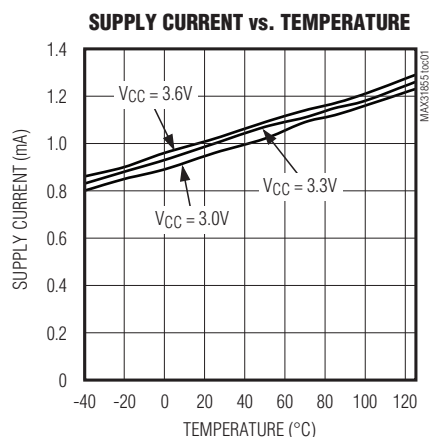


Figure 2. Serial-Interface Timing

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Typical Operating Characteristics

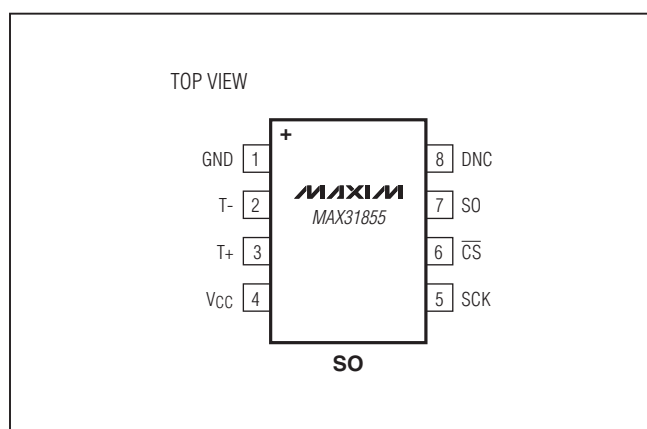
($V_{CC} = +3.3V$, $T_A = +25^\circ C$, unless otherwise noted.)



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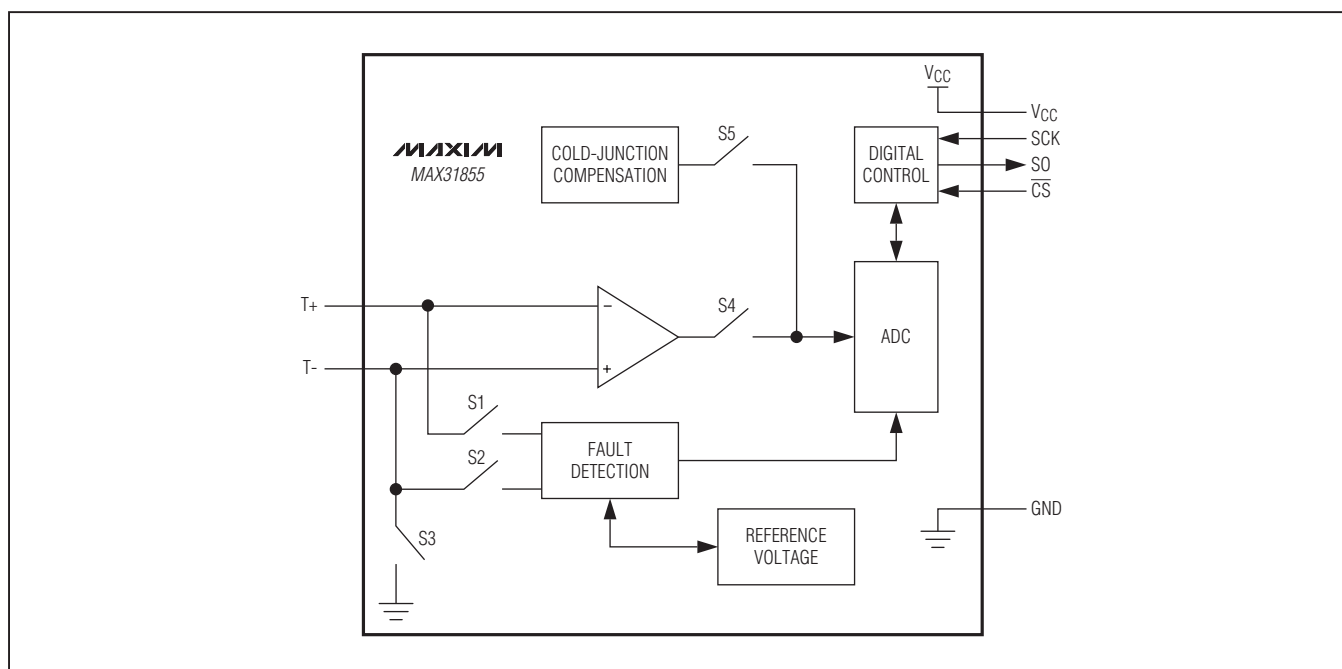
Pin Configuration



Pin Description

PIN	NAME	FUNCTION
1	GND	Ground
2	T-	Thermocouple Input. See Table 1. Do not connect to GND.
3	T+	Thermocouple Input. See Table 1.
4	V _{CC}	Power-Supply Voltage
5	SCK	Serial-Clock Input
6	$\overline{CS}$	Active-Low Chip Select. Set $\overline{CS}$ low to enable the serial interface.
7	SO	Serial-Data Output
8	DNC	Do Not Connect

Block Diagram



Cold-Junction Compensated Thermocouple-to-Digital Converter

Detailed Description

The MAX31855 is a sophisticated thermocouple-to-digital converter with a built-in 14-bit analog-to-digital converter (ADC). The device also contains cold-junction compensation sensing and correction, a digital controller, an SPI-compatible interface, and associated control logic. The device is designed to work in conjunction with an external microcontroller (μC) in thermostatic, process-control, or monitoring applications. The device is available in several versions, each optimized and trimmed for a specific thermocouple type (K, J, N, T, or E; contact the factory for S and R types). The thermocouple type is indicated in the suffix of the part number (e.g., MAX31855K). See the [Ordering Information](#) table for all options.

Temperature Conversion

The device includes signal-conditioning hardware to convert the thermocouple's signal into a voltage compatible with the input channels of the ADC. The T+ and T- inputs connect to internal circuitry that reduces the introduction of noise errors from the thermocouple wires. Before converting the thermoelectric voltages into equivalent temperature values, it is necessary to compensate

for the difference between the thermocouple cold-junction side (device ambient temperature) and a 0°C virtual reference. For a K type thermocouple, the voltage changes by about $41\mu\text{V}/^{\circ}\text{C}$, which approximates the thermocouple characteristic with the following linear equation:

$$V_{\text{OUT}} = (41.276\mu\text{V}/^{\circ}\text{C}) \times (T_{\text{R}} - T_{\text{AMB}})$$

where V_{OUT} is the thermocouple output voltage (μV), T_{R} is the temperature of the remote thermocouple junction ($^{\circ}\text{C}$), and T_{AMB} is the temperature of the device ($^{\circ}\text{C}$).

Other thermocouple types use a similar straight-line approximation but with different gain terms. Note that the MAX31855 assumes a linear relationship between temperature and voltage. Because all thermocouples exhibit some level of nonlinearity, apply appropriate correction to the device's output data.

Cold-Junction Compensation

The function of the thermocouple is to sense a difference in temperature between two ends of the thermocouple wires. The thermocouple's "hot" junction can be read across the operating temperature range ([Table 1](#)). The reference junction, or "cold" end (which should be at

Table 1. Thermocouple Wire Connections and Nominal Sensitivities

TYPE	T- WIRE	T+ WIRE	TEMP RANGE ($^{\circ}\text{C}$)	SENSITIVITY ($\mu\text{V}/^{\circ}\text{C}$)	COLD-JUNCTION SENSITIVITY ($\mu\text{V}/^{\circ}\text{C}$) (0°C TO $+70^{\circ}\text{C}$)
K	Alumel	Chromel	-200 to $+1350$	41.276 (0°C to $+1000^{\circ}\text{C}$)	40.73
J	Constantan	Iron	-40 to $+750$	57.953 (0°C to $+750^{\circ}\text{C}$)	52.136
N	Nisil	Nicrosil	-200 to $+1300$	36.256 (0°C to $+1000^{\circ}\text{C}$)	27.171
S*	Platinum/ Rhodium	Platinum	$+50$ to $+1600$	9.587 (0°C to $+1000^{\circ}\text{C}$)	6.181
T	Constantan	Copper	-250 to $+400$	52.18 (0°C to $+400^{\circ}\text{C}$)	41.56
E	Constantan	Chromel	-40 to $+900$	76.373 (0°C to $+1000^{\circ}\text{C}$)	44.123
R*	Platinum/ Rhodium	Platinum	-50 to $+1770$	10.506 (0°C to $+1000^{\circ}\text{C}$)	6.158

*Contact factory.

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the same temperature as the board on which the device is mounted) can range from -55°C to $+125^{\circ}\text{C}$. While the temperature at the cold end fluctuates, the device continues to accurately sense the temperature difference at the opposite end.

The device senses and corrects for the changes in the reference junction temperature with cold-junction compensation. It does this by first measuring its internal die temperature, which should be held at the same temperature as the reference junction. It then measures the voltage from the thermocouple's output at the reference junction and converts this to the noncompensated thermocouple temperature value. This value is then added to the device's die temperature to calculate the thermocouple's "hot junction" temperature. Note that the "hot junction" temperature can be lower than the cold junction (or reference junction) temperature.

Optimal performance from the device is achieved when the thermocouple cold junction and the device are at the same temperature. Avoid placing heat-generating devices or components near the MAX31855 because this could produce cold-junction-related errors.

Conversion Functions

During the conversion time, t_{CONV} , three functions are performed: the temperature conversion of the internal cold-junction temperature, the temperature conversion of the external thermocouple, and the detection of thermocouple faults.

When executing the temperature conversion for the internal cold-junction compensation circuit, the connection to signal from the external thermocouple is opened (switch S4) and the connection to the cold-junction compensation circuit is closed (switch S5). The internal T- reference to ground is still maintained (switch S3 is closed) and the connections to the fault-detection circuit are open (switches S1 and S2).

When executing the temperature conversion of the external thermocouple, the connections to the internal fault-detection circuit are opened (switches S1 and S2 in the [Block Diagram](#)) and the switch connecting the cold-junction compensation circuit is opened (switch S5). The internal ground reference connection (switch S3) and the connection to the ADC (switch S4) are closed. This allows the ADC to process the voltage detected across the T+ and T- terminals.

During fault detection, the connections from the external thermocouple and cold-junction compensation circuit to the ADC are opened (switches S4 and S5). The internal ground reference on T- is also opened (switch S3). The connections to the internal fault-detection circuit are closed (switch S1 and S2). The fault-detection circuit tests for shorted connections to VCC or GND on the T+ and T- inputs, as well as looking for an open thermocouple condition. Bits D0, D1, and D2 of the output data are normally low. Bit D2 goes high to indicate a thermocouple short to VCC, bit D1 goes high to indicate a thermocouple short to GND, and bit D0 goes high to indicate a thermocouple open circuit. If any of these conditions exists, bit D16 of the SO output data, which is normally low, also goes high to indicate that a fault has occurred.

Serial Interface

The [Typical Application Circuit](#) shows the device interfaced with a microcontroller. In this example, the device processes the reading from the thermocouple and transmits the data through a serial interface. Drive $\overline{\text{CS}}$ low and apply a clock signal at SCK to read the results at SO. Conversions are always being performed in the background. The fault and temperature data are only be updated when $\overline{\text{CS}}$ is high.

Drive $\overline{\text{CS}}$ low to output the first bit on the SO pin. A complete serial-interface read of the cold-junction compensated thermocouple temperature requires 14 clock cycles. Thirty-two clock cycles are required to read both the thermocouple and reference junction temperatures ([Table 2](#) and [Table 3](#).) Read the output bits on the falling edge of the clock. The first bit, D31, is the thermocouple temperature sign bit. Bits D[30:18] contain the converted temperature in the order of MSB to LSB. Bit D16 is normally low and goes high when the thermocouple input is open or shorted to GND or VCC. The reference junction temperature data begins with D15. $\overline{\text{CS}}$ can be taken high at any point while clocking out conversion data.

[Figure 1](#) and [Figure 2](#) show the serial-interface timing and order. [Table 2](#) and [Table 3](#) show the SO output bit weights and functions.

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Table 2. Memory Map—Bit Weights and Functions

	14-BIT THERMOCOUPLE TEMPERATURE DATA				RES	FAULT BIT	12-BIT INTERNAL TEMPERATURE DATA				RES	SCV BIT	SCG BIT	OC BIT
BIT	D31	D30	...	D18	D17	D16	D15	D14	...	D4	D3	D2	D1	D0
VALUE	Sign	MSB 2^{10} (1024°C)	...	LSB 2^{-2} (0.25°C)	Reserved	1 = Fault	Sign	MSB 2^6 (64°C)	...	LSB 2^{-4} (0.0625°C)	Reserved	1 = Short to V_{CC}	1 = Short to GND	1 = Open Circuit

Table 3. Memory Map—Descriptions

BIT	NAME	DESCRIPTION
D[31:18]	14-Bit Thermocouple Temperature Data	These bits contain the signed 14-bit thermocouple temperature value. See Table 4 .
D17	Reserved	This bit always reads 0.
D16	Fault	This bit reads at 1 when any of the SCV, SCG, or OC faults are active. Default value is 0.
D[15:4]	12-Bit Internal Temperature Data	These bits contain the signed 12-bit value of the reference junction temperature. See Table 5 .
D3	Reserved	This bit always reads 0.
D2	SCV Fault	This bit is a 1 when the thermocouple is short-circuited to V_{CC} . Default value is 0.
D1	SCG Fault	This bit is a 1 when the thermocouple is short-circuited to GND. Default value is 0.
D0	OC Fault	This bit is a 1 when the thermocouple is open (no connections). Default value is 0.

Table 4. Thermocouple Temperature Data Format

TEMPERATURE (°C)	DIGITAL OUTPUT (D[31:18])
+1600.00	0110 0100 0000 00
+1000.00	0011 1110 1000 00
+100.75	0000 0110 0100 11
+25.00	0000 0001 1001 00
0.00	0000 0000 0000 00
-0.25	1111 1111 1111 11
-1.00	1111 1111 1111 00
-250.00	1111 0000 0110 00

Note: The practical temperature ranges vary with the thermocouple type.

Table 5. Reference Junction Temperature Data Format

TEMPERATURE (°C)	DIGITAL OUTPUT (D[15:4])
+127.0000	0111 1111 0000
+100.5625	0110 0100 1001
+25.0000	0001 1001 0000
0.0000	0000 0000 0000
-0.0625	1111 1111 1111
-1.0000	1111 1111 0000
-20.0000	1110 1100 0000
-55.0000	1100 1001 0000

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Applications Information

Noise Considerations

Because of the small signal levels involved, thermocouple temperature measurement is susceptible to power-supply coupled noise. The effects of power-supply noise can be minimized by placing a 0.1 μ F ceramic bypass capacitor close to the VCC pin of the device and to GND.

The input amplifier is a low-noise amplifier designed to enable high-precision input sensing. Keep the thermocouple and connecting wires away from electrical noise sources.

Thermal Considerations

Self-heating degrades the device's temperature measurement accuracy in some applications. The magnitude of the temperature errors depends on the thermal conductivity of the device package, the mounting technique, and the effects of airflow. Use a large ground plane to improve the device's temperature measurement accuracy.

The thermocouple system's accuracy can also be improved by following these precautions:

- Use the largest wire possible that does not shunt heat away from the measurement area.

- If a small wire is required, use it only in the region of the measurement, and use extension wire for the region with no temperature gradient.
- Avoid mechanical stress and vibration, which could strain the wires.
- When using long thermocouple wires, use a twisted pair extension wire.
- Avoid steep temperature gradients.
- Try to use the thermocouple wire well within its temperature rating.
- Use the proper sheathing material in hostile environments to protect the thermocouple wire.
- Use extension wire only at low temperatures and only in regions of small gradients.
- Keep an event log and a continuous record of thermocouple resistance.

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Ordering Information

PART	THERMOCOUPLE TYPE	MEASURED TEMP RANGE	PIN-PACKAGE
MAX31855KASA+	K	-200°C to +1350°C	8 SO
MAX31855KASA+T	K	-200°C to +1350°C	8 SO
MAX31855JASA+	J	-40°C to +750°C	8 SO
MAX31855JASA+T	J	-40°C to +750°C	8 SO
MAX31855NASA+	N	-200°C to +1300°C	8 SO
MAX31855NASA+T	N	-200°C to +1300°C	8 SO
MAX31855SASA+*	S	+50°C to +1600°C	8 SO
MAX31855SASA+T*	S	+50°C to +1600°C	8 SO
MAX31855TASA+	T	-250°C to +400°C	8 SO
MAX31855TASA+T	T	-250°C to +400°C	8 SO
MAX31855EASA+	E	-40°C to +900°C	8 SO
MAX31855EASA+T	E	-40°C to +900°C	8 SO
MAX31855RASA+*	R	-50°C to +1770°C	8 SO
MAX31855RASA+T*	R	-50°C to +1770°C	8 SO

Note: All devices are specified over the -40°C to +125°C operating temperature range.

+Denotes a lead(Pb)-free/RoHS-compliant package.

T = Tape and reel.

*Future product—contact factory for availability.

Package Information

For the latest package outline information and land patterns (footprints), go to www.maxim-ic.com/packages. Note that a “+”, “#”, or “-” in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8 SO	S8+4	21-0041	90-0096

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	3/11	Initial release	—

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600 _____ **13**