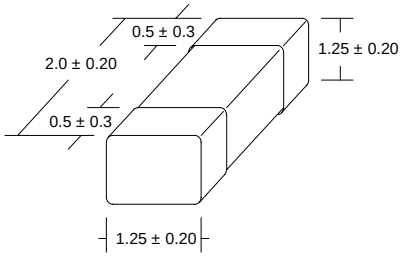
Surface Mount Multilayer Chip Inductor **(LSML-201212 Series)**

CALIBER ELECTRONICS INC.
specifications subject to change * revision 3-2003

Dimensions



(Not to scale)

Dimensions in mm

Part Numbering Guide

LSML - 201212 - 2R7 K - T

Dimensions
(Length, Width, Height)

Inductance Code

Packaging Style
B=Bulk
T=Tape & Reel
(3000 pcs per reel)

Tolerance
K=10%, M=20%

Features

Inductance Range	2.7 µH to 100 µH
Tolerance	10%, 20%
Operating Temperature	-25°C to +85°C

Electrical Specifications

Inductance Code	Inductance (µH)	Available Tolerance	Q Min	LQ Test Freq (MHz)	SRF Min (MHz)	DCR Max (Ohms)	IDC Max (mA)
2R7	2.7	K, M	40	10	45	0.75	30
3R3	3.3	K, M	40	10	41	0.80	30
3R9	3.9	K, M	40	10	38	0.90	30
4R7	4.7	K, M	40	10	35	1.00	30
5R6	5.6	K, M	45	4	32	0.90	15
6R8	6.8	K, M	45	4	29	1.00	15
8R2	8.2	K, M	45	4	26	1.10	15
100	10	K, M	45	2	24	1.15	15
120	12	K, M	45	2	22	1.25	15
150	15	K, M	30	1	19	0.80	5
180	18	K, M	30	1	18	0.90	5
220	22	K, M	30	1	16	1.10	5
270	27	K, M	30	1	14	1.15	5
330	33	K, M	30	0.4	13	1.25	5
390	39	K, M	35	2	8.0	2.90	4
470	47	K, M	35	2	7.5	3.00	4
560	56	K, M	35	2	7.0	3.10	4
680	68	K, M	25	1	6.5	2.90	2
820	82	K, M	25	1	6.0	3.00	2
101	100	K, M	25	1	5.5	3.10	2