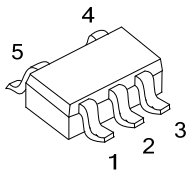
150mA LDO REGULATOR



SOT-23-5
(JEDEC TO-236)

■ DESCRIPTION

The UTC **LR9280** is a typical LDO (linear regulator) with the features of high output voltage accuracy, low supply current, low ON-resistance. Internally, there're many functions of UTC **LR9280** which can be seen in the block figure. There are a voltage reference unit, an error amplifier, resistor-net for voltage setting, a current limit circuit, and a chip enable circuit in each UTC **LR9280**.

The output voltage of these ICs is fixed with high accuracy. B version has a chip enable pin, therefore low consumption current standby mode can be realized with the pin.

■ FEATURES

* Output voltage accuracy (±2.0%)
* Output voltage Range (1.2V~3.6V)
* Dropout voltage (TYP=0.25V)(I_{OUT}=150mA 3.0V Output type)
* Line regulation (TYP=0.05%/V)
* Temperature-Drift Coefficient of Output Voltage (TYP=±100ppm/°C)
* Ceramic capacitors are recommended to be used with this IC (1μF)

■ ORDERING INFORMATION

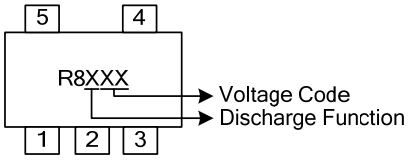
Ordering Number	Package	Packing
LR9280xG-xx-AE5-R	SOT-23-5	Tape Reel

Note: xx: Output Voltage, refer to Marking Information.

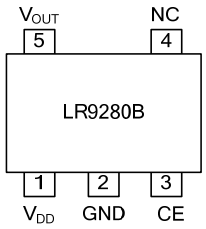
LR9280xG-xx-AE5-R (1)Packing Type (2)Package Type (3)Output Voltage Code (4)Green Package (5)Discharge Function	(1) R: Tape Reel (2) AE5: SOT-23-5 (3) xx: refer to Marking Information (4) G: Halogen Free and Lead Free (5) B: Active high type C: Without chip enable circuit

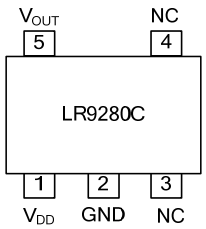
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■ MARKING INFORMATION

PACKAGE	VOLTAGE CODE	MARKING
SOT-23-5	12: 1.2V 15: 1.5V 18: 1.8V 28: 2.8V	

■ PIN CONFIGURATION



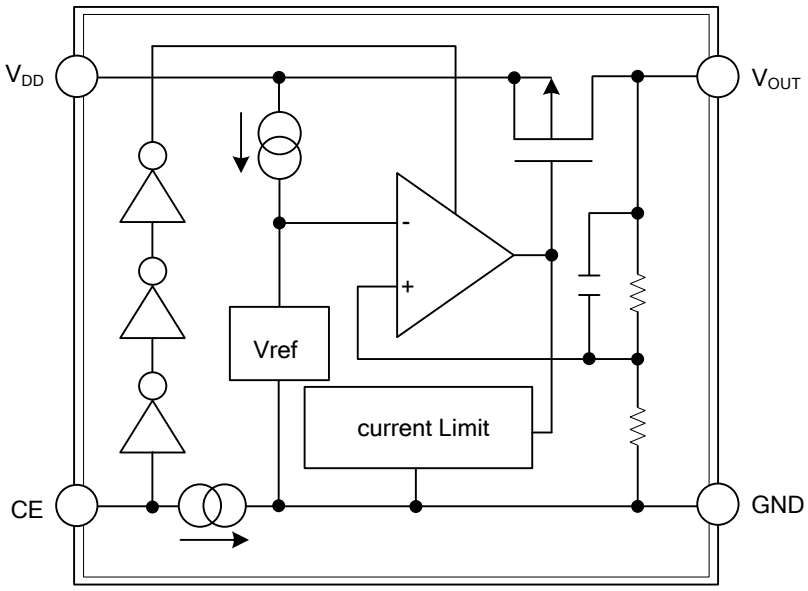


■ PIN DESCRIPTION

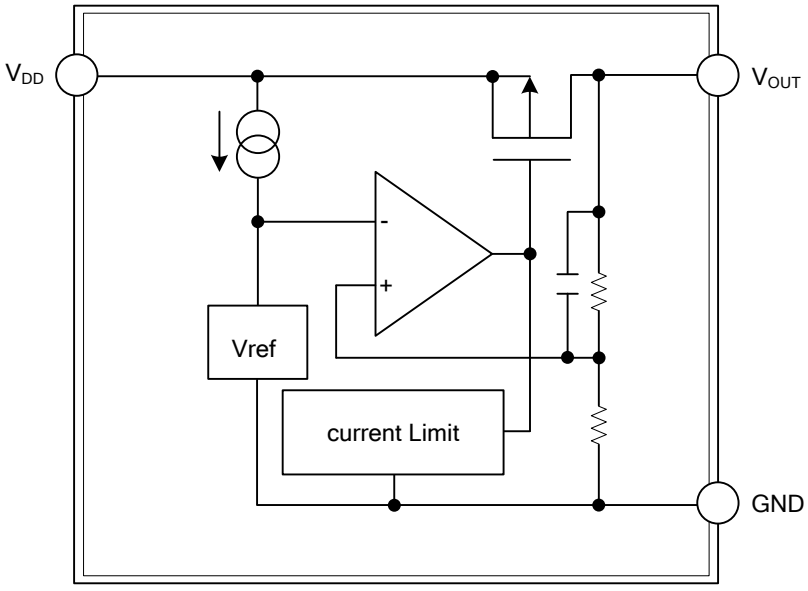
PIN NO.		PIN NAME	DESCRIPTION
LR9280B	LR9280C		
1	1	V_{DD}	Input pin
2	2	GND	Ground pin
3	-	CE	Chip Enable Pin
4	3, 4	NC	No Connection
5	5	V_{OUT}	Output pin

■ BLOCK DIAGRAM

LR9280B-xx



LR9280C-xx



■ ABSOLUTE MAXIMUM RATING

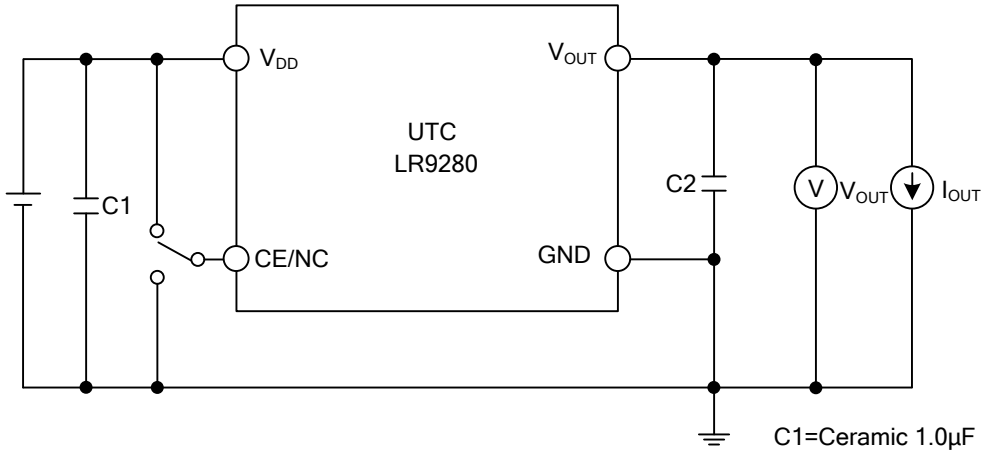
PARAMETER	SYMBOL	RATINGS	UNIT
Input Voltage	V_{IN}	6.5	V
Input Voltage(CE Pin)	V_{CE}	6.5	V
Output Voltage	V_{OUT}	-0.3~V_{IN}+0.3	V
Output Current	I_{OUT}	180	mA
Power Dissipation	P_D	420	mW
Operating Temperature	T_{OPR}	-40~85	°C
Storage Temperature	T_{STG}	-55~125	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

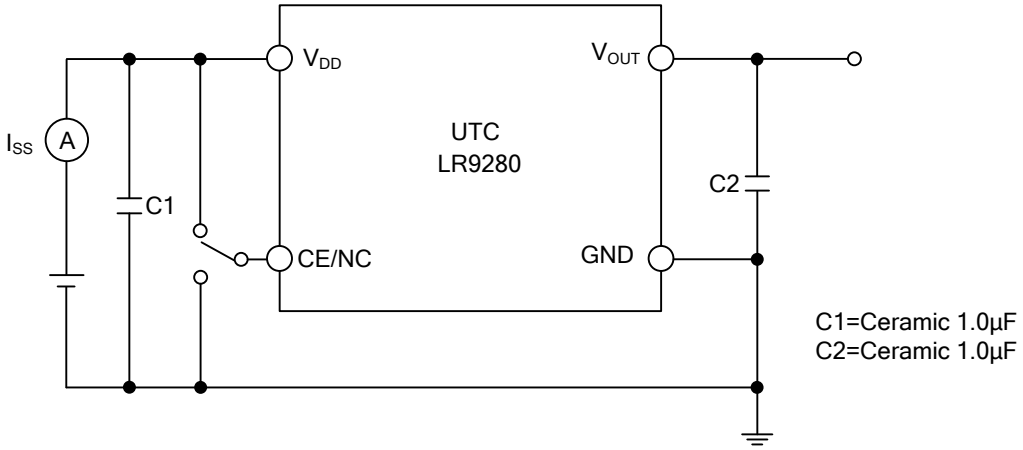
■ ELECTRICAL CHARACTERISTICS (T_A=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Output Voltage	V_{OUT}	V_{IN}=Set V_{OUT}+1V, 1μA ≤ I_{OUT}≤30mA		x0.980		x1.020	V
Output Current	I_{OUT}	V_{IN}−V_{OUT}=1.0V(V_{OUT} ≥1.5V) V_{IN}=2.4V(V_{OUT}<1.5V)		150			mA
Dropout Voltage	V_{DIF}	I_{OUT}=150mA	1.2≤V_{OUT}<1.3		0.85	1.20	V
			1.3≤V_{OUT}<1.4		0.75	1.10	V
			1.4≤V_{OUT}<1.5		0.65	1.00	V
			1.5≤V_{OUT}<1.7		0.60	0.90	V
			1.7≤V_{OUT}<1.9		0.50	0.75	V
			1.9≤V_{OUT}<2.1		0.40	0.65	V
			2.1≤V_{OUT}<2.8		0.35	0.55	V
			2.8≤V_{OUT}≤3.6		0.25	0.40	V
Input Voltage	V_{IN}			1.7		6.0	V
Supply Current	I_{SS}	V_{IN}-V_{OUT}=1.0V, I_{OUT}=0mA			0.7	1.5	μA
Standby Current	I_{STB}	V_{IN}−V_{OUT}=1.0V,V_{CE}=GND			0.1	1.0	μA
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	V_{IN}−V_{OUT}=1.0V(V_{OUT} ≥ 1.5V) V_{IN}=2.4V(V_{OUT}<1.5V) 1μA ≤I_{OUT}≤150mA			20	40	mV
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	I_{OUT}=30mA V_{OUT}+0.5V≤V_{IN} ≤ 6.0V (V_{OUT}≥1.5V), 2.0V≤V_{IN}≤6.0V (1.2V≤V_{OUT}≤1.4V)			0.05	0.20	%/V
Output Voltage Temperature Coefficient	$\Delta V_{OUT}/\Delta T_{OPT}$	I_{OUT}=30mA, -40°C≤T_{OPT}≤85°C			±100		ppm/°C
Short Current Limit	I_{SC}	V_{OUT}=0V			500		mA
CE Pull-down Constant Current	I_{PD}	LR9280B			0.35		μA
CE Input Voltage “H”	V_{CEH}	LR9280B		1.2		6.0	V
CE Input Voltage “L”	V_{CEL}	LR9280B		0.0		0.3	V

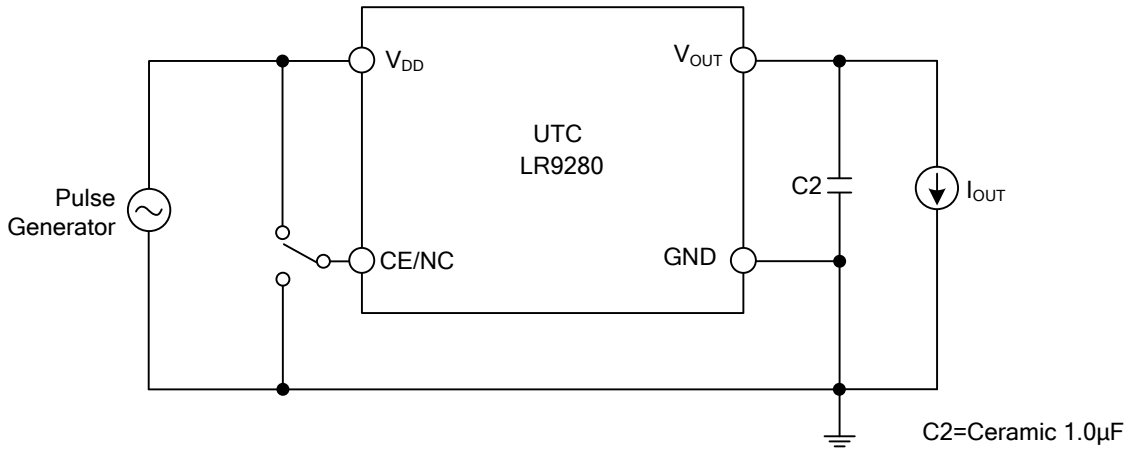
■ TEST CIRCUITS



Standard test Circuit

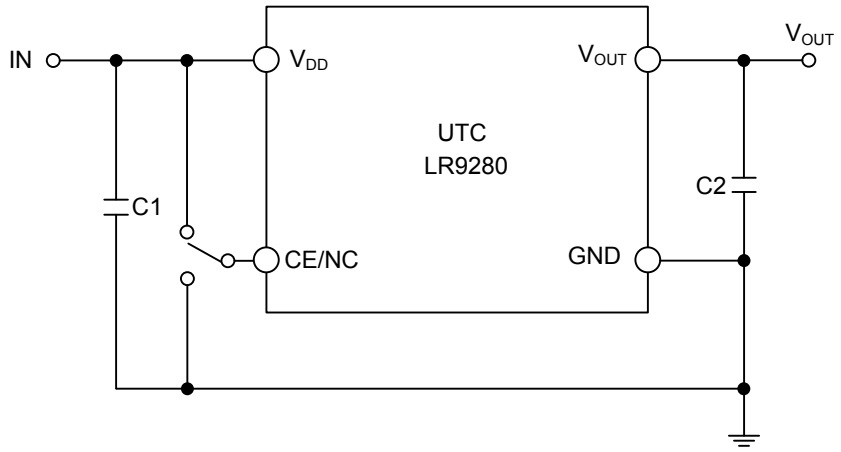


Supply Current Test Circuit



Ripple Rejection, Line Transient Response Test Circuit

■ TYPICAL APPLICATION CIRCUIT



(External Components)
Output Capacitor
Ceramic Capacitor 1μF

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