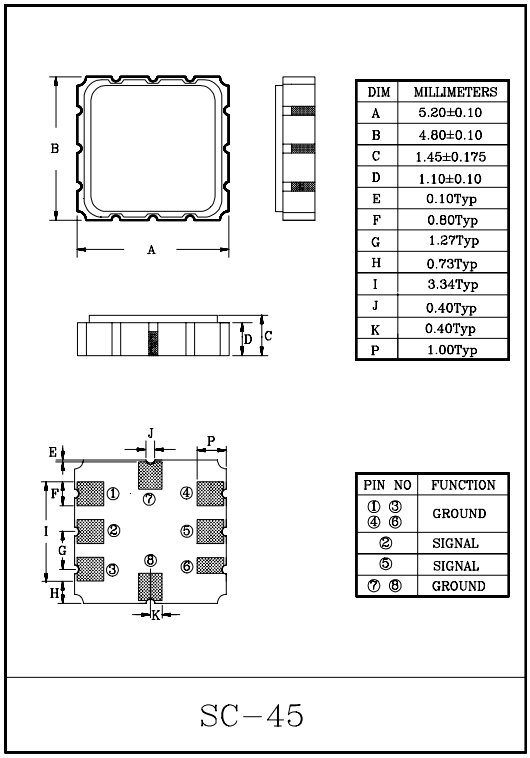
Band pass filters for the receiving RF circuits of pager

- High stability and reliability with good performance and no adjustment.
- Wide and sharp pass band characteristics.
- Low insertion loss and deep stop band attenuation for interference.
- Package is designed for SMD (Surface Mount Device) type.



DIM	MILLIMETERS
A	5.20±0.10
B	4.60±0.10
C	1.45±0.175
D	1.10±0.10
E	0.10Typ
F	0.80Typ
G	1.27Typ
H	0.73Typ
I	3.34Typ
J	0.40Typ
K	0.40Typ
P	1.00Typ

PIN NO	FUNCTION
① ③ ④ ⑥	GROUND
②	SIGNAL
⑤	SIGNAL
⑦ ⑧	GROUND

SC-45

MAXIMUM RATINGS (Ta=25℃)

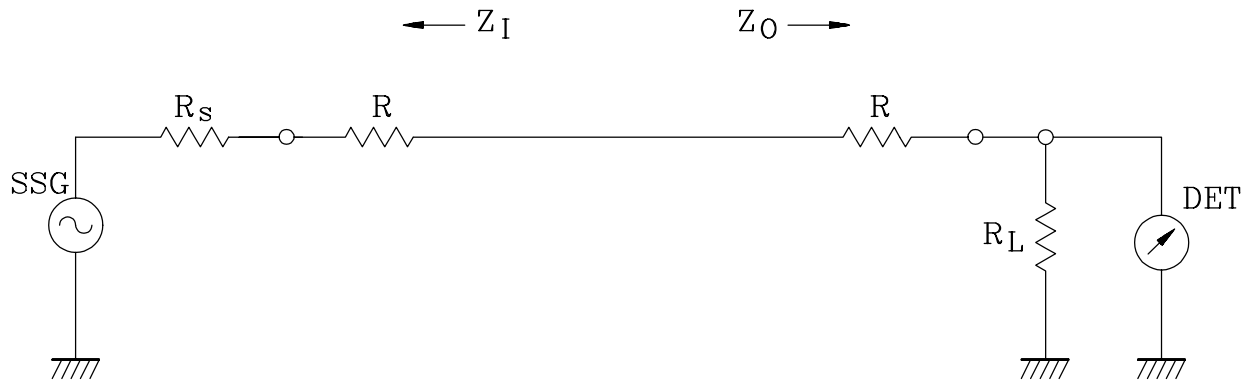
ITEM	SYMBOL	RATING	UNIT
Input Signal Level	IS_{max}	0	dBm
DC Permissive Voltage	V_{DC}	+10	V
Operating Temperature Range	T_{opr}	-10~+50	℃
Storage Temperature Range	T_{stg}	-30~+85	℃

ELECTRICAL CHARACTERISTICS (Temperature 20±2℃, Humidity 65±5%)

ITEMS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Nominal Center Frequency	fo	-	-	325.3	-	MHz
Bandwidth	BW_{3dB}	-	f_O±3.3	-	-	MHz
Insertion Loss	IL_{PASS}	f_O±3.3MHz	-	-	4.0	dB
Ripple Level	A_{RIP}	f_O±3.3MHz	-	-	2.0	dB
Rejection Level	IL_{STOP}	f_O-100~f_O-39.5MHz	50	-	-	dB
		f_O+39.5~f_O+100MHz	50	-	-	dB
Input/Output Impedance	$Z_I(Z_O)$	-	-	150Ω//0pF	-	-

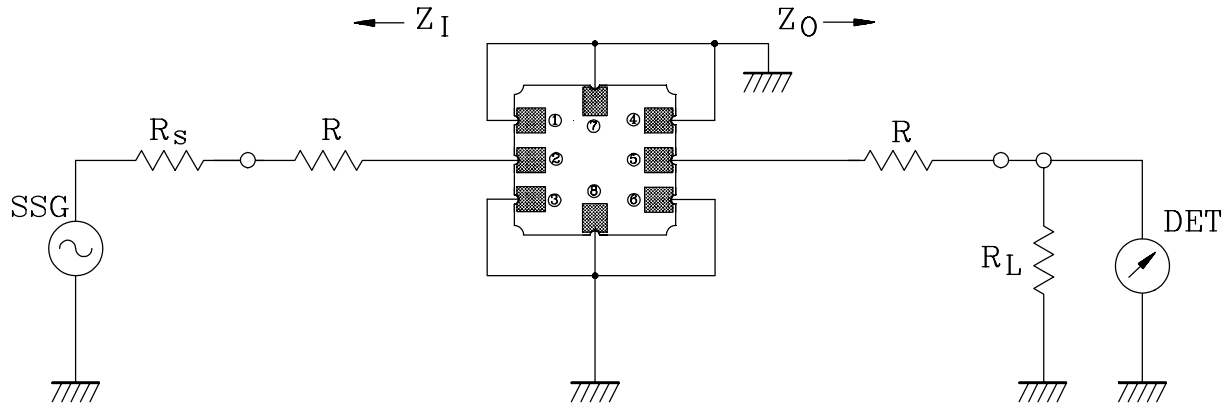
TEST CIRCUIT

REFERENCE LEVEL TEST CIRCUIT



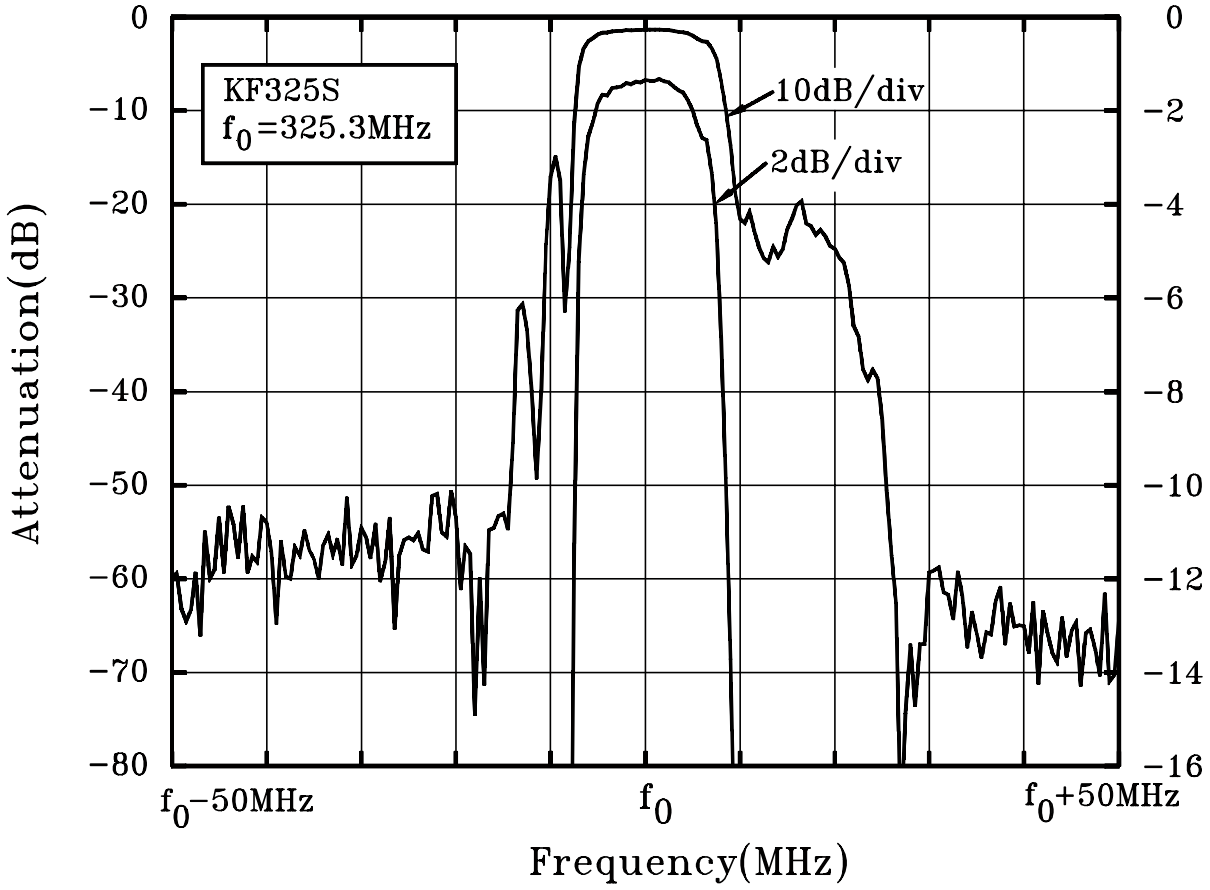
R_S, R_L : 50Ω (Internal Impedance of Source and Load)
R : 100Ω
$Z_I(Z_O)=R_S(R_L)+R$

MEASUREMENT CIRCUIT

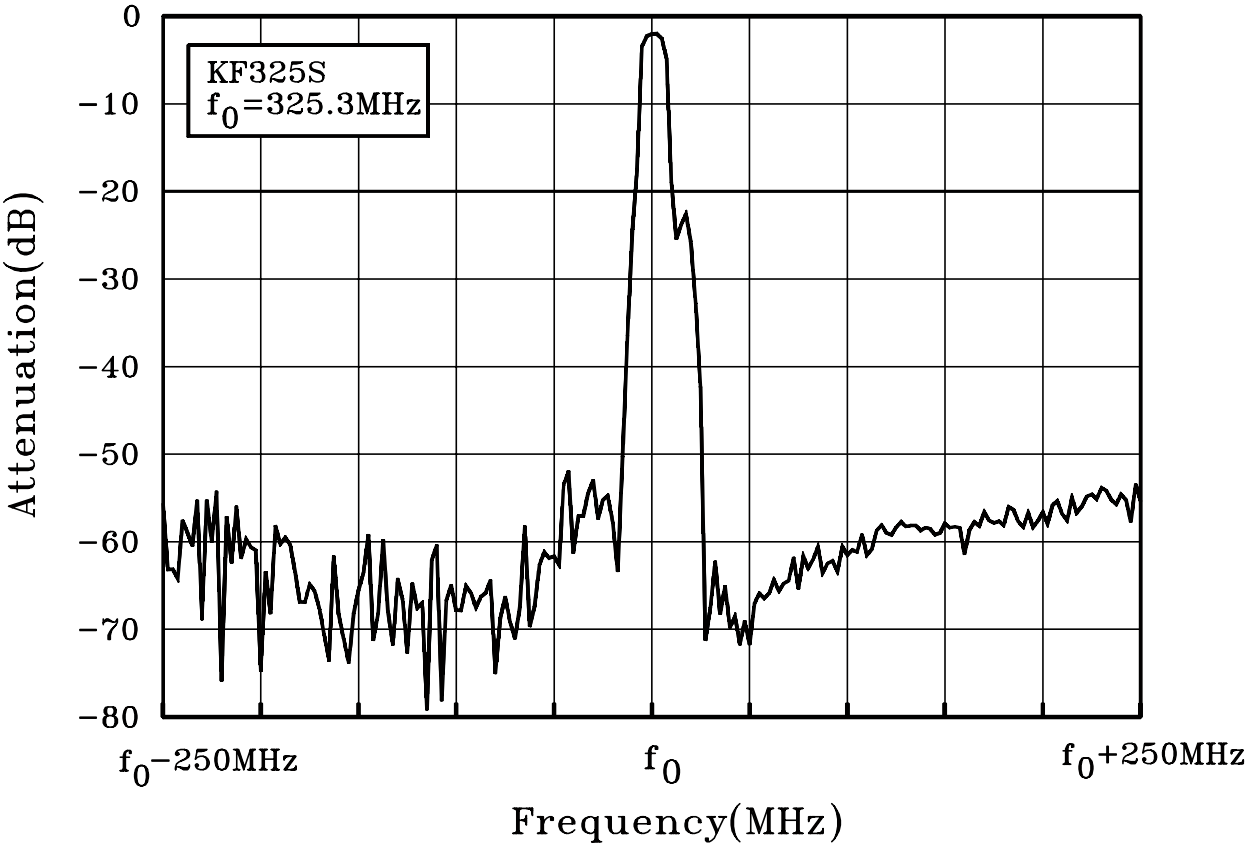


R_S, R_L : 50Ω (Internal Impedance of Source and Load)
R : 100Ω
$Z_I(Z_O)=R_S(R_L)+R$

ATT – f


KF325S
f_0=325.3MHz
10dB/div
2dB/div
Attenuation(dB)
0
−10
−20
−30
−40
−50
−60
−70
−80
0
−2
−4
−6
−8
−10
−12
−14
−16
f_0−50MHz
f_0
f_0+50MHz
Frequency(MHz)

ATT – f


KF325S
f_0=325.3MHz
Attenuation(dB)
0
−10
−20
−30
−40
−50
−60
−70
−80
f_0−250MHz
f_0
f_0+250MHz
Frequency(MHz)