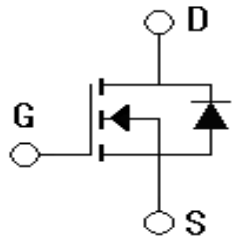
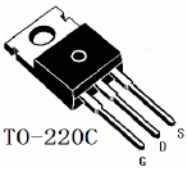
JCS6N90H

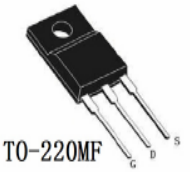
主要参数 MAIN CHARACTERISTICS

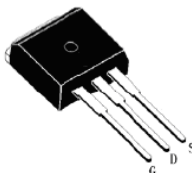
I_D	6 A
V_{DSS}	900 V
Rdson-max （@Vgs=10V）	3.0 Ω
Qg-typ	14 nC

封装 Package








TO-262

用途

- 高频开关电源
- 电子镇流器
- UPS 电源

APPLICATIONS

- High efficiency switch mode power supplies
- Electronic lamp ballasts based on half bridge
- UPS

产品特性

- 低栅极电荷
- 低 C_{rss} (典型值 9pF)
- 开关速度快
- 产品全部经过雪崩测试
- 高抗 dv/dt 能力
- RoHS 产品

FEATURES

- Low gate charge
- Low C_{rss} (typical 9pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS product

订货信息 ORDER MESSAGE

订 货 型 号 Order codes	印 记 Marking	封 装 Package	无卤素 Halogen Free	包 装 Packaging	器件重量 Device Weight
JCS6N90CH-O-C-N-B	JCS6N90CH	TO-220C	否 NO	条管 Tube	2.15 g(typ)
JCS6N90FH-O-F-N-B	JCS6N90FH	TO-220MF	否 NO	条管 Tube	2.20 g(typ)
JCS6N90BH-O-B-N-B	JCS6N90BH	TO-262	否 NO	条管 Tube	1.71 g(typ)

绝对最大额定值 ABSOLUTE RATINGS (Tc=25℃)

项 目 Parameter	符 号 Symbol	数 值 Value		单位 Unit
		JCS6N90CH/BH	JCS6N90FH	
最高漏极－源极直流电压 Drain-Source Voltage	V_{DSS}	900		V
连续漏极电流 Drain Current -continuous	I_D T=25℃	6	6*	A
	T=100℃	3.8	3.8*	A
最大脉冲漏极电流（注 1） Drain Current - pulse （note 1）	I_{DM}	24	24*	A
最高栅源电压 Gate-Source Voltage	V_{GSS}	±30		V
单脉冲雪崩能量（注 2） Single Pulsed Avalanche Energy（note 2）	E_{AS}	650		mJ
雪崩电流（注 1） Avalanche Current（note 1）	I_{AR}	6		A
重复雪崩能量（注 1） Repetitive Avalanche Current （note 1）	E_{AR}	16.7		mJ
二极管反向恢复最大电压变化速率（注 3） Peak Diode Recovery dv/dt （note 3）	dv/dt	4.5		V/ns
耗散功率 Power Dissipation	P_D T_C=25℃	167	58	W
	-Derate above 25℃	1.43	0.48	W/℃
最高结温及存储温度 Operating and Storage Temperature Range	T_J，T_{STG}	-55～+150		℃
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	T_L	300		℃

*漏极电流由最高结温限制

*Drain current limited by maximum junction temperature

电特性 ELECTRICAL CHARACTERISTICS

项　　目 Parameter	符　号 Symbol	测试条件 Tests conditions	最小 Min	典型 Typ	最大 Max	单位 Units
关态特性 Off –Characteristics						
漏－源击穿电压 Drain-Source Voltage	BV_{DSS}	I_D=250μA, V_{GS}=0V	900	-	-	V
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	I_D=250μA, referenced to 25℃	-	1.05	-	V/℃
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=900V,V_{GS}=0V, T_C=25℃	-	-	1	μA
		V_{DS}=720V, T_C=125℃	-	-	10	μA
正向栅极体漏电流 Gate-body leakage current, forward	I_{GSSF}	V_{DS}=0V, V_{GS} =30V	-	-	100	nA
反向栅极体漏电流 Gate-body leakage current, reverse	I_{GSSR}	V_{DS}=0V, V_{GS} =-30V	-	-	-100	nA
通态特性 On-Characteristics						
阈值电压 Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS} , I_D=250μA	3.0	-	5.0	V
静态导通电阻 Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS} =10V , I_D=3.0A	-	2.6	3.0	Ω
正向跨导 Forward Transconductance	g_{fs}	V_{DS} = 40V, I_D=3.0A（note 4）	-	5.6	-	S
动态特性 Dynamic Characteristics						
输入电容 Input capacitance	C_{iss}	V_{DS}=25V, V_{GS} =0V, f=1.0MHz	-	1320	1716	pF
输出电容 Output capacitance	C_{oss}		-	105	136	pF
反向传输电容 Reverse transfer capacitance	C_{rss}		-	9	12	pF

电特性 ELECTRICAL CHARACTERISTICS

开关特性 Switching Characteristics						
延迟时间 Turn-On delay time	t_d(on)	V_{DD}=450V,I_D=6A,R_G=25Ω （note 4，5）	-	34	75	ns
上升时间 Turn-On rise time	t_r		-	85	155	ns
延迟时间 Turn-Off delay time	t_d(off)		-	56	113	ns
下降时间 Turn-Off Fall time	t_f		-	59	118	ns
栅极电荷总量 Total Gate Charge	Q_g	V_{DS} =720V , I_D=6A V_{GS} =10V （note 4，5）	-	14	19	nC
栅－源电荷 Gate-Source charge	Q_{gs}		-	5	-	nC
栅－漏电荷 Gate-Drain charge	Q_{gd}		-	6	-	nC
漏－源二极管特性及最大额定值 **Drain-Source Diode Characteristics and Maximum Ratings**						
正向最大连续电流 Maximum Continuous Drain -Source Diode Forward Current	I_S		-	-	6	A
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}		-	-	24	A
正向压降 Drain-Source Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_S=6A	-	-	1.4	V
反向恢复时间 Reverse recovery time	t_{rr}	V_{GS}=0V, I_S=6A dI_F/dt=100A/μs (note 4)	-	625	-	ns
反向恢复电荷 Reverse recovery charge	Q_{rr}		-	6.71	-	μC

热特性 THERMAL CHARACTERISTIC

项 目 Parameter	符 号 Symbol	最大 Max		单 位 Unit
		JCS6N90CH/BH	JCS6N90FH	
结到管壳的热阻 Thermal Resistance, Junction to Case	$R_{th(j-c)}$	0.78	2.3	℃/W
结到环境的热阻 Thermal Resistance, Junction to Ambient	$R_{th(j-A)}$	62.5	62.5	℃/W

注释：

1：脉冲宽度由最高结温限制

2：L=2.0mH, I_{AS}=6A, V_{DD}=50V, R_G=25 Ω,起始结温 T_J=25℃

3：I_{SD} ≤6A,di/dt ≤200A/μs,VDD≤BV_{DSS},起始结温 T_J=25℃

4：脉冲测试：脉冲宽度≤300μs,占空比≤2%

5：基本与工作温度无关

Notes:

1：Pulse width limited by maximum junction temperature

2：L=2.0mH, I_{AS}=6A, V_{DD}=50V, R_G=25 Ω,Starting T_J=25℃

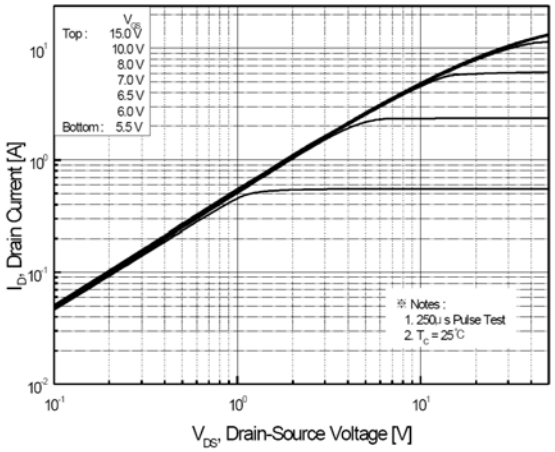
3：I_{SD} ≤6A,di/dt ≤200A/μs,VDD≤BV_{DSS}, Starting T_J=25℃

4：Pulse Test：Pulse Width ≤300μs,Duty Cycle≤2%

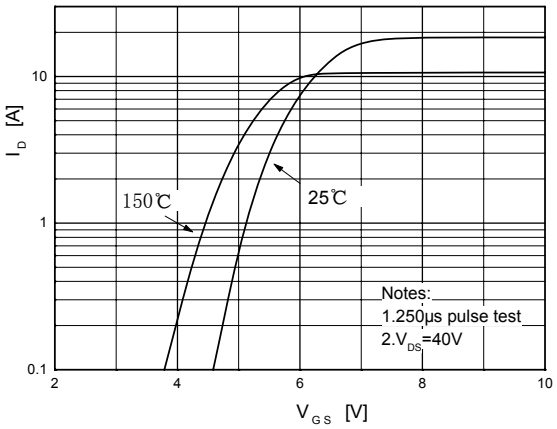
5：Essentially independent of operating temperature

特征曲线 ELECTRICAL CHARACTERISTICS (curves)

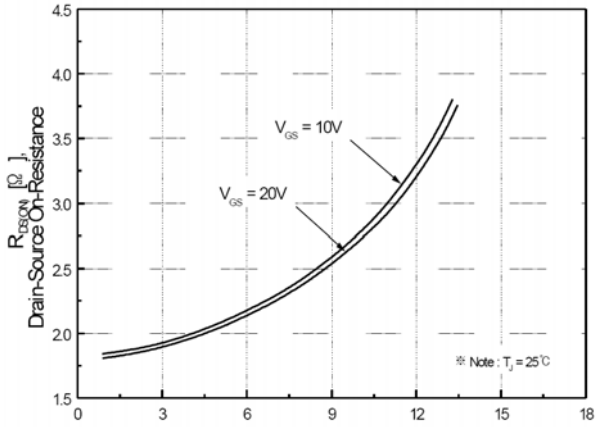
On-Region Characteristics



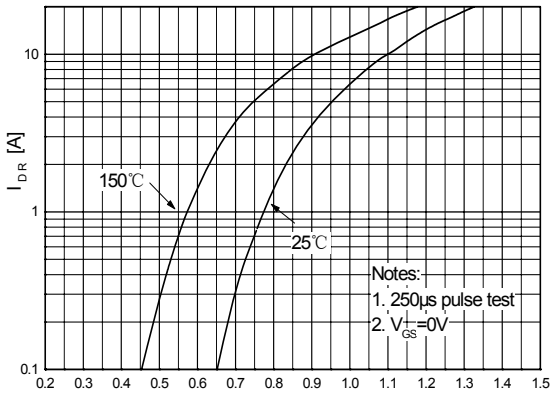
Transfer Characteristics



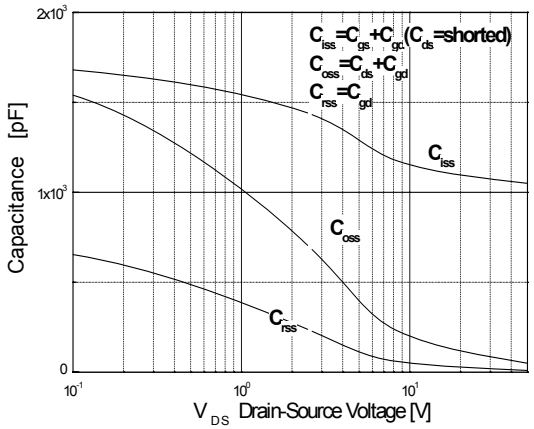
On-Resistance Variation vs. Drain Current and Gate Voltage



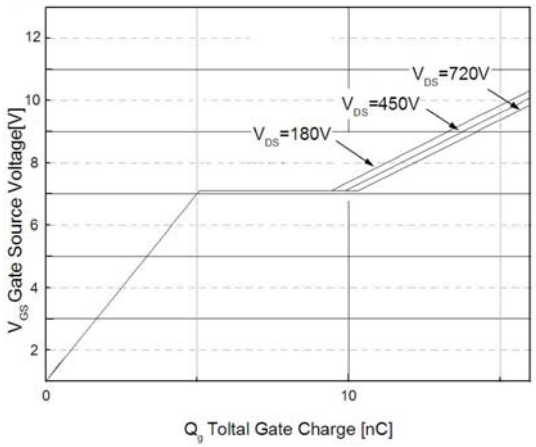
Body Diode Forward Voltage Variation vs. Source Current and Temperature



Capacitance Characteristics

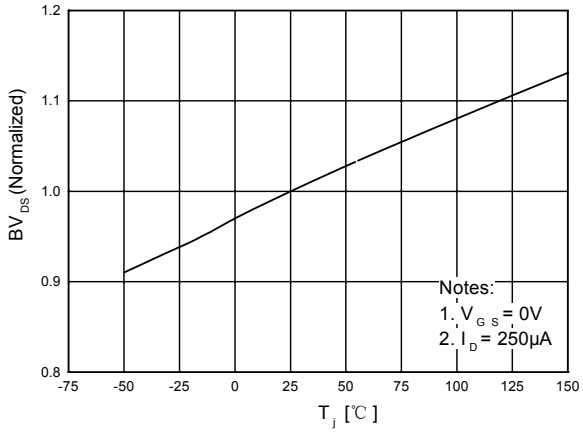


Gate Charge Characteristics

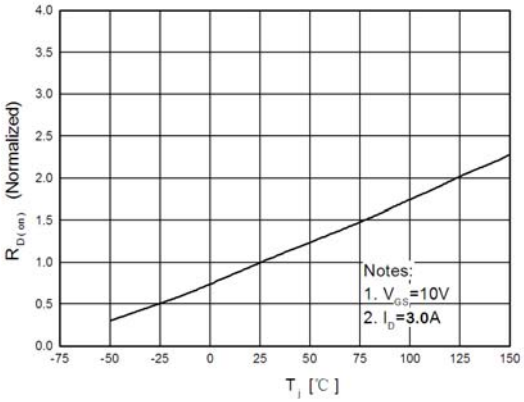


特征曲线 ELECTRICAL CHARACTERISTICS (curves)

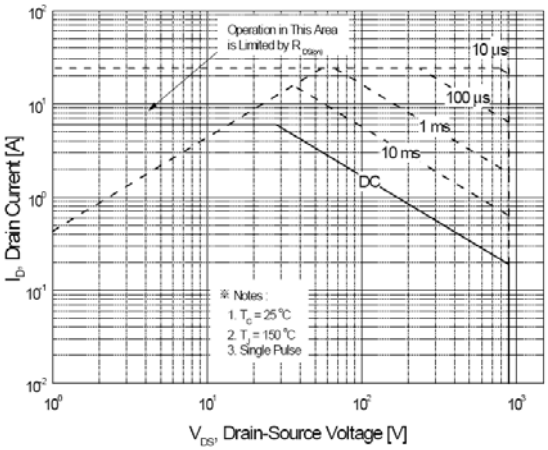
Breakdown Voltage Variation vs. Temperature



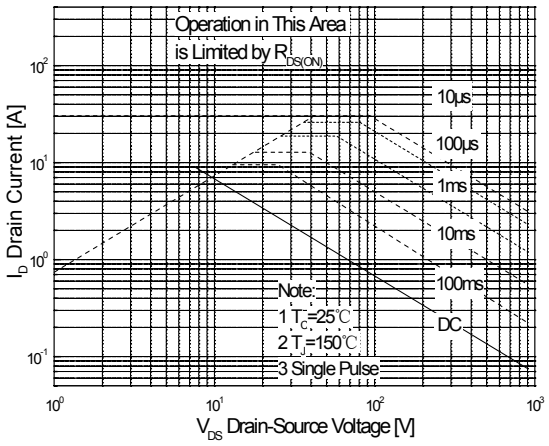
On-Resistance Variation vs. Temperature



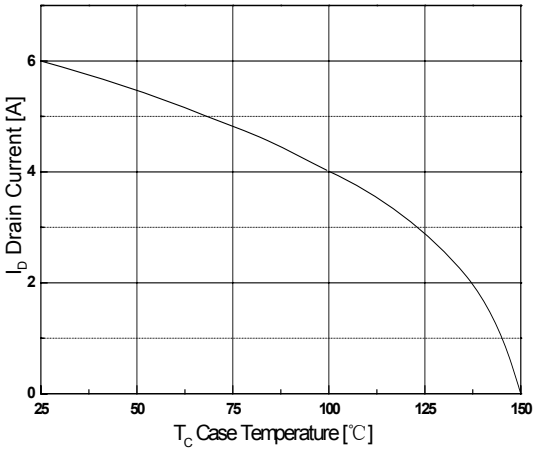
Maximum Safe Operating Area JCS6N90CH



Maximum Safe Operating Area JCS6N90FH

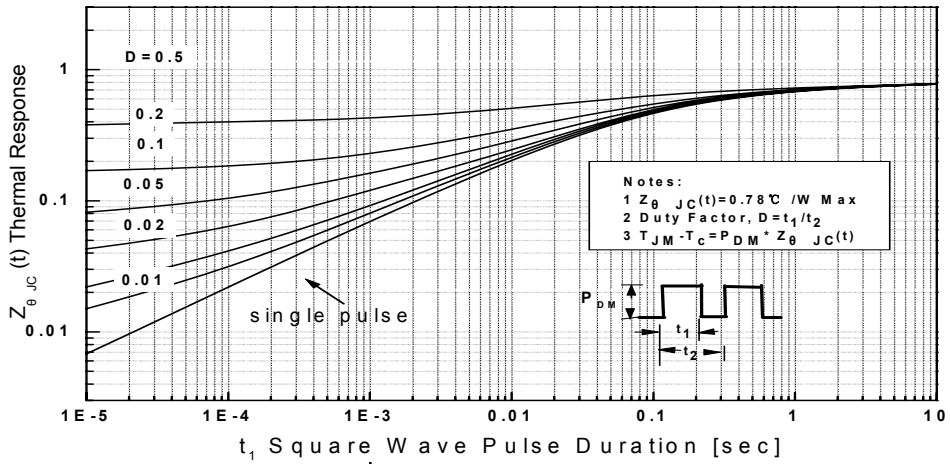


Maximum Drain Current vs. Case Temperature

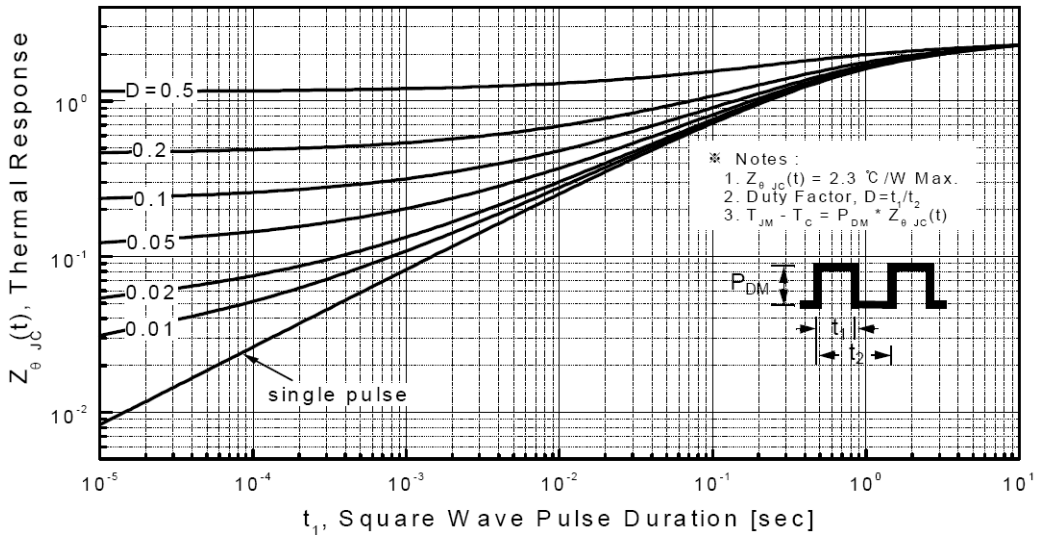


特征曲线 ELECTRICAL CHARACTERISTICS (curves)

Transient Thermal Response Curve
JCS6N90CH



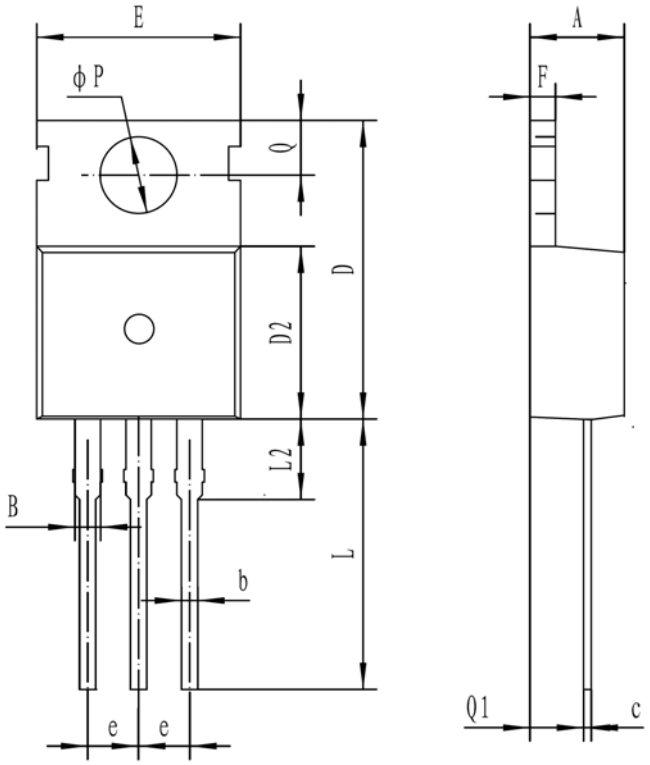
Transient Thermal Response Curve
JCS6N90FH

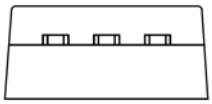


外形尺寸 **PACKAGE MECHANICAL DATA**

TO-220C

单位 Unit：mm



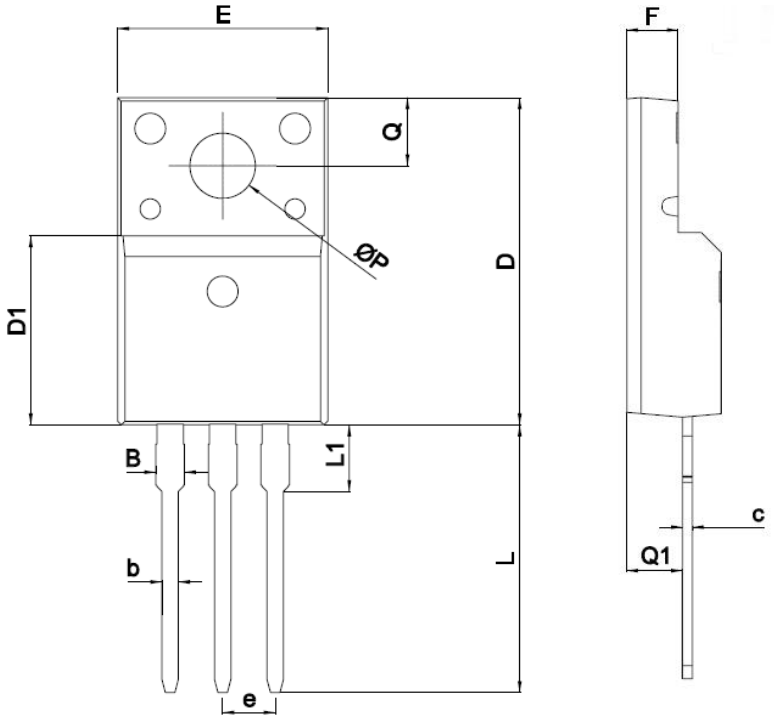


符号 symbol	MIN	MAX
A	4.30	4.70
B	1.10	1.40
b	0.70	0.95
c	0.40	0.65
D	15.20	16.20
D2	9.00	9.40
E	9.70	10.10
e	2.39	2.69
F	1.25	1.40
L	12.60	13.60
L2	2.80	3.20
Q	2.60	3.00
Q1	2.20	2.60
P	3.50	3.80

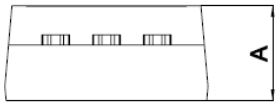
外形尺寸 **PACKAGE MECHANICAL DATA**

TO-220MF

单位 Unit：mm



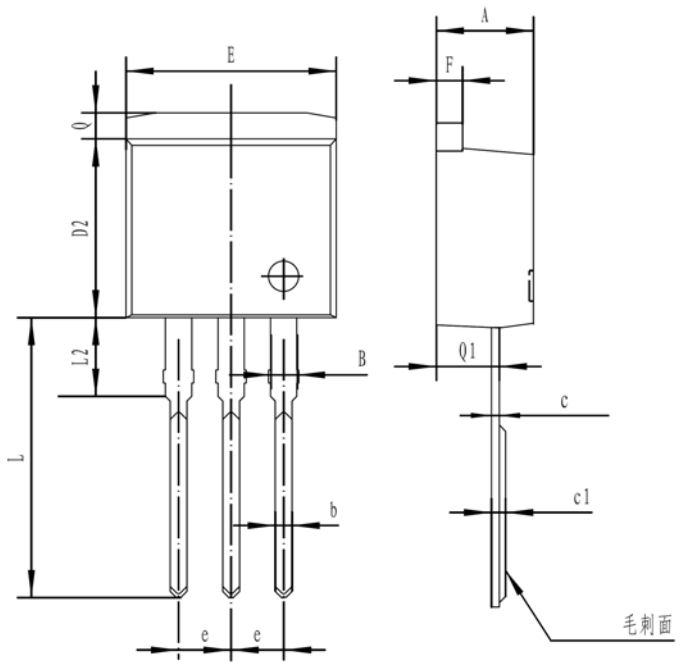
SYMBOL	mm	
	MIN	MAX
A	4.5	4.9
B		1.47
b	0.7	0.9
c	0.45	0.60
D	15.67	16.07
D1	9.04	9.20
e	2.54TYPE	
E	9.96	10.36
F	2.34	2.74
L	12.58	13.38
L1	3.13	3.33
Q	3.2	3.4
Q1	2.56	2.96
ΦP	3.08	3.28



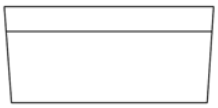
外形尺寸 PACKAGE MECHANICAL DATA

TO-262

单位 Unit：mm



符号 symbol	MIN	MAX
A	4.40	4.90
B	1.10	1.40
b	0.70	0.95
c	0.30	0.60
c1	0.33	0.63
D2	8.20	9.20
E	9.60	10.50
e	2.39	2.69
F	1.20	1.35
L	13.11	14.61
L2	3.55	4.05
Q	1.10	1.40
Q1	2.65	2.85



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2.购买时请认清公司商标，如有疑问请与公司本部联系。

3.在电路设计时请不要超过器件的绝对最大额定值，否则会影响整机的可靠性。

4.本说明书如有版本变更不另外告知

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2. We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.
3. Please do not exceed the absolute maximum ratings of the device when circuit designing.
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