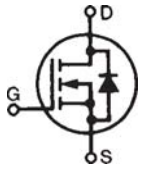
IXYS

HiPerFET™ Power MOSFETs

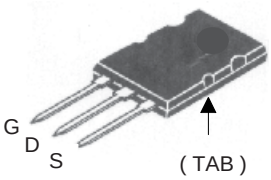
IXFB80N50Q2

V_{DSS} = 500V
I_{D25} = 80A
$R_{DS(on)}$ ≤ 60mΩ
t_{rr} ≤ 250ns

N-Channel Enhancement Mode
Avalanche Rated, Low Q_g, Low Intrinsic R_G
High dV/dt, Low t_{rr}



PLUS264™(IXFB)



G = Gate D = Drain
S = Source TAB = Drain

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C	500	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ	500	V
V_{GSS}	Continuous	± 30	V
V_{GSM}	Transient	± 40	V
I_{D25}	T_C = 25°C	80	A
I_{DRMS}	External lead limited	75	A
I_{DM}	T_C = 25°C, pulse width limited by T_{JM}	320	A
I_{AR}	T_C = 25°C	80	A
E_{AR}	T_C = 25°C	60	mJ
E_{AS}	T_C = 25°C	5.0	J
dV/dt	$I_S \le I_{DM}$, $V_{DD} \le V_{DSS}$, $T_J \le 150°C$	20	V/ns
P_D	T_C = 25°C	960	W
T_J		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
T_L	1.6 mm (0.063 in.) from case for 10s	300	°C
T_{SOLD}	Plastic body for 10s	260	°C
F_C	Mounting force	30...120/6.7...27	N / lbs
Weight		10	g

Symbol	Test Conditions (T$_J$ = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0 V, I_D = 1mA	500			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 8mA	3.0		5.5	V
I_{GSS}	V_{GS} = ±30 V, V_{DS} = 0V			± 200	nA
I_{DSS}	$V_{DS} = V_{DSS}$, V_{GS} = 0V			100	μA
	T_J = 125°C			5	mA
$R_{DS(on)}$	V_{GS} = 10V, $I_D = 0.5 \bullet I_{D25}$, Note 1			60	mΩ

Features

- Double metal process for low gate resistance
- Unclamped Inductive Switching (UIS) rated
- Low package inductance - easy to drive and to protect
- Fast intrinsic rectifier

Applications

- DC-DC converters
- Switched-mode and resonant-mode power supplies, >500kHz switching
- DC choppers
- Pulse generation
- Laser drivers

Advantages

- PLUS 264™ package for clip or spring mounting
- Space savings
- High power density

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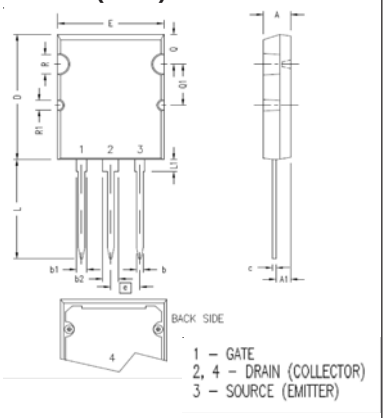
Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.	
g_{fs}	V_{DS} = 10V, I_D = 0.5 • I_{D25}, Note 1	50	65		S
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		15		nF
C_{oss}			1610		pF
C_{rss}			300		pF
$t_{d(on)}$	**Resistive Switching Times**		29		ns
t_r	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25}		25		ns
$t_{d(off)}$	R_G = 1Ω (External)		60		ns
t_f			11		ns
$Q_{g(on)}$	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25}		250		nC
Q_{gs}			80		nC
Q_{gd}			120		nC
R_{thJC}				0.13	°C/W
R_{thCK}			0.13		°C/W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.	
I_S	V_{GS} = 0V			80	A
I_{SM}	Repetitive; pulse width limited by T_{JM}			320	A
V_{SD}	I_F = I_S, V_{GS} = 0V, Note 1			1.5	V
t_{rr}	I_F = 25A, V_{GS} = 0V			250	ns
Q_{RM}	-di/dt = 100 A/μs		1.4		μC
I_{RM}	V_R = 100 V		12		A

Note: 1. Pulse test, t ≤ 300μs, duty cycle, d ≤ 2 %.

PLUS264™ (IXFB) Outline



SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	.185	.209	4.70	5.31
A1	.102	.118	2.59	3.00
b	.037	.055	0.94	1.40
b1	.087	.102	2.21	2.59
b2	.110	.126	2.79	3.20
c	.017	.029	0.43	0.74
D	1.007	1.047	25.58	26.59
E	.760	.799	19.30	20.29
e	.215 BSC		5.46 BSC	
L	.779	.842	19.79	21.39
L1	.087	.102	2.21	2.59
Q	.240	.256	6.10	6.50
Q1	.330	.346	8.38	8.79
ØR	.155	.187	3.94	4.75
ØR1	.085	.093	2.16	2.36

IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by one or moreof the following U.S. patents: 4,835,592 4,850,072 4,881,106 4,931,844 5,017,508 5,034,796 5,049,961 5,063,307 5,187,117 5,237,481 5,381,025 5,486,715 6,162,665 6,259,123 B1 6,306,728 B1 6,404,065 B1 6,534,343 6,583,505 6,683,344 6,710,405B2 6,710,463 6,727,585 6,759,692 6771478 B2 7,005,734B2

Fig. 1. Output Characteristics @ 25 Deg. C

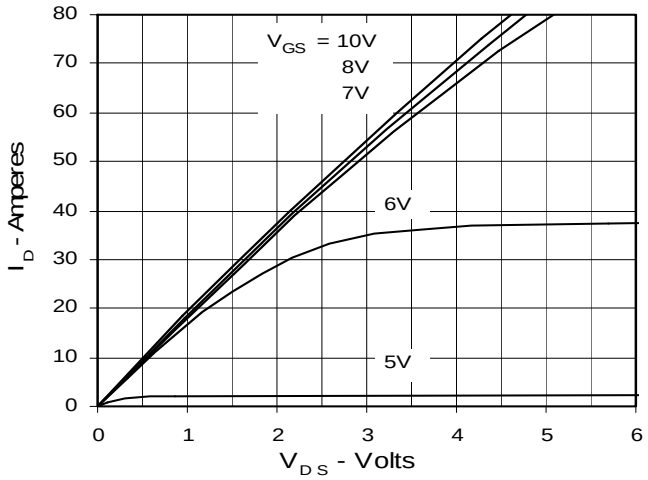


Fig. 2. Extended Output Characteristics @ 25 deg. C

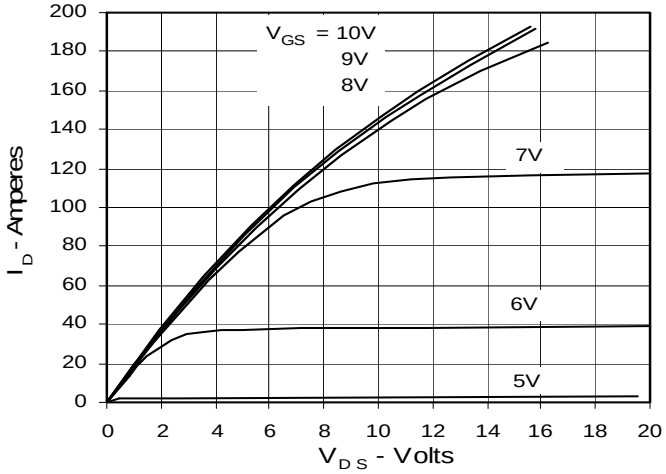


Fig. 3. Output Characteristics @ 125 Deg. C

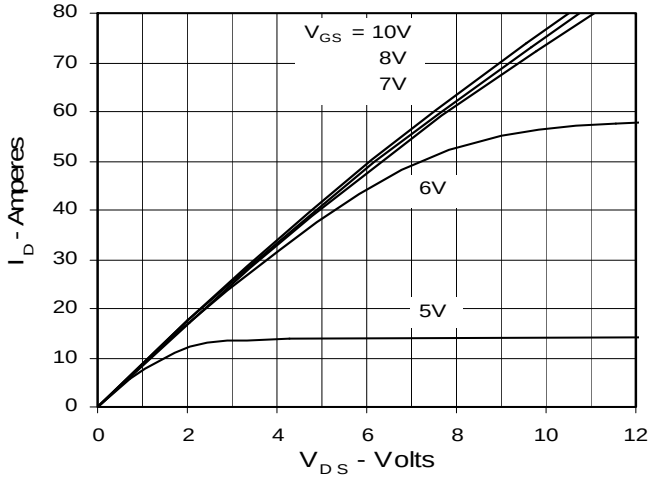


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 40A Value vs. Junction Temperature

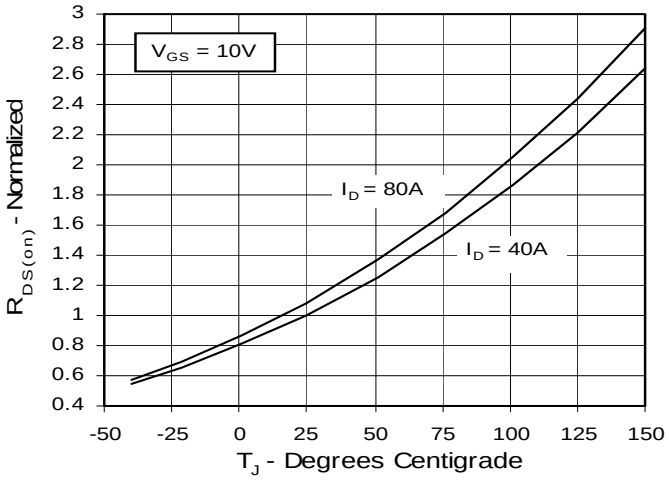


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 40A Value vs. Drain Current

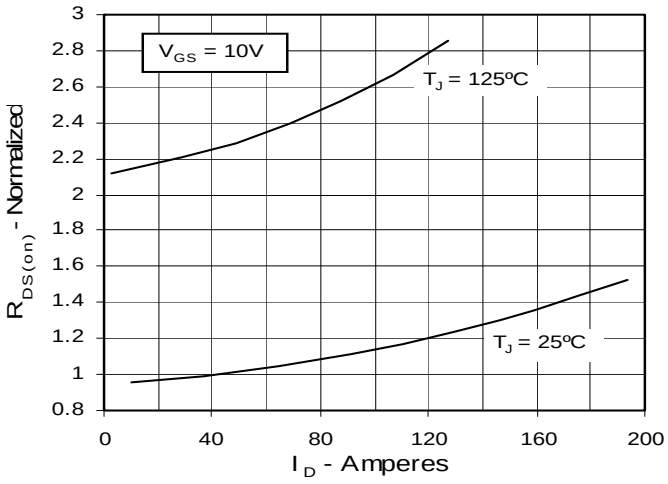
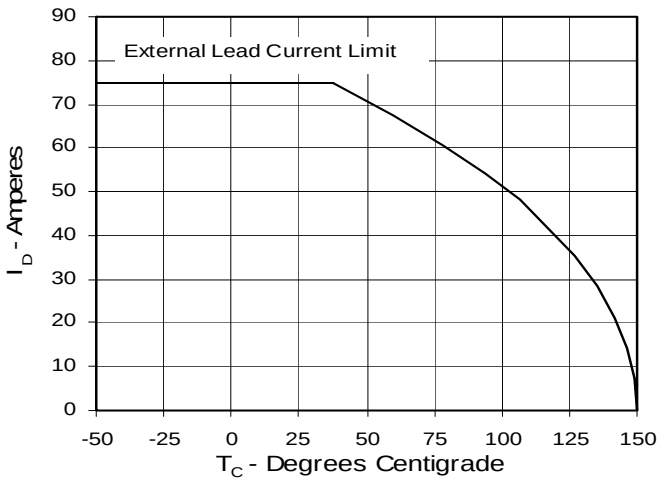


Fig. 6. Drain Current vs. Case Temperature



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Fig. 7. Input Admittance

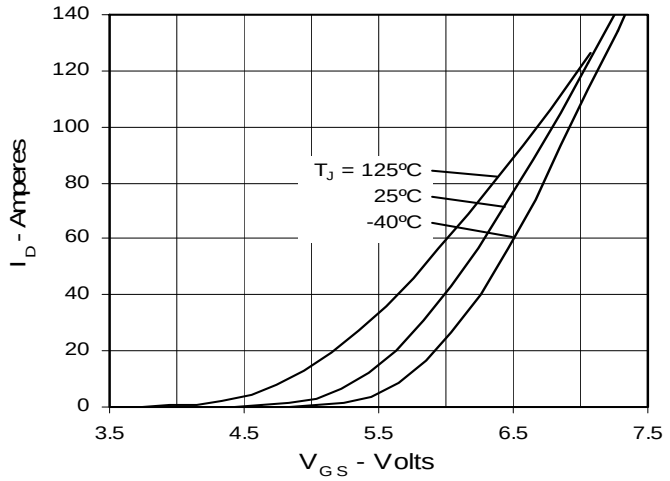


Fig. 8. Transconductance

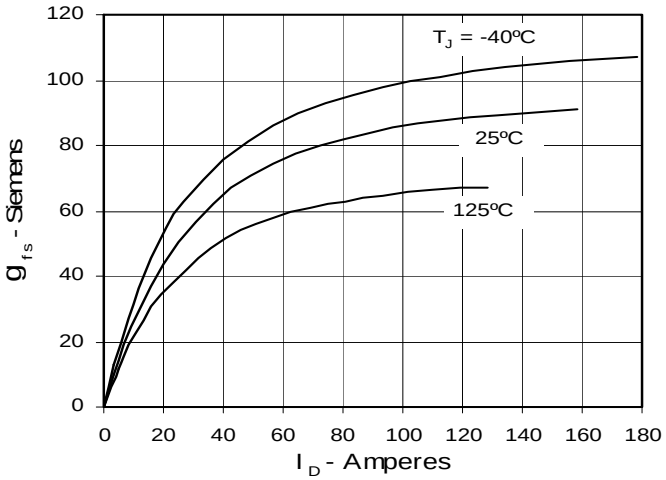


Fig. 9. Source Current vs. Source-To-Drain Voltage

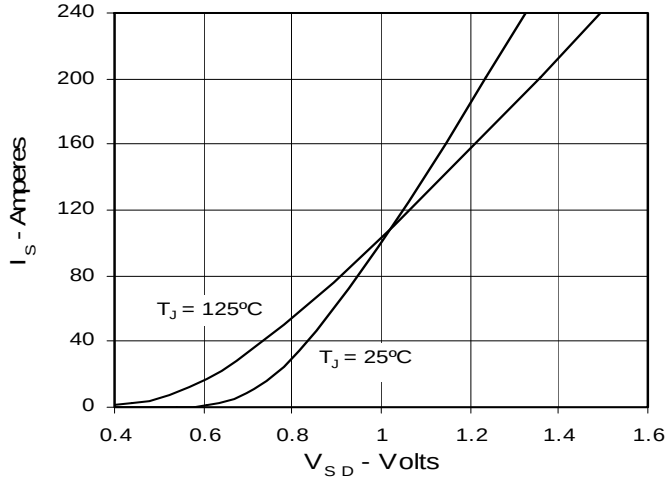


Fig. 10. Gate Charge

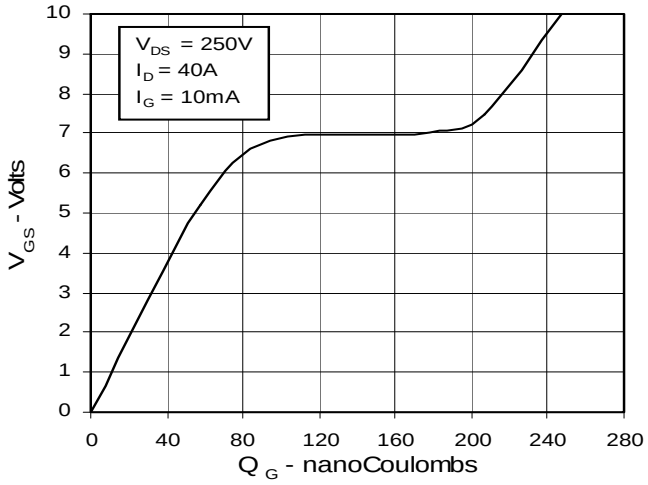


Fig. 11. Capacitance

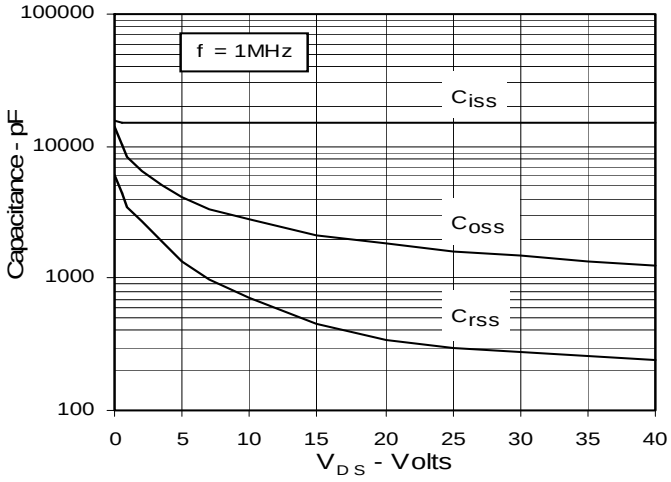
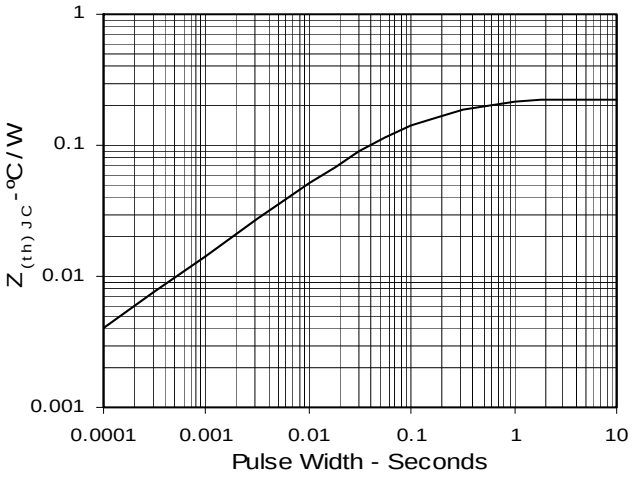


Fig. 12. Maximum Transient Thermal Impedance



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