

ISL9V3040x3ST-F085C

EcoSPARK[®] Ignition IGBT

300 mJ, 400 V, N-Channel Ignition IGBT

Features

- SCIS Energy = 300 mJ at $T_J = 25^\circ\text{C}$
- Logic Level Gate Drive
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Automotive Ignition Coil Driver Circuits
- High Current Ignition System
- Coil on Plug Application

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
BV_{CER}	Collector to Emitter Breakdown Voltage ($I_C = 1\text{ mA}$)	400	V
BV_{ECS}	Emitter to Collector Voltage – Reverse Battery Condition ($I_C = 10\text{ mA}$)	24	V
$ESCIS_{25}$	ISCIS = 14.2 A, L = 3.0 mHy, RGE = 1 K Ω , $T_C = 25^\circ\text{C}$ (Note 1)	300	mJ
$ESCIS_{150}$	ISCIS = 10.6 A, L = 3.0 mHy, RGE = 1 K Ω , $T_C = 150^\circ\text{C}$ (Note 2)	170	mJ
IC_{25}	Collector Current Continuous at $V_{GE} = 4.0\text{ V}$, $T_C = 25^\circ\text{C}$	21	A
IC_{110}	Collector Current Continuous at $V_{GE} = 4.0\text{ V}$, $T_C = 110^\circ\text{C}$	17	A
V_{GEM}	Gate to Emitter Voltage Continuous	± 10	V
PD	Power Dissipation Total, $T_C = 25^\circ\text{C}$	150	W
	Power Dissipation Derating, $T_C > 25^\circ\text{C}$	1	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating Junction and Storage Temperature	-55 to $+175$	$^\circ\text{C}$
T_L	Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	300	$^\circ\text{C}$
T_{PKG}	Reflow Soldering according to JESD020C	260	$^\circ\text{C}$
ESD	HBM–Electrostatic Discharge Voltage at 100 pF, 1500 Ω	4	kV
	CDM–Electrostatic Discharge Voltage at 1 Ω	2	kV

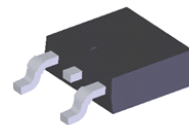
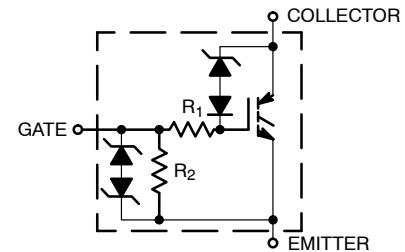
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Self clamped inductive Switching Energy ($ESCIS_{25}$) of 300 mJ is based on the test conditions that is starting $T_J = 25^\circ\text{C}$, L = 3 mHy, ISCIS = 14.2 A, VCC = 100 V during inductor charging and VCC = 0 V during time in clamp.
2. Self Clamped inductive Switching Energy ($ESCIS_{150}$) of 170 mJ is based on the test conditions that is starting $T_J = 150^\circ\text{C}$, L = 3mHy, ISCIS = 10.6 A, VCC = 100 V during inductor charging and VCC = 0 V during time in clamp.

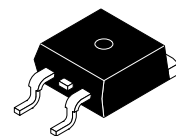


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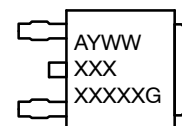


DPAK3
CASE 369AS



D2PAK-3
CASE 418AJ

MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
XXXX = Device Code
G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ISL9V3040x3ST-F085C

THERMAL RESISTANCE RATINGS

Characteristic	Symbol	Max	Units
Junction-to-Case – Steady State (Drain)	$R_{\theta JC}$	1	°C/W

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min	Typ.	Max.	Units
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OFF CHARACTERISTICS

BV _{CER}	Collector to Emitter Breakdown Voltage	I _{CE} = 2 mA, V _{GE} = 0 V, R _{GE} = 1 kΩ, T _J = −40 to 150°C		370	400	430	V
BV _{CES}	Collector to Emitter Breakdown Voltage	I _{CE} = 10 mA, V _{GE} = 0 V, R _{GE} = 0, T _J = −40 to 150°C		390	420	450	V
BV _{ECS}	Emitter to Collector Breakdown Voltage	I _{CE} = −75 mA, V _{GE} = 0 V, T _J = 25°C		30	–	–	V
BV _{GES}	Gate to Emitter Breakdown Voltage	I _{GES} = ±2 mA		±12	±14	–	V
I _{CER}	Collector to Emitter Leakage Current	V _{CE} = 175 V R _{GE} = 1 kΩ	T _J = 25°C	–	–	25	μA
			T _J = 150°C	–	–	1	mA
I _{ECS}	Emitter to Collector Leakage Current	V _{EC} = 24 V	T _J = 25°C	–	–	1	mA
			T _J = 150°C	–	–	40	
R ₁	Series Gate Resistance			–	70	–	Ω
R ₂	Gate to Emitter Resistance			10K	–	26K	Ω

ON CHARACTERISTICS

$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	$I_{CE} = 6\text{ A}$, $V_{GE} = 4\text{ V}$, $T_J = 25^\circ\text{C}$	–	1.25	1.65	V
$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	$I_{CE} = 10\text{ A}$, $V_{GE} = 4.5\text{ V}$, $T_J = 150^\circ\text{C}$	–	1.58	1.80	V
$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	$I_{CE} = 15\text{ A}$, $V_{GE} = 4.5\text{ V}$, $T_J = 150^\circ\text{C}$	–	1.90	2.20	V

DYNAMIC CHARACTERISTICS

Q _{G(ON)}	Gate Charge	I _{CE} = 10 A, V _{CE} = 12 V, V _{GE} = 5 V		–	17	–	nC
V _{GE(TH)}	Gate to Emitter Threshold Voltage	I _{CE} = 1 mA V _{CE} = V _{GE}	T _J = 25°C	1.3	–	2.2	V
			T _J = 150°C	0.75	–	1.8	
V _{GEP}	Gate to Emitter Plateau Voltage	V _{CE} = 12 V, I _{CE} = 10 A		–	3.0	–	V

SWITCHING CHARACTERISTICS

$t_{d(ON)R}$	Current Turn-On Delay Time–Resistive	$V_{CE} = 14\text{ V}$, $R_L = 1\text{ }\Omega$, $V_{GE} = 5\text{ V}$, $R_G = 470\text{ }\Omega$, $T_J = 25^\circ\text{C}$	–	0.7	4	μs
t_{rR}	Current Rise Time–Resistive		–	2.1	7	
$t_{d(OFF)L}$	Current Turn-Off Delay Time–Inductive	$V_{CE} = 300\text{ V}$, $L = 1\text{ mH}$, $V_{GE} = 5\text{ V}$, $R_G = 470\text{ }\Omega$, $I_{CE} = 6.5\text{ A}$, $T_J = 25^\circ\text{C}$	–	4.8	15	
t_{fL}	Current Fall Time–Inductive		–	2.8	15	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PACKAGE MARKING AND ORDERING INFORMATION

Device	Package	Shipping [†]
ISL9V3040D3ST-F085C	DPAK (Pb-Free)	2500 Units/Tape & Reel
ISL9V3040S3ST-F085C	D2PAK (Pb-Free)	800 Units/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

TYPICAL CHARACTERISTICS

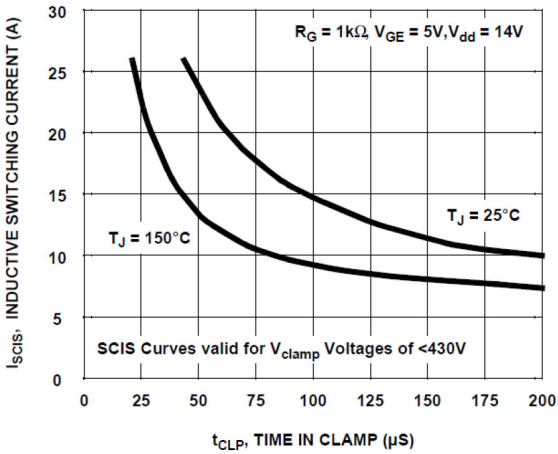


Figure 1. Self Clamped Inductive Switching Current vs. Time in Clamp

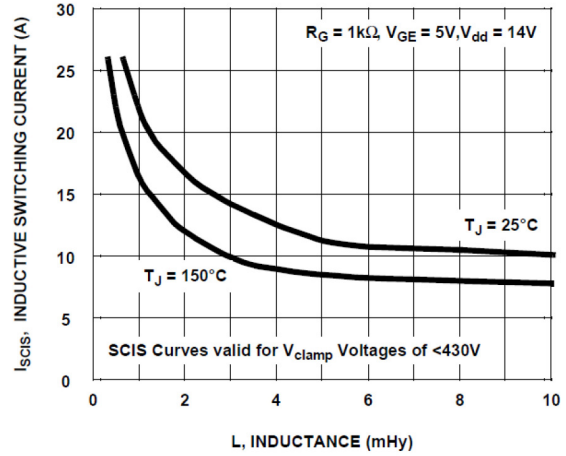


Figure 2. Self Clamped Inductive Switching Current vs. Inductance

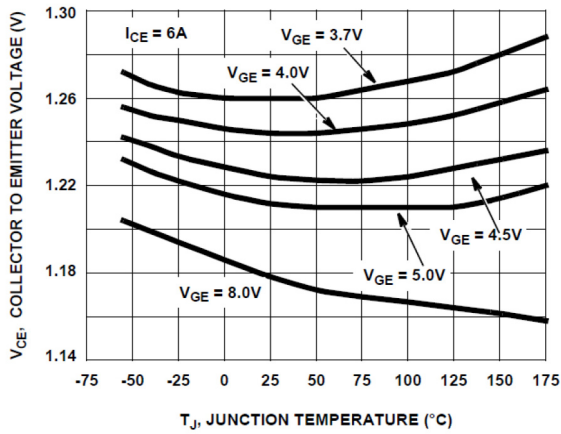


Figure 3. Collector to Emitter On-State Voltage vs. Junction Temperature

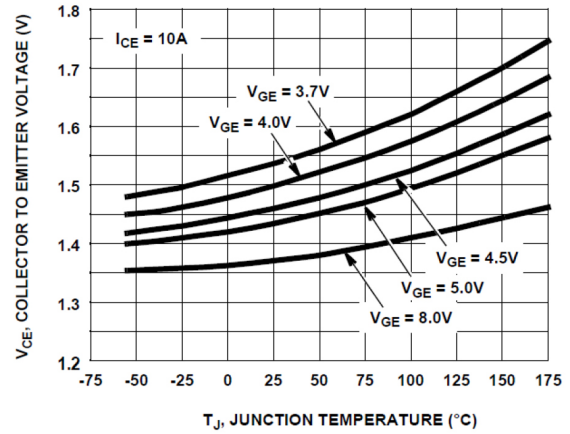


Figure 4. Collector to Emitter On-State Voltage vs. Junction Temperature

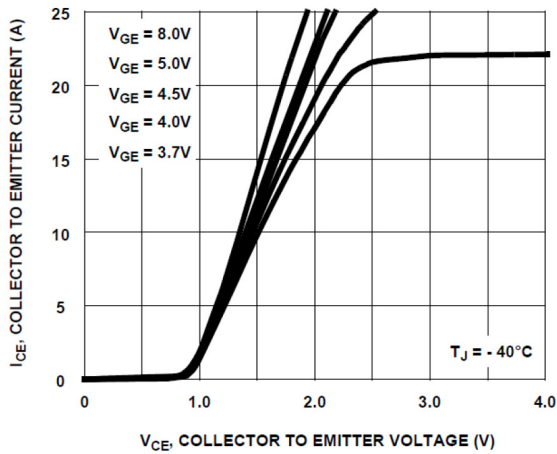


Figure 5. Collector to Emitter On-State Voltage vs. Collector Current

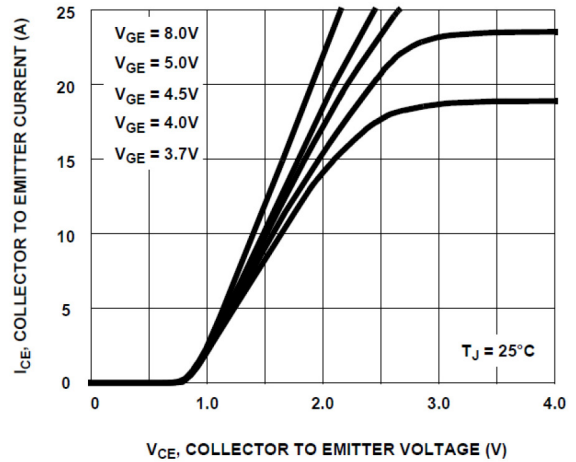


Figure 6. Collector to Emitter On-State Voltage vs. Collector Current

TYPICAL CHARACTERISTICS (continued)

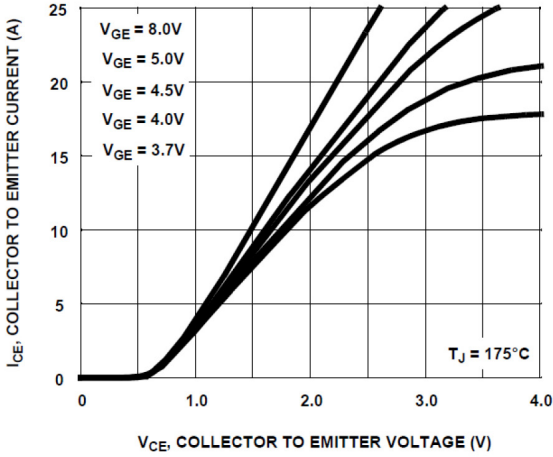


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

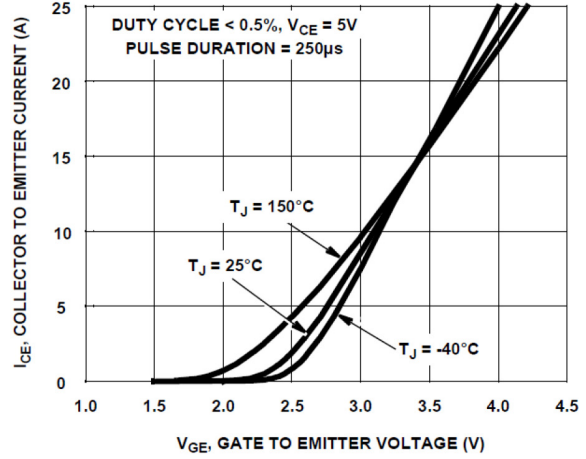


Figure 8. Transfer Characteristics

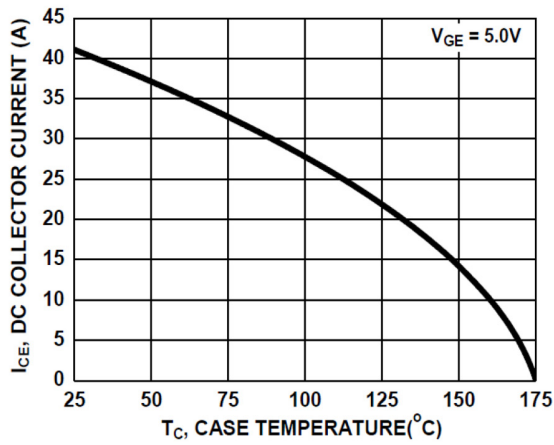


Figure 9. DC Collector Current vs. Case Temperature

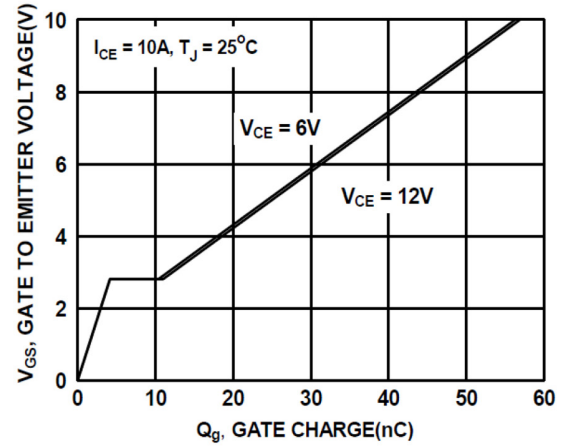


Figure 10. Gate Charge

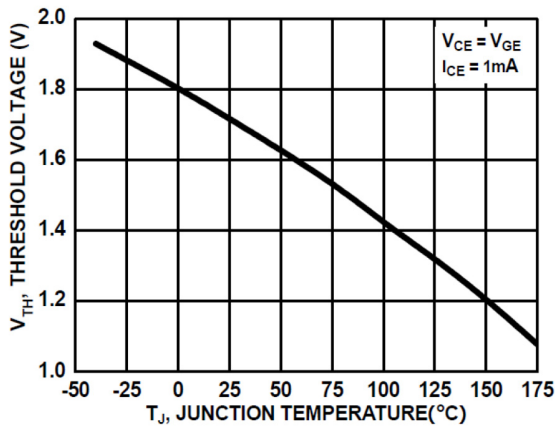


Figure 11. Threshold Voltage vs. Junction Temperature

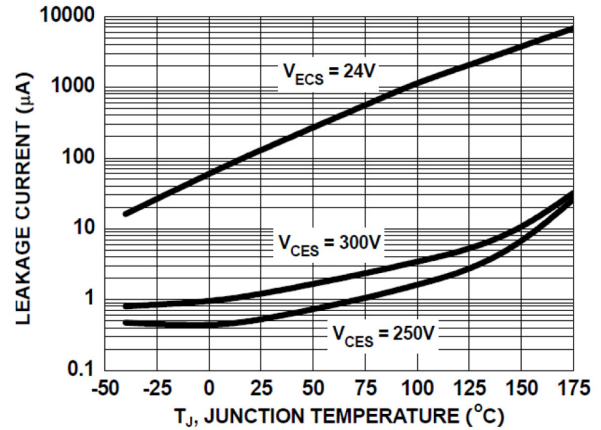


Figure 12. Leakage Current vs. Junction Temperature

ISL9V3040x3ST-F085C

TYPICAL CHARACTERISTICS (continued)

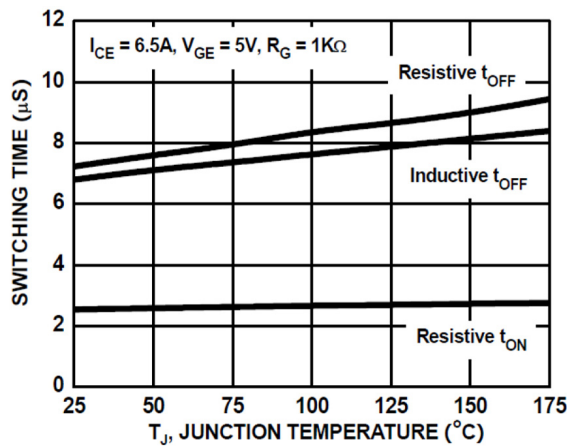


Figure 13. Switching Time vs. Junction Temperature

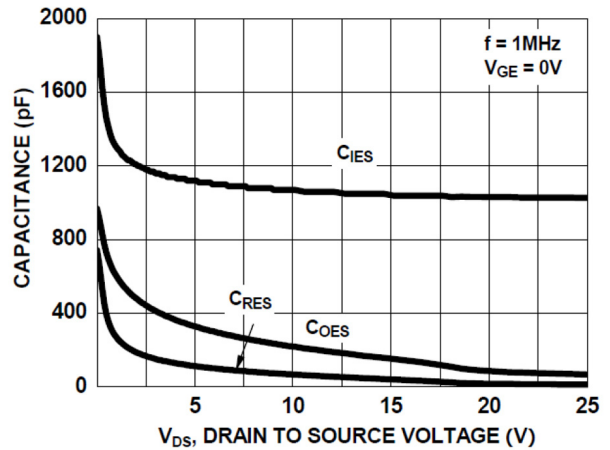


Figure 14. Capacitance vs. Collector to Emitter

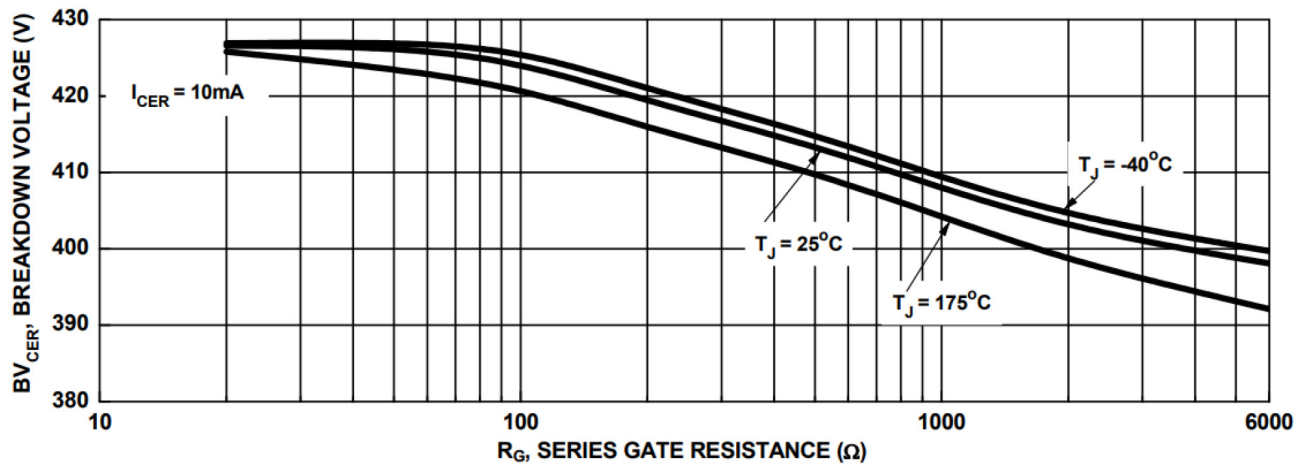


Figure 15. Break Down Voltage vs. Series Resistance

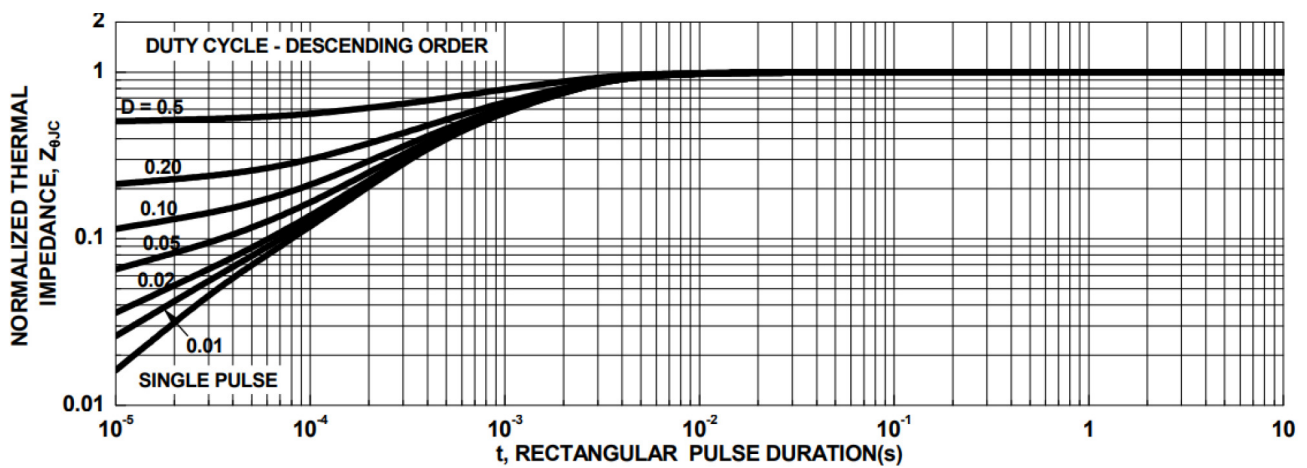


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

TEST CIRCUIT AND WAVEFORMS

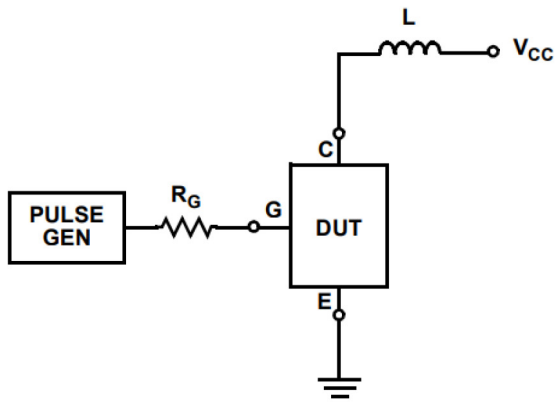


Figure 17. Inductive Switching Test Circuit

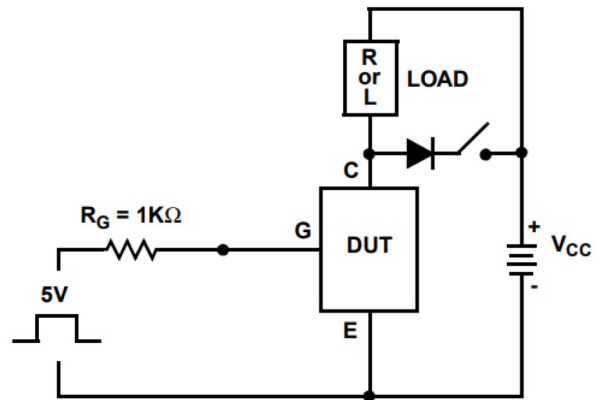


Figure 18. t_{ON} and t_{OFF} Switching Test Circuit

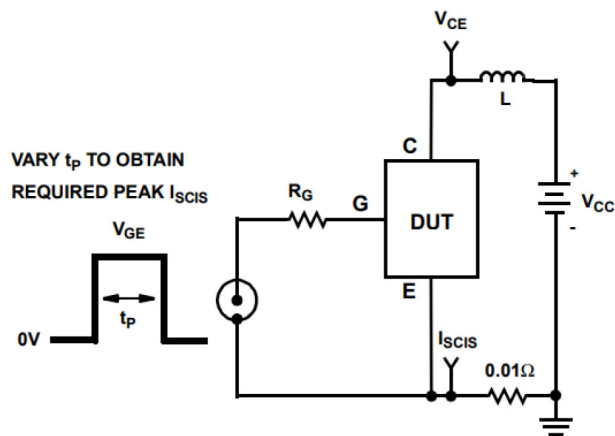


Figure 19. Energy Test Circuit

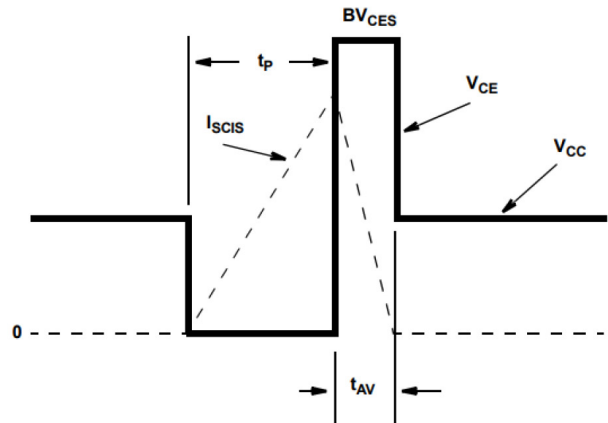
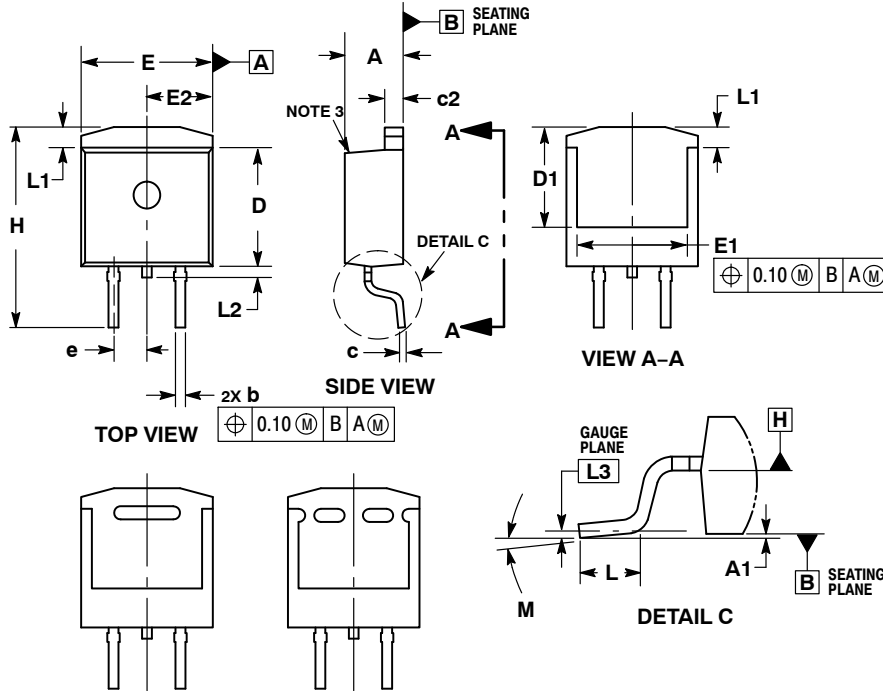


Figure 20. Energy Waveforms

ISL9V3040x3ST-F085C

PACKAGE DIMENSIONS

D²PAK-3 (TO-263, 3-LEAD)
CASE 418AJ
ISSUE C



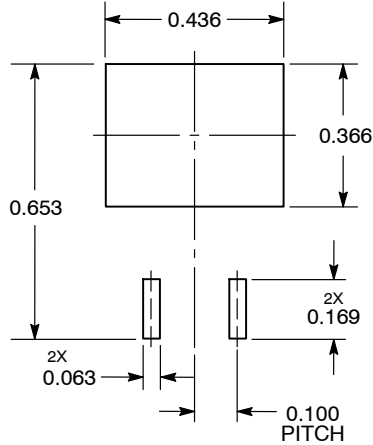
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. CHAMFER OPTIONAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
5. THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1 AND E1.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.160	0.190	4.06	4.83
A1	0.000	0.010	0.00	0.25
b	0.020	0.039	0.51	0.99
c	0.012	0.029	0.30	0.74
c2	0.045	0.065	1.14	1.65
D	0.330	0.380	8.38	9.65
D1	0.260	---	6.60	---
E	0.380	0.420	9.65	10.67
E1	0.245	---	6.22	---
e	0.100 BSC		2.54 BSC	
H	0.575	0.625	14.60	15.88
L	0.070	0.110	1.78	2.79
L1	---	0.066	---	1.68
L2	---	0.070	---	1.78
L3	0.010 BSC		0.25 BSC	
M	-8°	8°	-8°	8°

VIEW A-A
OPTIONAL CONSTRUCTIONS

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: INCHES

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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