

**RADIATION HARDENED
POWER MOSFET
THRU-HOLE (TO-254AA)**

**IRHM7264SE
JANSR2N7434
250V, N-CHANNEL
REF: MIL-PRF-19500/661
RAD Hard™ HEXFET® TECHNOLOGY**

Product Summary

Part Number	Radiation Level	RDS(on)	Id	QPL Part Number
IRHM7264SE	100K Rads (Si)	0.11Ω	31A	JANSR2N7434



International Rectifier's RADHard™ HEXFET® MOSFET technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low RDS(on) and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.

Features:

- Single Event Effect (SEE) Hardened
- Ultra Low RDS(on)
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Light Weight
- ESD Rating: Class 3B per MIL-STD-750, Method 1020

Absolute Maximum Ratings

Pre-Irradiation

	Parameter		Units
Id @ VGS = 12V, TC = 25°C	Continuous Drain Current	31	A
Id @ VGS = 12V, TC = 100°C	Continuous Drain Current	19	
IDM	Pulsed Drain Current ①	124	
PD @ TC = 25°C	Max. Power Dissipation	250	W
	Linear Derating Factor	2.0	W/°C
VGS	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	500	mJ
IAR	Avalanche Current ①	31	A
EAR	Repetitive Avalanche Energy ①	25	mJ
dv/dt	Peak Diode Recovery dv/dt ③	2.5	V/ns
TJ	Operating Junction	-55 to 150	°C
TSTG	Storage Temperature Range		
	Lead Temperature	300 (0.063 in. (1.6mm) from case for 10 sec.)	
	Weight	9.3 (Typical)	g

For footnotes, refer to the last page

Electrical Characteristics @ T_j = 25°C (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	250	—	—	V	V _{GS} = 0V, I _D = 1.0mA
ΔBV _{DSS} /ΔT _J	Temperature Coefficient of Breakdown Voltage	—	0.32	—	V/°C	Reference to 25°C, I _D = 1.0mA
R _{DS(on)}	Static Drain-to-Source On-State Resistance	—	—	0.110 0.123	Ω	V _{GS} = 12V, I _D = 19A ④ V _{GS} = 12V, I _D = 31A
V _{GS(th)}	Gate Threshold Voltage	2.5	—	4.5	V	V _{DS} = V _{GS} , I _D = 1.0mA
g _{fs}	Forward Transconductance	10	—	—	S	V _{DS} = 15V, I _{DS} = 19A ④
I _{DSS}	Zero Gate Voltage Drain Current	—	—	50 250	μA	V _{DS} = 200V, V _{GS} = 0V V _{DS} = 200V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	V _{GS} = 20V
I _{GSS}	Gate-to-Source Leakage Reverse	—	—	-100	nA	V _{GS} = -20V
Q _g	Total Gate Charge	—	—	210	nC	V _{GS} = 12V, I _D = 31A V _{DS} = 125V
Q _{gs}	Gate-to-Source Charge	—	—	50	nC	
Q _{gd}	Gate-to-Drain ('Miller') Charge	—	—	110	nC	
t _{d(on)}	Turn-On Delay Time	—	—	30	ns	V _{DD} = 125V, I _D = 31A, V _{GS} = 12V, R _G = 2.35Ω
t _r	Rise Time	—	—	130		
t _{d(off)}	Turn-Off Delay Time	—	—	100		
t _f	Fall Time	—	—	90		
L _S + L _D	Total Inductance	—	6.8	—	nH	Measured from drain lead (6mm/0.25in. from package) to source lead (6mm/0.25in. from package)
C _{iss}	Input Capacitance	—	4000	—	pF	V _{GS} = 0V, V _{DS} = 25V f = 1.0MHz
C _{oss}	Output Capacitance	—	1300	—		
C _{rss}	Reverse Transfer Capacitance	—	480	—		

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I _S	Continuous Source Current (Body Diode)	—	—	31	A	
I _{SM}	Pulse Source Current (Body Diode) ①	—	—	124		
V _{SD}	Diode Forward Voltage	—	—	1.4	V	T _J = 25°C, I _S = 31A, V _{GS} = 0V ④
t _{rr}	Reverse Recovery Time	—	—	700	ns	T _J = 25°C, I _F = 31A, di/dt ≤ 100A/μs V _{DD} ≤ 50V ④
Q _{RR}	Reverse Recovery Charge	—	—	16	μC	
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L _S + L _D .				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R _{thJC}	Junction-to-Case	—	—	0.50	°C/W	Typical socket mount
R _{thCS}	Case-to-Sink	—	0.21	—		
R _{thJA}	Junction-to-Ambient	—	—	48		

Note: Corresponding Spice and Saber models are available on International Rectifier Website.

For footnotes, refer to the last page

Pre-Irradiation

IRHM7264SE, JANSR2N7434

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ $T_j = 25^\circ\text{C}$, Post Total Dose Irradiation ⑤⑥

	Parameter	100 kRads (Si)		Units	Test Conditions
		Min	Max		
BV_{DSS}	Drain-to-Source Breakdown Voltage	250	—	V	$V_{GS} = 0V, I_D = 1.0mA$
$V_{GS(th)}$	Gate Threshold Voltage	2.0	4.5		$V_{GS} = V_{DS}, I_D = 1.0mA$
I_{GSS}	Gate-to-Source Leakage Forward	—	100	nA	$V_{GS} = 20V$
I_{GSS}	Gate-to-Source Leakage Reverse	—	100		$V_{GS} = -20V$
I_{DSS}	Zero Gate Voltage Drain Current	—	50	μA	$V_{DS} = 200V, V_{GS} = 0V$
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (TO-3)	—	0.11	Ω	$V_{GS} = 12V, I_D = 19A$
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (TO-254)	—	0.11	Ω	$V_{GS} = 12V, I_D = 19A$
V_{SD}	Diode Forward Voltage ④	—	1.4	V	$V_{GS} = 0V, I_D = 31A$

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Typical Single Event Effect Safe Operating Area

Ion	LET MeV/(mg/cm ²)	Energy (MeV)	Range (μm)	V_{DS} (V)				
				@ $V_{GS} = 0V$	@ $V_{GS} = -5V$	@ $V_{GS} = -10V$	@ $V_{GS} = -15V$	@ $V_{GS} = -20V$
Cu	28	285	43	250	250	250	250	250
Br	36.8	305	39	250	250	250	225	210

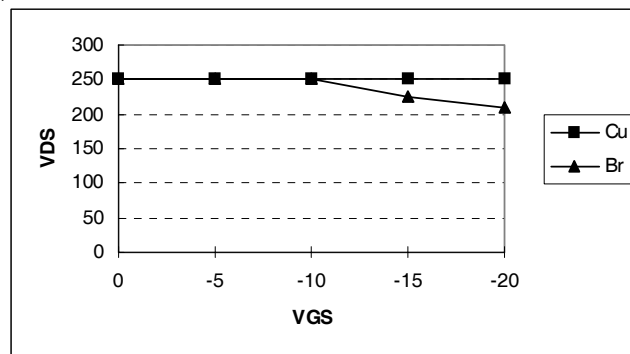


Fig a. Typical Single Event Effect, Safe Operating Area

For footnotes, refer to the last page

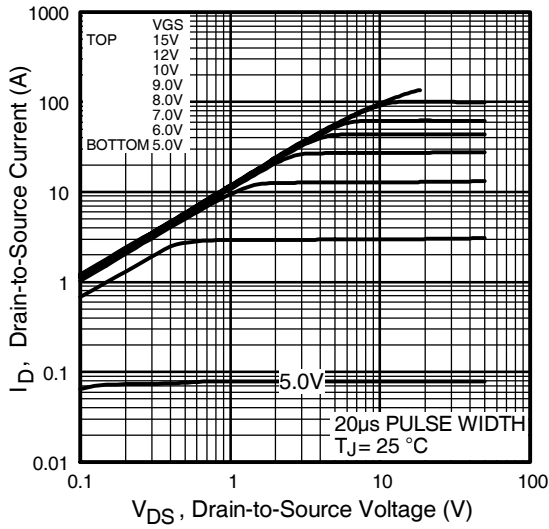


Fig 1. Typical Output Characteristics

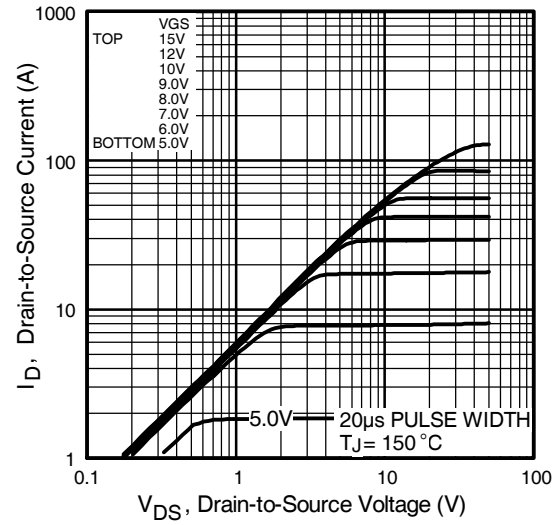


Fig 2. Typical Output Characteristics

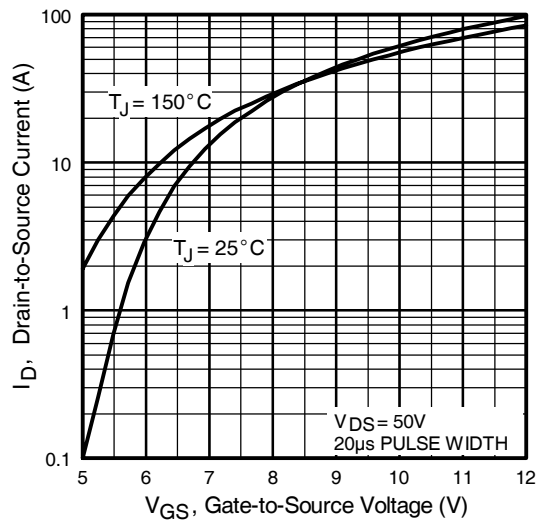


Fig 3. Typical Transfer Characteristics

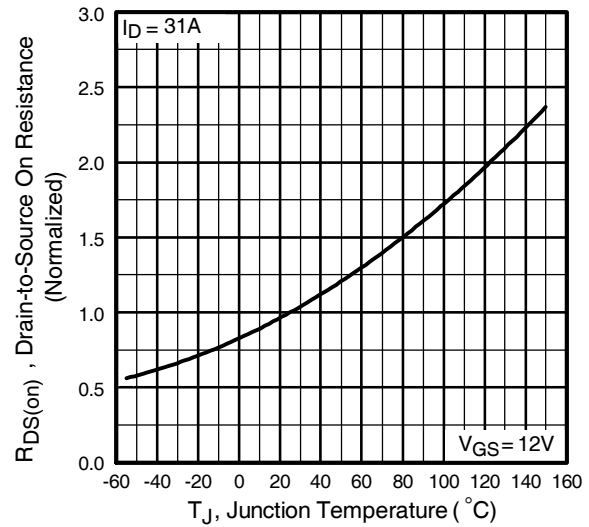


Fig 4. Normalized On-Resistance Vs. Temperature

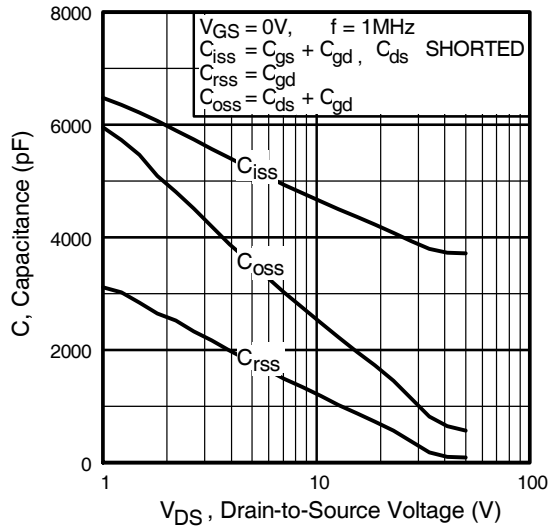


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

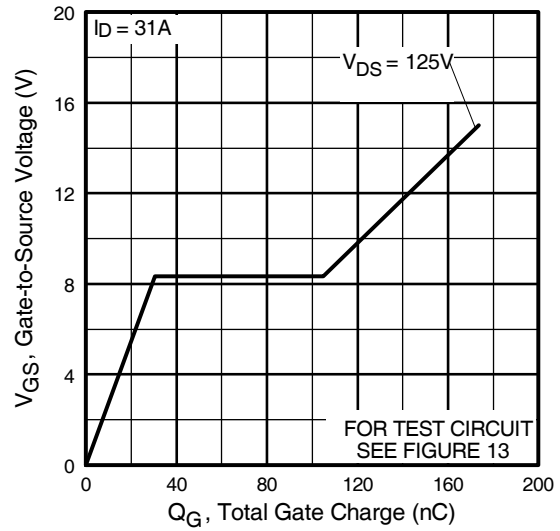


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

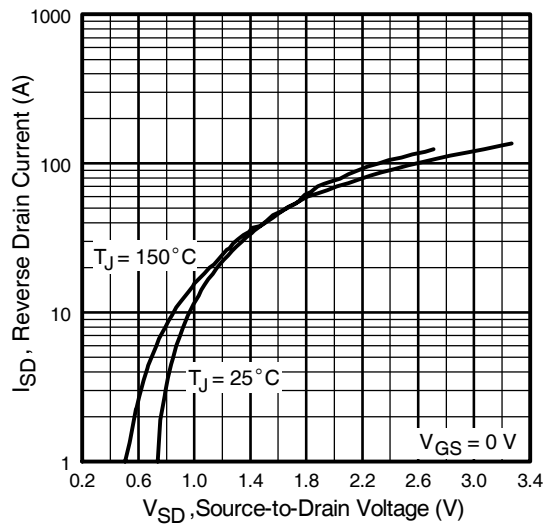


Fig 7. Typical Source-Drain Diode Forward Voltage

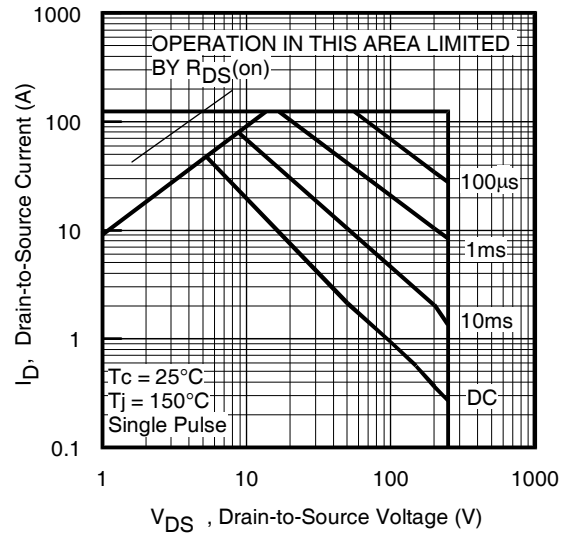


Fig 8. Maximum Safe Operating Area

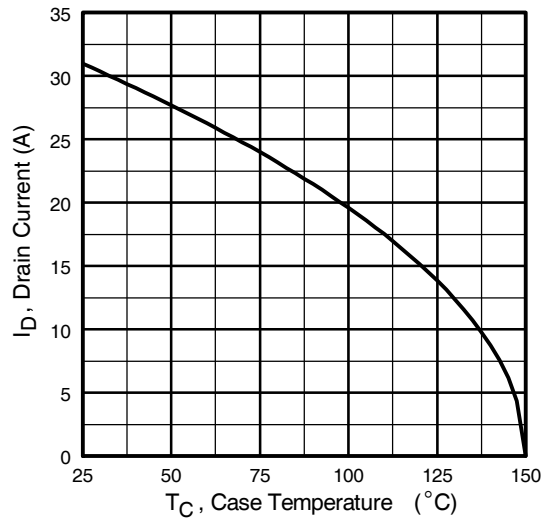


Fig 9. Maximum Drain Current Vs. Case Temperature

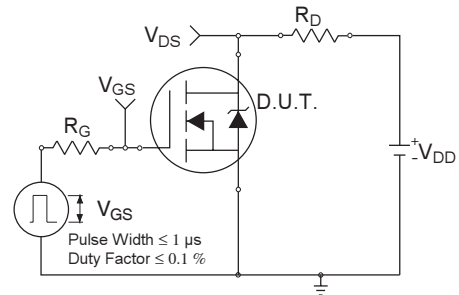


Fig 10a. Switching Time Test Circuit

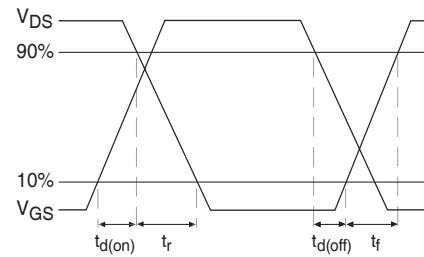


Fig 10b. Switching Time Waveforms

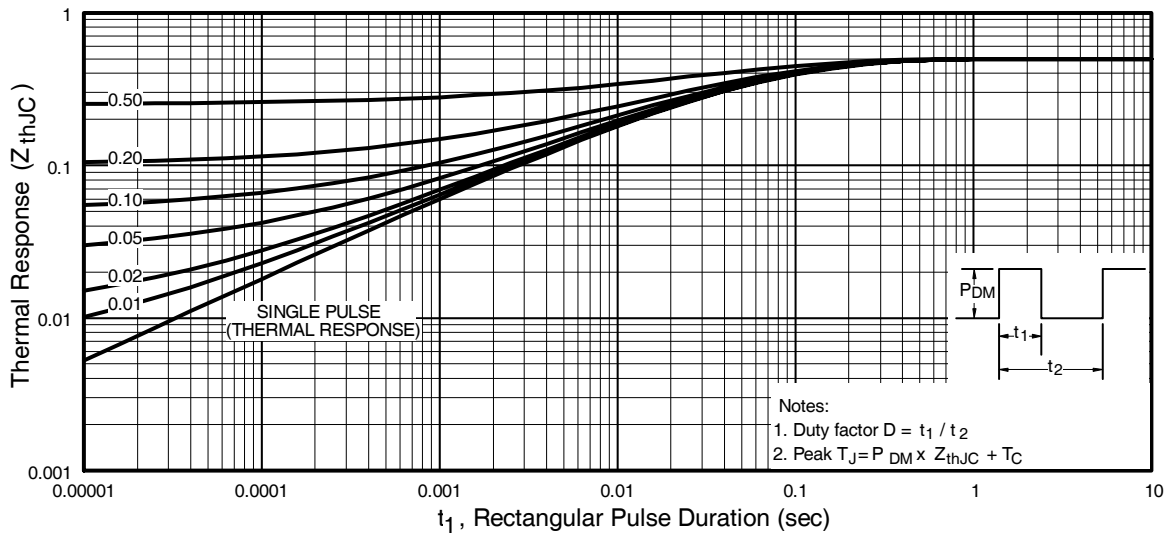


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

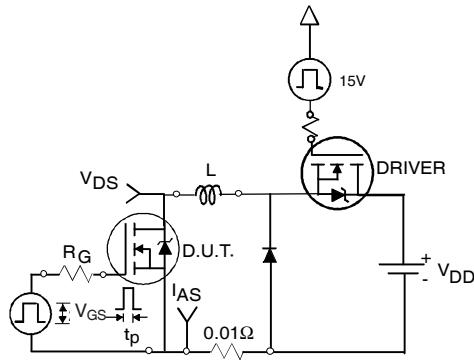


Fig 12a. Unclamped Inductive Test Circuit

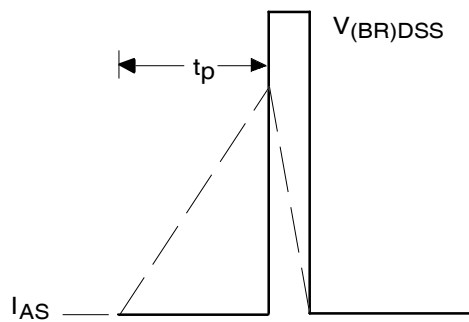


Fig 12b. Unclamped Inductive Waveforms

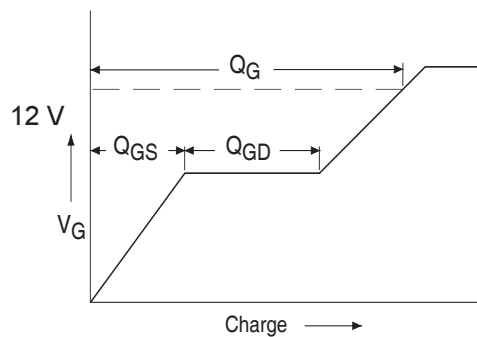


Fig 13a. Basic Gate Charge Waveform

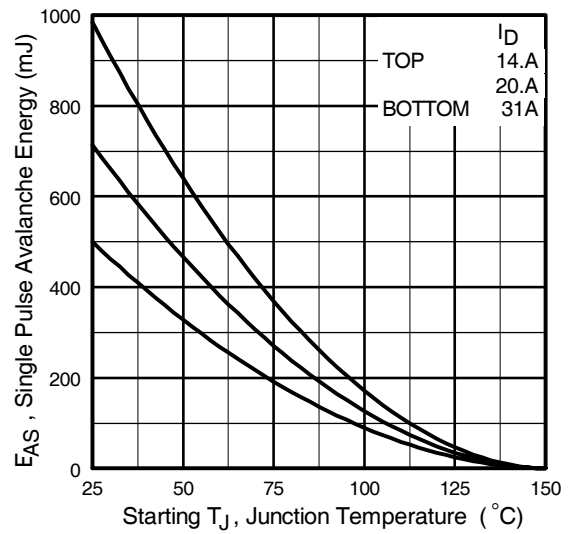


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

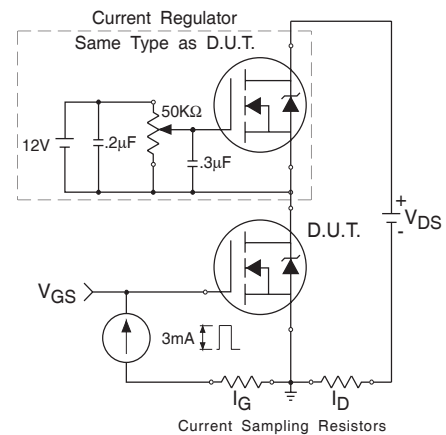


Fig 13b. Gate Charge Test Circuit

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② $V_{DD} = 50V$, starting $T_J = 25^{\circ}C$, $L = 1.0\text{ mH}$
Peak $I_L = 31A$, $V_{GS} = 12V$
- ③ $I_{SD} \leq 31A$, $di/dt \leq 300A/\mu s$,
 $V_{DD} \leq 250V$, $T_J \leq 150^{\circ}C$

- ④ Pulse width $\leq 300 \mu\text{s}$; Duty Cycle $\leq 2\%$
- ⑤ **Total Dose Irradiation with V_{GS} Bias.**
12 volt V_{GS} applied and $V_{DS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V_{DS} Bias.**
200 volt V_{DS} applied and $V_{GS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.

Technical drawing of a mechanical part showing two views: a front view (left) and a side view (right).

Front View Dimensions:

- Overall width: 13.84 [0.545]
- Overall height: 20.32 [0.800]
- Top hole diameter: $\varnothing 3.78$ [0.149]
- Top hole position from left: 3.53 [0.139]
- Top hole position from center: 13.59 [0.535]
- Bottom hole diameter: $\varnothing 1.14$ [0.045]
- Bottom hole position from left: 3.81 [0.150]
- Bottom hole position from center: 13.59 [0.535]
- Bottom hole position from right: 3.81 [0.150]
- Bottom hole diameter: $\varnothing 0.89$ [0.035]
- Bottom hole position from left: 14.48 [0.570]
- Bottom hole position from center: 12.95 [0.510]
- Bottom hole position from right: 14.48 [0.570]
- Bottom hole diameter: $\varnothing 0.89$ [0.035]
- Bottom hole position from left: 14.48 [0.570]
- Bottom hole position from center: 12.95 [0.510]
- Bottom hole position from right: 14.48 [0.570]

Side View Dimensions:

- Overall width: 13.84 [0.545]
- Overall height: 20.32 [0.800]
- Top hole diameter: $\varnothing 3.78$ [0.149]
- Top hole position from left: 3.53 [0.139]
- Top hole position from center: 13.59 [0.535]
- Bottom hole diameter: $\varnothing 1.14$ [0.045]
- Bottom hole position from left: 3.81 [0.150]
- Bottom hole position from center: 13.59 [0.535]
- Bottom hole position from right: 3.81 [0.150]
- Bottom hole diameter: $\varnothing 0.89$ [0.035]
- Bottom hole position from left: 14.48 [0.570]
- Bottom hole position from center: 12.95 [0.510]
- Bottom hole position from right: 14.48 [0.570]

Surface Treatment Table:

$\oplus$	$\varnothing 0.36$ [0.014]	$\textcircled{M}$	B	A	$\textcircled{L}$
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1. DIMENSIONING & TOLERANCING PER ASME Y14.5M-1994.
2. ALL DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
3. CONTROLLING DIMENSION: INCH.
4. CONFORMS TO JEDEC OUTLINE TO-254AA.

1 = DRAIN
2 = SOURCE
3 = GATE

BERYLLIA WARNING PER MIL-PRF-19500

Package containing beryllia shall not be ground, sandblasted, machined, or have other operations performed on them which will produce beryllia or beryllium dust. Furthermore, beryllium oxide packages shall not be placed in acids that will produce fumes containing beryllium.

International
IOR Rectifier

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