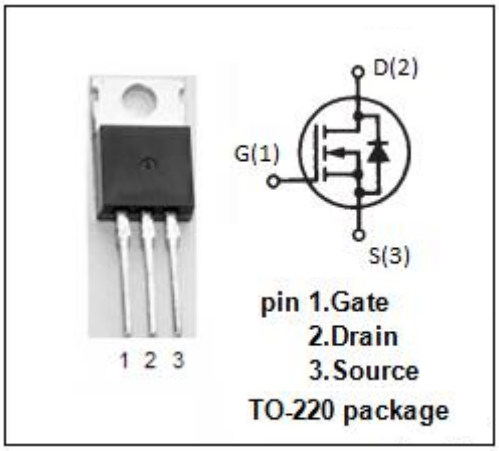
iscN-Channel MOSFET Transistor IRFB17N50L

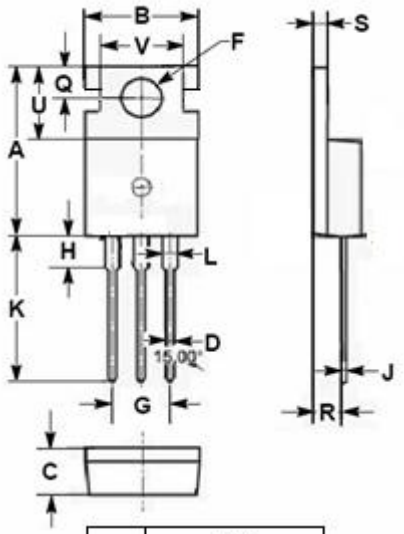


- **FEATURES**
- Low drain-source on-resistance:
  R_{DS}(ON) =0.32Ω (MAX)
- Enhancement mode:
  Vth = 3.0 to 5.0V (V_{DS} = 10 V, I_D=0.25mA)
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

- **DESCRIPTION**
- Switching Voltage Regulators

- **ABSOLUTE MAXIMUM RATINGS(T_a=25℃)**

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage	±30	V
I_D	Drain Current-Continuous	16	A
I_{DM}	Drain Current-Single Pulsed	64	A
P_D	Total Dissipation @T_C=25℃	220	W
T_j	Max. Operating Junction Temperature	-55~150	℃
T_{stg}	Storage Temperature	-55~150	℃



DIM	mm MIN	mm MAX
A	15.50	15.90
B	9.80	10.20
C	4.20	4.50
D	0.70	0.90
F	3.40	3.70
G	4.98	5.18
H	2.68	2.90
J	0.44	0.60
K	12.80	13.40
L	1.20	1.45
Q	2.70	2.90
R	2.30	2.70
S	1.29	1.35
U	6.45	6.65
V	8.66	8.86

- **THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
Rth(ch-c)	Channel-to-case thermal resistance	0.56	℃/W

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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V; I_D= 0.25mA	500			V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}= 10V; I_D=0.25mA	3.0		5.0	V
$R_{DS(on)}$	Drain-Source On-Resistance	V_{GS}=10V; I_D=9.9A			0.32	Ω
I_{GSS}	Gate-Source Leakage Current	V_{GS}= ±30V;V_{DS}= 0V			±100	nA
I_{DSS}	Drain-Source Leakage Current	V_{DS}=500V; V_{GS}= 0V V_{DS}=400V; V_{GS}= 0V;T_J=125℃			0.05 2.0	mA
V_{SDF}	Diode forward voltage	I_{DR} =16A, V_{GS} = 0 V			1.5	V

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