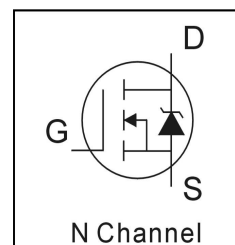


N Channel Power MOSFET FOR LOGIC DRIVER

Chip Specification

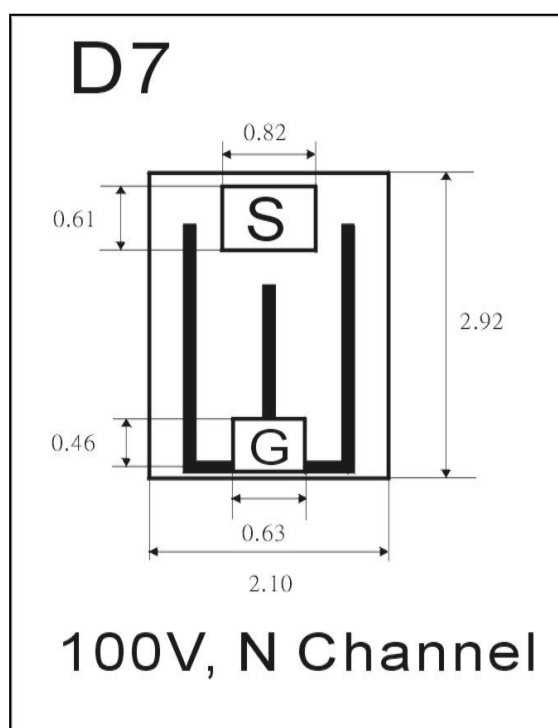
General Description:

- * Advanced Process Technology
- * Dynamic dv/dt Rating
- * 175°C Operating Temperature
- * Fast Switching
- * Fully Avalanche Rated
- * RDS(ON) rated at VGS = 5V
- * Ease of parallel



Mechanical Data:

D7	
Dimension	2.1mm x 2.92mm
Thickness:	400 μm
Metallization:	
Top :	Al
Backside :	CrNiAg / Au
Suggested Bonding Conditions:	
Die Mounting:	Solder Perform
	95/5 PbSn or 92.5/2.5/5 PbAgIn
Source Bonding Wire:	8 mil Al



Absolute Maximum Rating

@Ta=25°C

Characteristics	Symbol	Limit	Unit	Test Conditions
Drain-to-Source Breakdown Voltage	V(BR)DSS	100	V	VGS=0V, ID=250μA
Static Drain-to - Source On-resistance	RDS(ON)	0.27	Ω	VGS=5V, ID=5.5A
Continuous Drain current (in target package)	ID@25°C	9.2	A	VGS= 5V
Continuous Drain current (in target package)	ID@100°C	6.5	A	VGS= 5V
Operation Junction Temperature	Tj	-55~175	°C	
Storage Temperature	TSTR	-55~175	°C	

Target Device: IRL520

TO-220AB

Pd

60

W

@Tc=25°C

