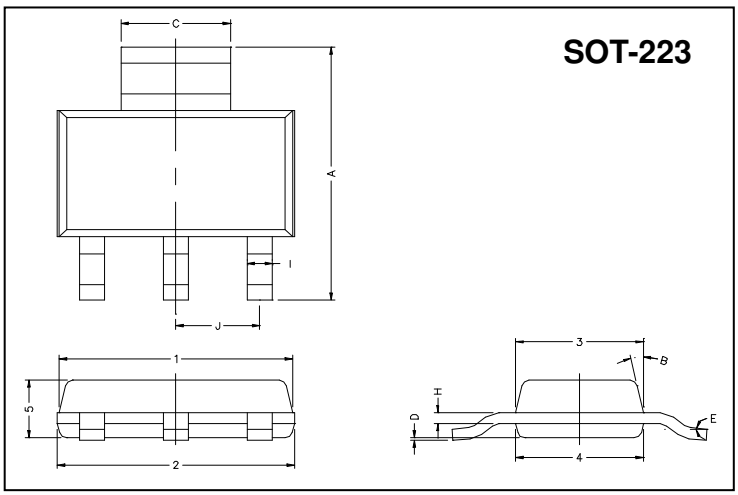
GL4033

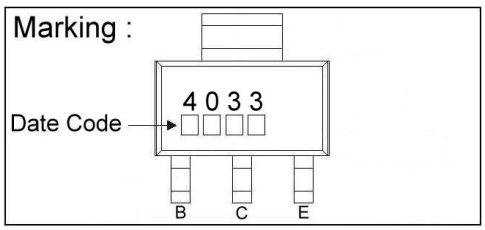
PNP EPITAXIAL PLANAR TRANSISTOR

Description

The GL4033 is designed for high current general purpose amplifier applications.

Package Dimensions





REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	6.70	7.30	B	13°TYP.	
C	2.90	3.10	J	2.30 REF.	
D	0.02	0.10	1	6.30	6.70
E	0°	10°	2	6.30	6.70
I	0.60	0.80	3	3.30	3.70
H	0.25	0.35	4	3.30	3.70
			5	1.40	1.80

Absolute Maximum Ratings (Ta = 25℃,unless otherwise specified)

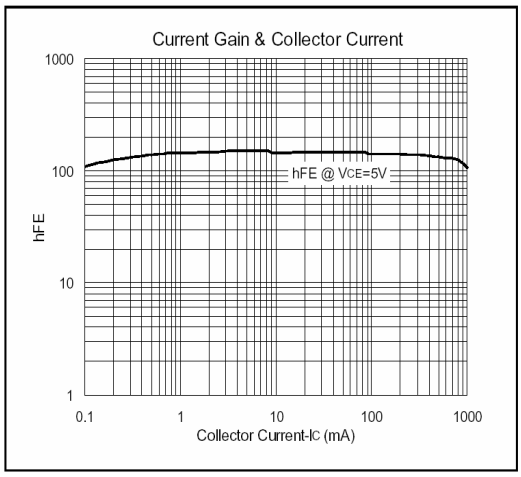
Parameter	Symbol	Ratings	Unit
Junction Temperature	Tj	+150	℃
Storage Temperature Range	Tstg	-55 ~ +150	℃
Collector to Base Voltage	V_{CBO}	-80	V
Collector to Emitter Voltage	V_{CEO}	-80	V
Emitter to Base Voltage	V_{EBO}	-5	V
Collect Current(DC)	I_C	-1	A
Total Power Dissipation	P_D	2	W

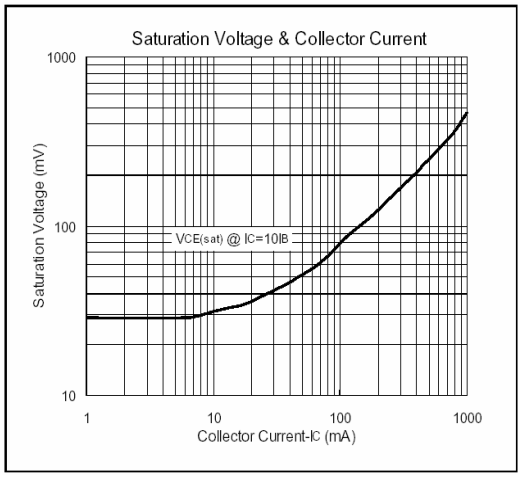
Electrical Characteristics(Ta = 25℃,unless otherwise specified)

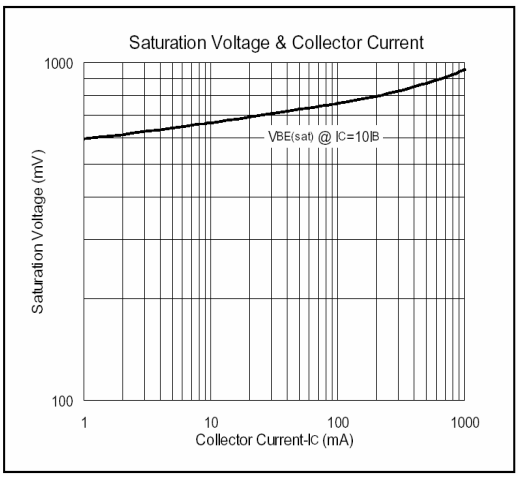
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV_{CBO}	-80	-	-	V	I_C=-10uA , I_E=0
BV_{CEO}	-80	-	-	V	I_C=-10mA, I_B=0
BV_{EBO}	-5	-	-	V	I_E=-10uA, I_C=0
I_{CBO}	-	-	-50	nA	V_{CB}=-60V, I_E=0
I_{EBO}	-	-	-10	nA	V_{EB}=-5V, I_C=0
$V_{CE(sat)}1$	-	-	-150	mV	I_C=-150mA, I_B=-15mA
$V_{CE(sat)}2$	-	-	-500	mV	I_C=-500mA, I_B=-50mA
$V_{BE(sat)}1$	-	-	-900	mV	I_C=-150mA, I_B=-15mA
$V_{BE(sat)}2$	-	-	-1.1	V	I_C=-500mA, I_B=-50mA
$h_{FE}1$	75	-	-		V_{CE}=-5V, I_C=-0.1mA
$h_{FE}2$	100	-	-		V_{CE}=-5V, I_C=-100mA
$h_{FE}3$	70	-	-		V_{CE}=-5V, I_C=-500mA
$h_{FE}4$	25	-	-		V_{CE}=-5V, I_C=-1A
fT	100	-	-	MHz	V_{CE}=-10V, I_C=-50mA, f=100MHz
Cob	-	-	20	pF	V_{CB}=-10V, I_E=0, f=1MHz

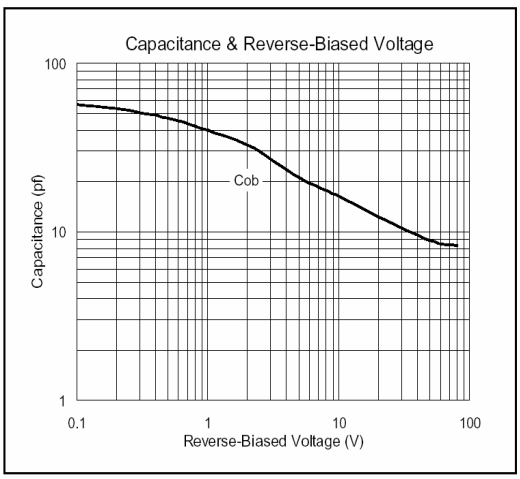
*** Pulse Test: Pulse Width≦380μs, Duty Cycle≦2%**

Characteristics Curve









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