GBS-9280FG (RoHS Compliant)

5V / 1550 nm / 2.125 Gb/s Signle-Mode Gigabit Interface Converter (GBIC)

FEATURES

- Up to 2.5 Gbps Bi-directional Data Links
- Compliant with 1X / 2X Fibre Channel FC-PI 13.0
- Compliant with Gigabit Interface Converter (GBIC) Specification Rev. 5.5
- 1550 nm DFB Laser Transmitter
- 23 dB Power Budget at Least
- Hot Pluggable module with Duplex SC Connector
- Multi-rate Operation for 2.5 / 2.125 / 1.062 / 1.25 Gbps
- Link Distance up to 80 km
- SCA-2 Host Connector
- Single +5 V Power Supply
- RoHS Compliant
- Differential PECL Inputs and Outputs
- TTL RX-LOS Output
- Class 1 Laser International Safety Standard IEC-60825 Compliant

APPLICATIONS

- Switch to Switch Interface
- High Speed Interface for File Servers
- Data Storage
- Dual Rate 1.06 / 2.125 Gb/s Fibre Channel

DESCRIPTION

The GBS-9280FG series optical transceivers meet the Gigabit Interface Converter (GBIC) specification Rev. 5.5. It satisfies the optical interface specifications defined in IEEE 802.3z Drift 5.0 1000 BASE-LX for Gigabit Ethernet and 1x / 2x Fibre Channel FC-PI 13.0. This module is designed for single-mode fiber and operates at a nominal wavelength of 1550 nm. A guaranteed minimum optical link budget of 23 dB is offered which can correspond to a link distance of over 80 km (assuming worst case fiber loss of 0.25 dB/km). The transmitter section uses a multiple quantum well 1550 nm DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC. A PECL input / output logic interface is used. TTL RX-LOS output simplifies interface to external circuitry. A 20-pin SCA-2 host connector is used to connect the converter to the host system.

LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

ORDER INFORMATION

P/No.	Bit Rate (Gb/s)	FC-PI	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
GBS-9280F	2.125/1.063	200/100	80	1550	SC GBIC	0 to 70	5 to 0	-23	Yes

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Temperature	Topr	-5	70	°C	Air flow 1m/sec
Power Supply Voltage	Vcc	-0.5	6	V	
Input Voltage	---	-0.5	Vcc	V	

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units	Conditions
Power Supply Voltage	Vcc	4.75	5	5.25	V	Reference to GND.
Power Supply Current	$I_{TX} + I_{RX}$		200	300	mA	
Operating Temperature	Topr	0		70	°C	Air flow 1m/sec
Data Rate			1.0625 / 1.25 2.125 / 2.5	2.5	Gb/s	
Fiber Length on 9/125 µm SMF			80		km	@1.0625 /2.125 Gb/s

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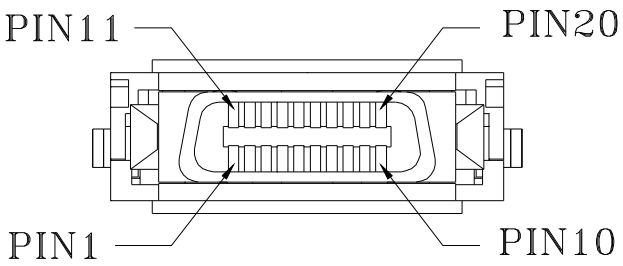
Transmitter Specifications ($0^oC < Topr < 70^oC$, 4.75 V < Vcc < 5.25V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Optical Transmit Power	Po	0	---	5	dBm	1
Output Center Wavelength	λ	1500	1550	1580	nm	
Output Spectrum Width	Δλ		---	1	nm	-20 dB Width
Extinction Ratio	E_R	9		---	dB	
Optical Rise Time	t_r			150	ps	20% to 80% value
Optical Fall Time	t_f			150	ps	20% to 80% value
Pout TX_Disable Asserted	P_{OFF}			-35	dBm	
Electrical						
Differential Input Voltage	V_{IH} - V_{IL}	0.65		2.0	Vp-p	
Transmit Fault Load	$TX\text{-}FAULT_{LOAD}$	4.7		10	kΩ	2
Transmit Fault Output-Low	$V_{TX\text{-}FAULT\text{-}L}$	0.0		0.5	V	
Transmit Fault Output-High	$V_{TX\text{-}FAULT\text{-}H}$	Vcc – 0.5		Vcc + 0.3	V	
TX-Disable Input - Low	$V_{TX\text{-}DISABLE\text{-}L}$	0		0.8	V	
TX-Disable Input - High	$V_{TX\text{-}DISABLE\text{-}H}$	2.0		Vcc + 0.3	V	
TX-Disable Assert Time	t_off			10	μs	
TX-Disable Negate Time	t-on			1	ms	
Time to initialize, includes reset of TX-FAULT	t-int			300	ms	
TX FAULT from fault to assertion	t-fault			100	μs	
TX-Disable time to start reset	t-reset	10			μs	

1. Output power is power coupled into a 9/125 μm SM fiber.
2. Pull-up resistor on host Vcc.

Receiver Specifications ($0^oC < Topr < 70^oC$, 4.75 V < Vcc < 5.25V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Sensitivity @2.125Gb/s @ $BER=10^{-12}$	Sens			-23	dBm	3
Maximum Input Power	Pin	-3		---	dBm	3
Signal detect – Hysteresis		1.0	---		dB	
Wavelength of Operation		1200	---	1600	nm	
RX-LOS – Asserted	Pa	---	---	-23	dBm	Transition: low to high
RX-LOS – Deasserted	Pd	-31	---	---	dBm	Transition: high to low
Electrical						
Differential Output Voltage	±RX-DAT	0.37		2.0	Vp-p	
Data Output Rise Time	$Tr_{RX\text{-}DAT}$			0.35	ns	
Data Output Fall Time	$Tf_{RX\text{-}DAT}$			0.35	ns	
Receiver Loss of Light Load	$RX\text{-}LOS_{LOAD}$	4.7		10	kΩ	4
Loss of Signal Output Voltage --- Low	$RX\text{-}LOS_L$	0		0.5	V	
Loss of Signal Output Voltage --- High	$RX\text{-}LOS_H$	2.4		Vcc	V	
Loss of Signal Assert Time (off to on)	$T_{A,RX\text{-}LOS}$			100	μs	
Loss of Signal Deassert Time (on to off)	$T_{D,RX\text{-}LOS}$			100	μs	

3. Minimum sensitivity and saturation levels at BER=1E-12 for a 2^7-1 PRBS.
4. Pull-up resistor on host Vcc.

PIN ASSIGNMENT



PIN	Signal Name	Sequence	Description
1	RX-LOS	2	Receiver Loss of Signal, logic high, open collector compatible, 4.7K to 10K ohm pull-up to $V_{CC}T$ on host
2	R_{GND}	2	Receiver Ground
3	R_{GND}	2	Receiver Ground
4	MOD-DEF(0)	2	TTL Low
5	MOD-DEF(1)	2	SCL Serial Clock Signal
6	MOD-DEF(2)	2	SDA Serial Data Signal
7	TX-DIABLE	2	Transmitter Disable, logic high, open collector compatible
8	T_{GND}	2	Transmitter Ground
9	T_{GND}	2	Transmitter Ground
10	TX-FAULT	2	Transmitter Fault, logic high, open collector compatible, 4.7K to 10K ohm pull-up to $V_{CC}T$ on GBIC
11	R_{GND}	1	Receiver Ground
12	-RX-DAT	1	Receiver Data Bar, Differential PECL, AC coupled
13	+RX-DAT	1	Receiver Data, Differential PECL, AC coupled
14	R_{GND}	1	Receiver Ground
15	$V_{CC}R$	2	Receiver +5V Supply
16	$V_{CC}T$	2	Transmitter +5V Supply
17	T_{GND}	1	Transmitter Ground
18	+TX-DAT	1	Transmitter Data, Differential PECL, AC coupled
19	-TX-DAT	1	Transmitter Data Bar, Differential PECL, AC coupled
20	T_{GND}	1	Transmitter Ground

A sequence value of 1 indicates that the signal is in the first group to engage during plugging of a module. A sequence value of 2 indicates that the signal is the second and last group. The two guide pins on the connector are electrically connected to the transceiver circuit ground. These two guide pins make contact with circuit prior to sequence 1 signals.

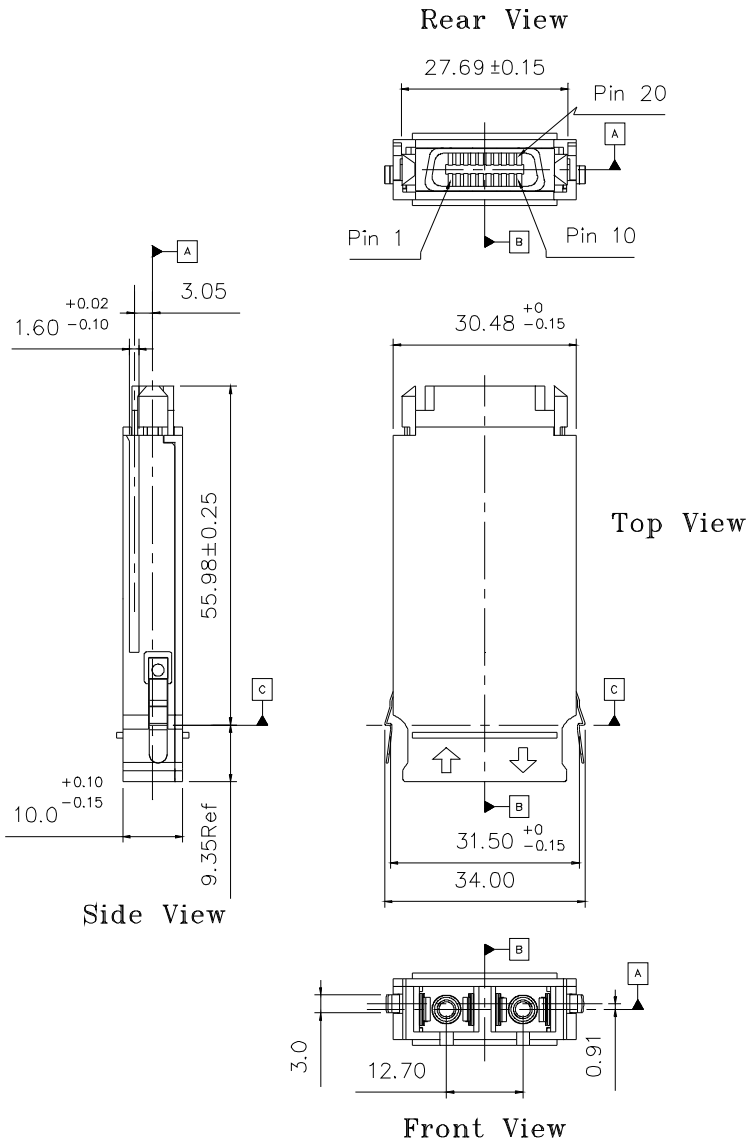
Module Definition

Module Definition	MOD-DEF(0) PIN 4	MOD-DEF (1) PIN 5	MOD-DEF (2) PIN 6	Interpretation by Host
4	TTL Low	SCL	SDA	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, MOD-DEF(1:2) appear as no connector (NC) and MOD-DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E^2PROM protocol of the ATMEL AT24C01A/02/04 family of components.

PACKAGE DIAGRAM

Units in mm



Note: Specifications subject to change without notice.