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FAIRCHILD SEMICONDUCTOR®

November 2013

FQP4N80

N-Channel QFET® MOSFET

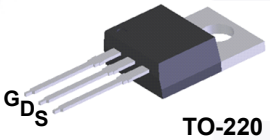
800 V, 3.9 A, 3.6 Ω

Description

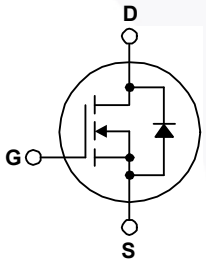
This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 3.9 A, 800 V, $R_{DS(on)}$ = 3.6 Ω (Max.) @ V_{GS} = 10 V, I_D = 1.95 A
- Low Gate Charge (Typ. 19 nC)
- Low Crss (Typ. 8.6 pF)
- 100% Avalanche Tested



TO-220



Absolute Maximum Ratings

T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQP4N80	Unit
V_{DSS}	Drain-Source Voltage		800	V
I_D	Drain Current - Continuous (T_C = 25°C)		3.9	A
	- Continuous (T_C = 100°C)		2.47	A
I_{DM}	Drain Current - Pulsed	(Note 1)	15.6	A
V_{GSS}	Gate-Source Voltage		± 30	V
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	460	mJ
I_{AR}	Avalanche Current	(Note 1)	3.9	A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	13	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.0	V/ns
P_D	Power Dissipation (T_C = 25°C)		130	W
	- Derate above 25°C		1.04	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FQP4N80	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.96	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

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Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQP4N80	FQP4N80	TO-220	Tube	N/A	N/A	50 units

Electrical Characteristics

T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 μA	800	--	--	V
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C	--	0.95	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 800 V, V_{GS} = 0 V	--	--	10	μA
		V_{DS} = 640 V, T_C = 125°C	--	--	100	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30 V, V_{DS} = 0 V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -30 V, V_{DS} = 0 V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250 μA	3.0	--	5.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS} = 10 V, I_D = 1.95 A	--	2.8	3.6	Ω
g_{FS}	Forward Transconductance	V_{DS} = 50 V, I_D = 1.95 A	--	3.8	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz	--	680	880	pF
C_{oss}	Output Capacitance		--	75	100	pF
C_{rss}	Reverse Transfer Capacitance		--	8.6	12	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 400 V, I_D = 3.9 A, R_G = 25 Ω (Note 4)	--	16	40	ns
t_r	Turn-On Rise Time		--	45	100	ns
$t_{d(off)}$	Turn-Off Delay Time		--	35	80	ns
t_f	Turn-Off Fall Time		--	35	80	ns
Q_g	Total Gate Charge	V_{DS} = 640 V, I_D = 3.9 A, V_{GS} = 10 V (Note 4)	--	19	25	nC
Q_{gs}	Gate-Source Charge		--	4.2	--	nC
Q_{gd}	Gate-Drain Charge		--	9.1	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	3.9	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	15.6	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0 V, I_S = 3.9 A	--	--	1.4	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_S = 3.9 A, dI_F / dt = 100 A/μs	--	575	--	ns
Q_{rr}	Reverse Recovery Charge		--	3.65	--	μC

Notes:
1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. L = 57 mH, I_{AS} = 3.9 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.
3. $I_{SD} \leq$ 3.9 A, di/dt ≤ 200 A/μs, $V_{DD} \leq BV_{DSS}$, starting T_J = 25°C .
4. Essentially independent of operating temperature.

Typical Characteristics

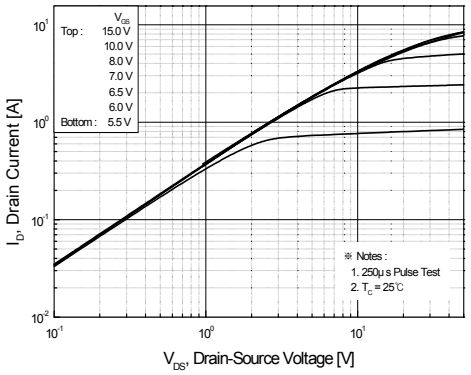


Figure 1. On-Region Characteristics

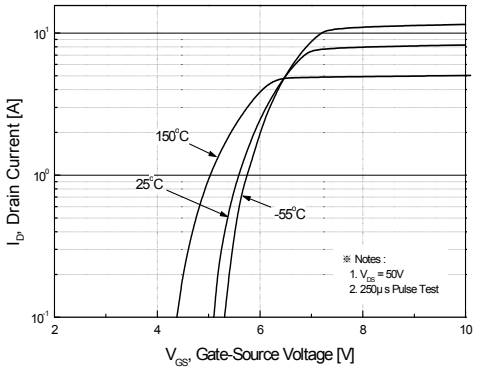


Figure 2. Transfer Characteristics

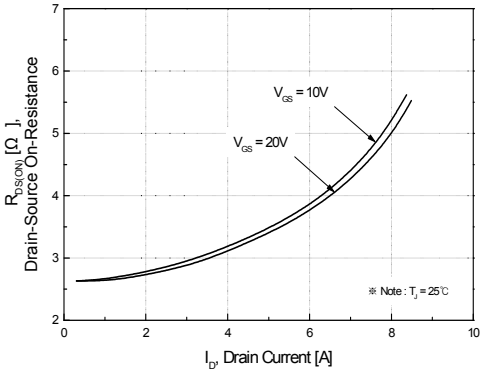


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

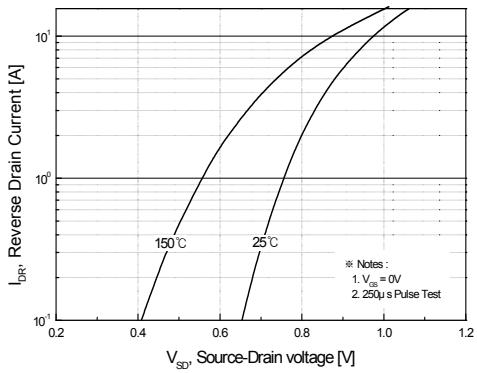


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

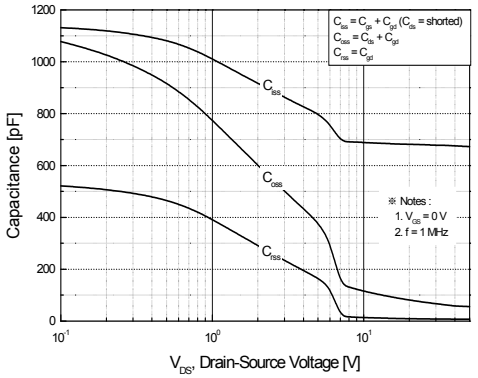


Figure 5. Capacitance Characteristics

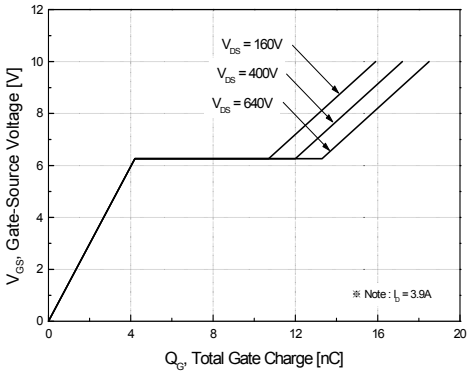


Figure 6. Gate Charge Characteristics

Typical Characteristics (continued)

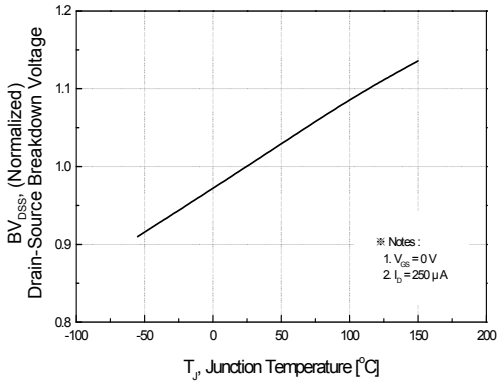


Figure 7. Breakdown Voltage Variation vs. Temperature

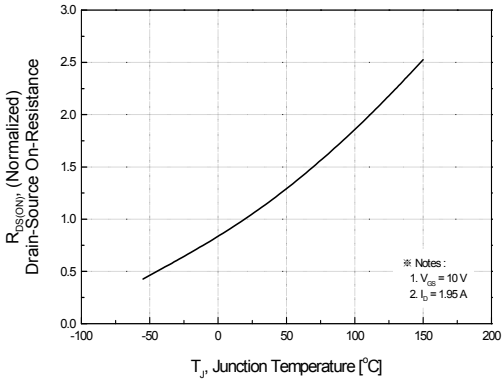


Figure 8. On-Resistance Variation vs. Temperature

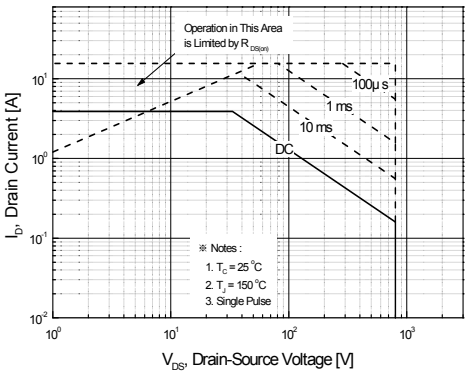


Figure 9. Maximum Safe Operating Area

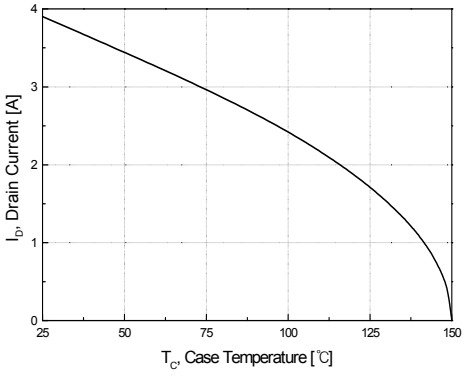


Figure 10. Maximum Drain Current vs. Case Temperature

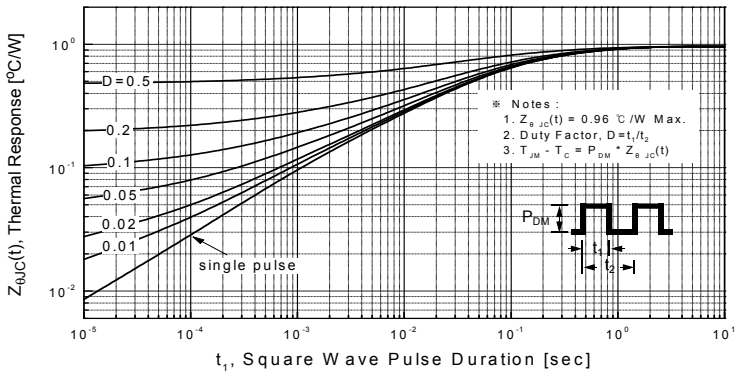


Figure 11. Transient Thermal Response Curve

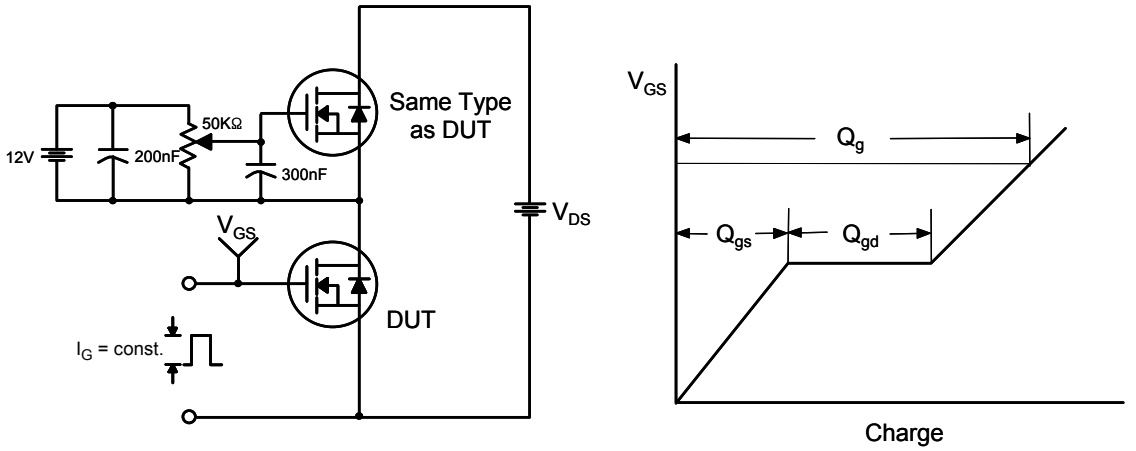


Figure 12. Gate Charge Test Circuit & Waveform

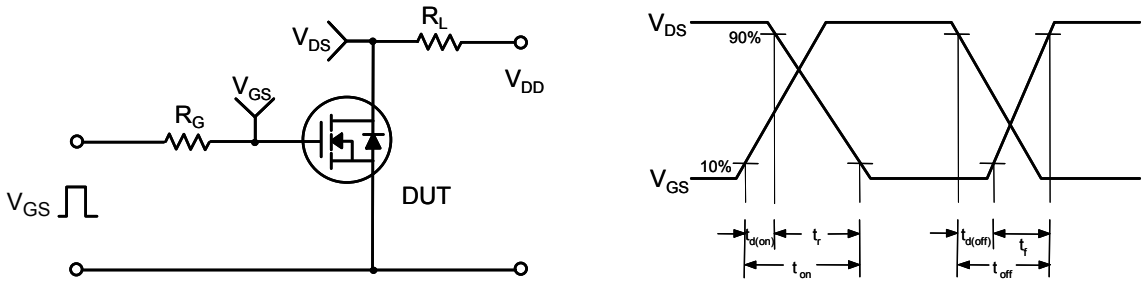


Figure 13. Resistive Switching Test Circuit & Waveforms

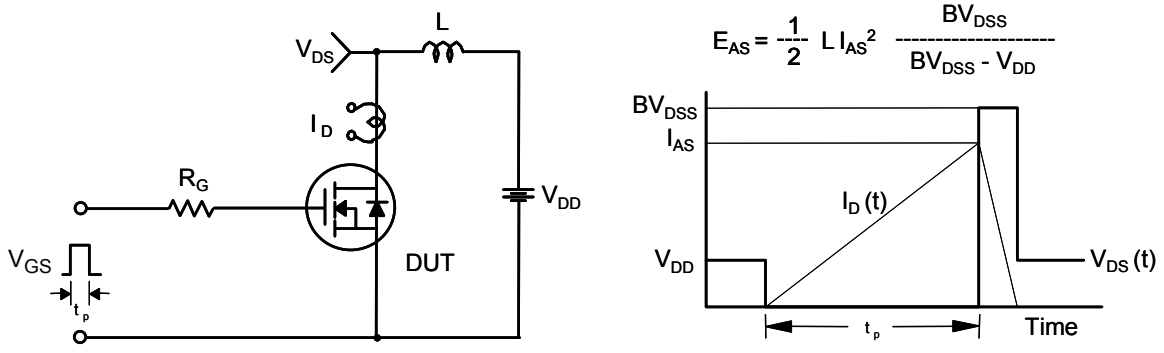


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

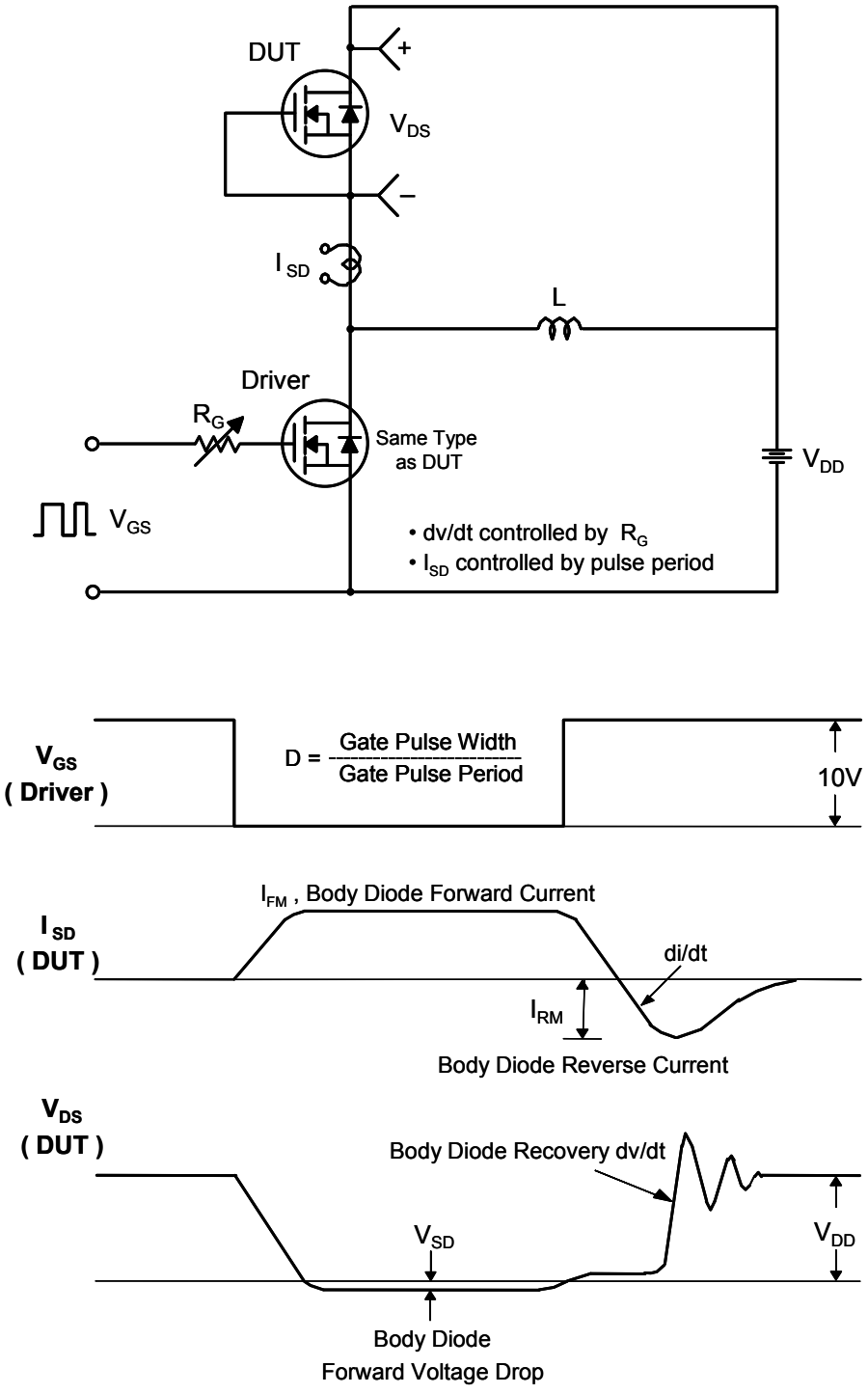


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions

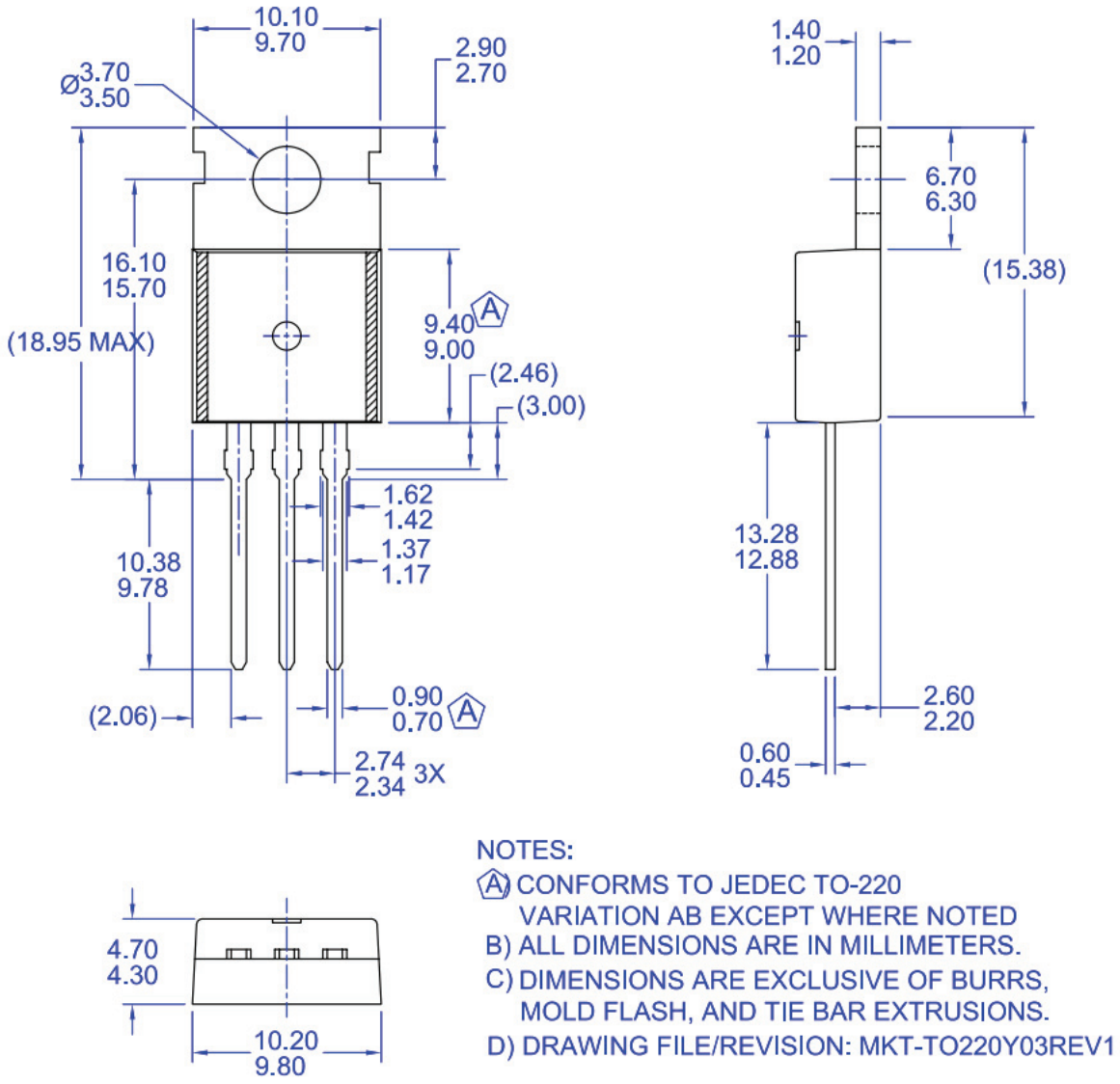


Figure 16. TO220, Molded, 3-Lead, Jedec Variation AB

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