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FAIRCHILD SEMICONDUCTOR®

November 2013

FDI150N10

N-Channel PowerTrench® MOSFET

100 V, 57 A, 16 mΩ

Features

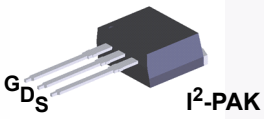
- $R_{DS(on)}$ = 12 mΩ (Typ.) @ V_{GS} = 10 V, I_D = 49 A
- Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(on)}$
- High Power and Current Handling Capability
- RoHS Compliant

Description

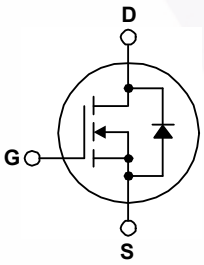
This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench® process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Micor Solar Inverter



I²-PAK



MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FDI150N10	Unit
V_{DSS}	Drain to Source Voltage		100	V
V_{GSS}	Gate to Source Voltage		±20	V
I_D	Drain Current	- Continuous (T_C = 25°C)	57	A
		- Continuous (T_C = 100°C)	40	A
I_{DM}	Drain Current	- Pulsed (Note 1)	228	A
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	132	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.5	V/ns
P_D	Power Dissipation	(T_C = 25°C)	110	W
		- Derate Above 25°C	0.88	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Thermal Characteristics

Symbol	Parameter	FDI150N10	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.13	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

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Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FDI150N10	FDI150N10	I^2-PAK	Tube	N/A	N/A	50 units

Electrical Characteristics $T_C = 25^oC$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV_{DSS}	Drain to Source Breakdown Voltage	I_D = 250 μA, V_{GS} = 0 V, T_C= 25°C	100	-	-	V
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C	-	0.1	-	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 100 V, V_{GS} = 0 V	-	-	1	μA
		V_{DS} = 100 V, V_{GS} = 0 V, T_C = 150°C	-	-	500	
I_{GSS}	Gate to Body Leakage Current	V_{GS} = ±20 V, V_{DS} = 0 V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 250 μA	2.5	-	4.5	V
$R_{DS(on)}$	Static Drain to Source On Resistance	V_{GS} = 10 V, I_D = 49 A	-	12	16	mΩ
g_{FS}	Forward Transconductance	V_{DS} = 20 V, I_D = 49 A	-	156	-	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	-	3580	4760	pF
C_{oss}	Output Capacitance		-	340	450	pF
C_{rss}	Reverse Transfer Capacitance		-	140	210	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 50 V, I_D = 49 A, V_{GS} = 10 V, R_G = 25 Ω	-	47	104	ns
t_r	Turn-On Rise Time		-	164	338	ns
$t_{d(off)}$	Turn-Off Delay Time		-	86	182	ns
t_f	Turn-Off Fall Time	(Note 4)	-	83	176	ns
$Q_{g(tot)}$	Total Gate Charge at 10V	V_{DS} = 80 V, I_D = 49 A, V_{GS} = 10 V	-	53	69	nC
Q_{gs}	Gate to Source Gate Charge		-	19	-	nC
Q_{gd}	Gate to Drain “Miller” Charge	(Note 4)	-	15	-	nC
Drain-Source Diode Characteristics						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	57	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	228	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0 V, I_{SD} = 49 A	-	-	1.3	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_{SD} = 49 A, dI_F/dt = 100 A/μs	-	41	-	ns
Q_{rr}	Reverse Recovery Charge		-	70	-	nC

Notes:

1: Repetitive rating: pulse-width limited by maximum junction temperature.

2: L = 0.11 mH, I_{AS} = 49 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.

3: I_{SD} ≤ 49 A, di/dt ≤ 200 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.

4: Essentially independent of operating temperature typical characteristics.

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Typical Performance Characteristics

Figure 1. On-Region Characteristics

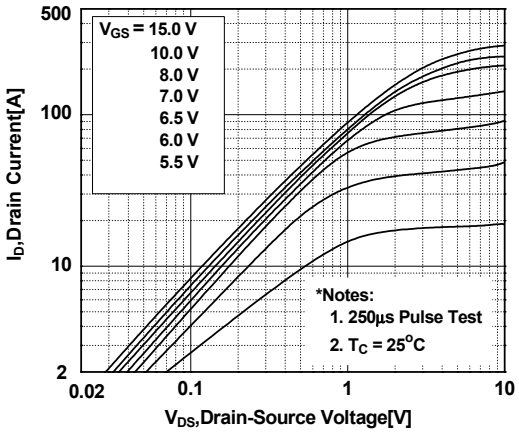


Figure 2. Transfer Characteristics

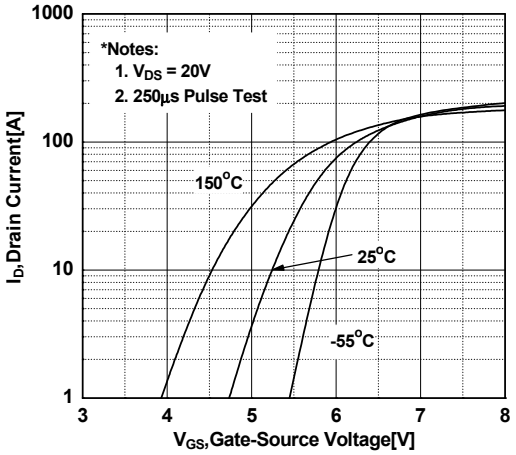


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

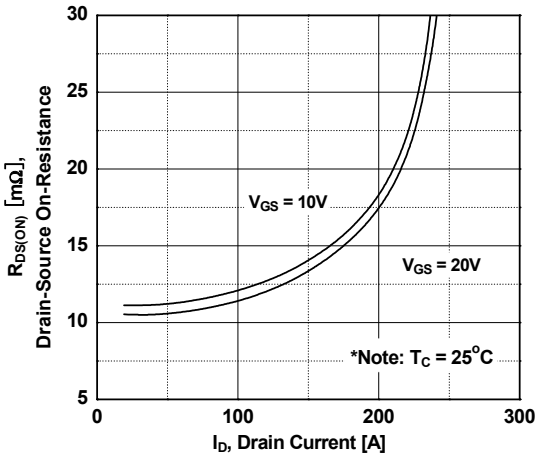


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

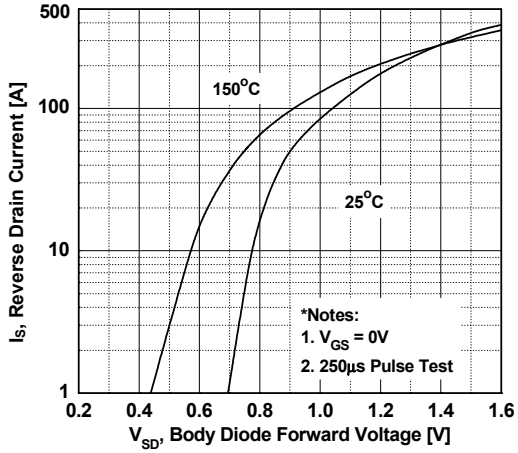


Figure 5. Capacitance Characteristics

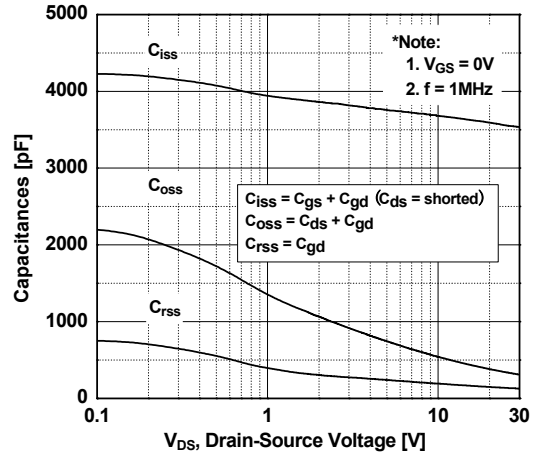
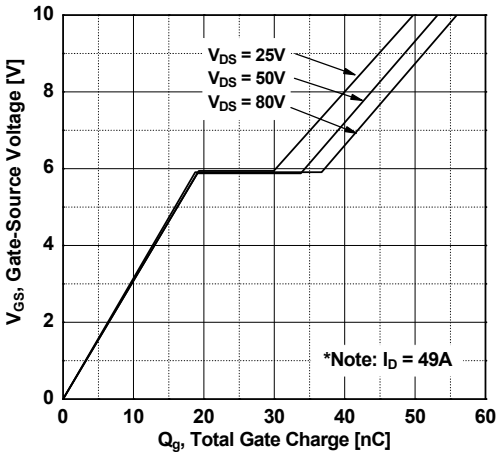


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

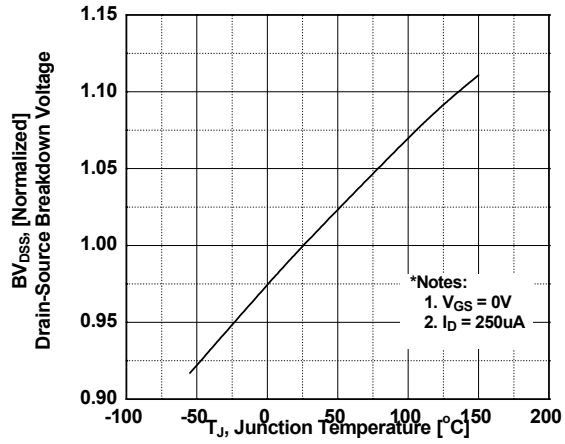


Figure 8. On-Resistance Variation vs. Temperature

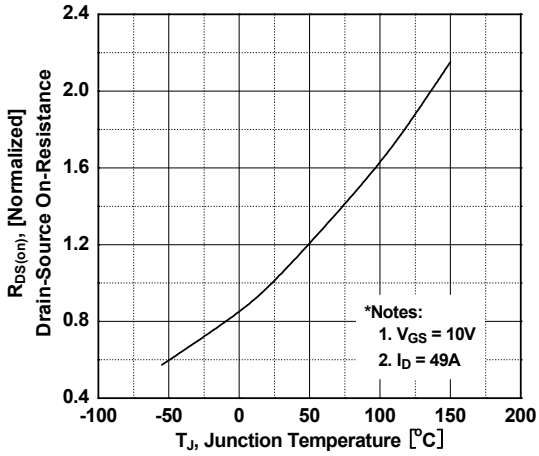


Figure 9. Maximum Safe Operating Area

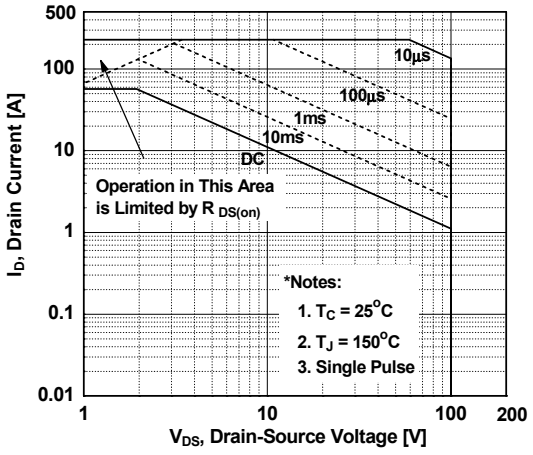


Figure 10. Maximum Drain Current vs. Case Temperature

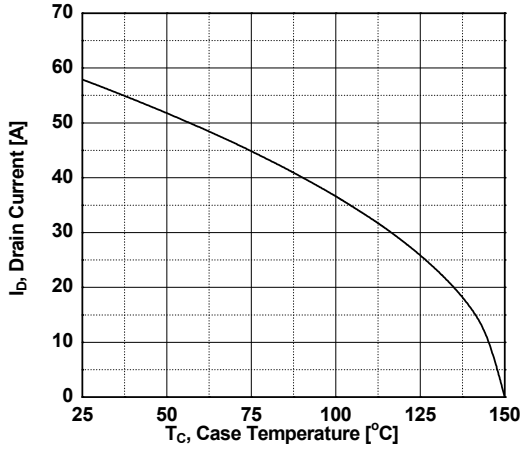
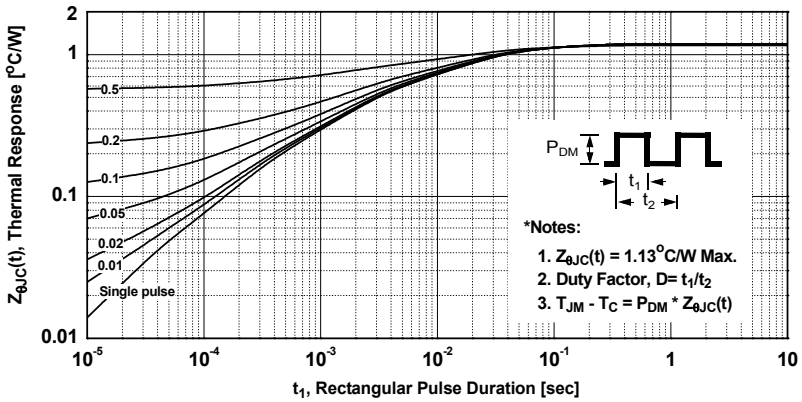


Figure 11. Transient Thermal Response Curve



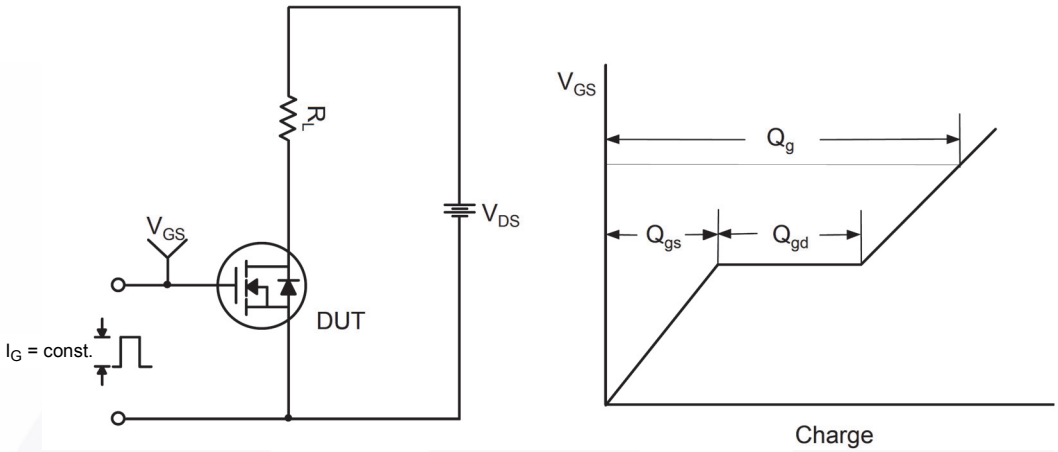


Figure 12. Gate Charge Test Circuit & Waveform

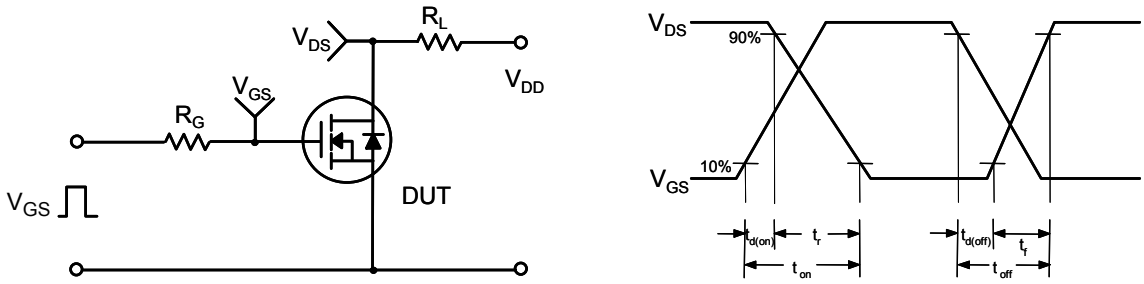


Figure 13. Resistive Switching Test Circuit & Waveforms

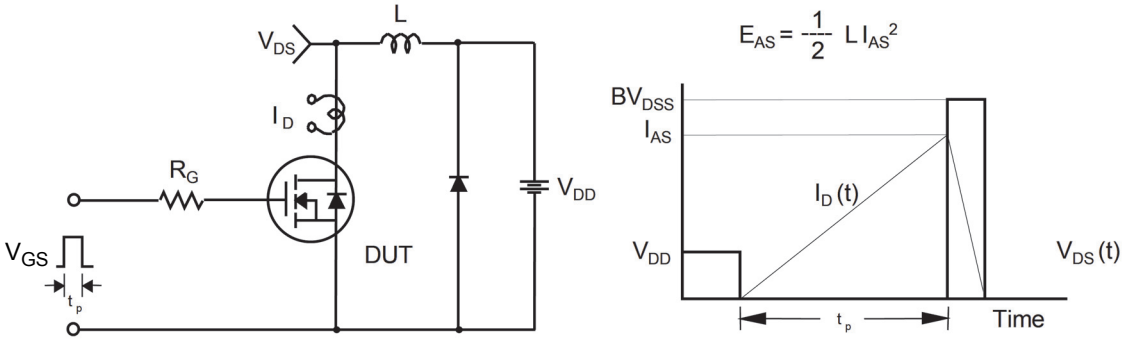


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

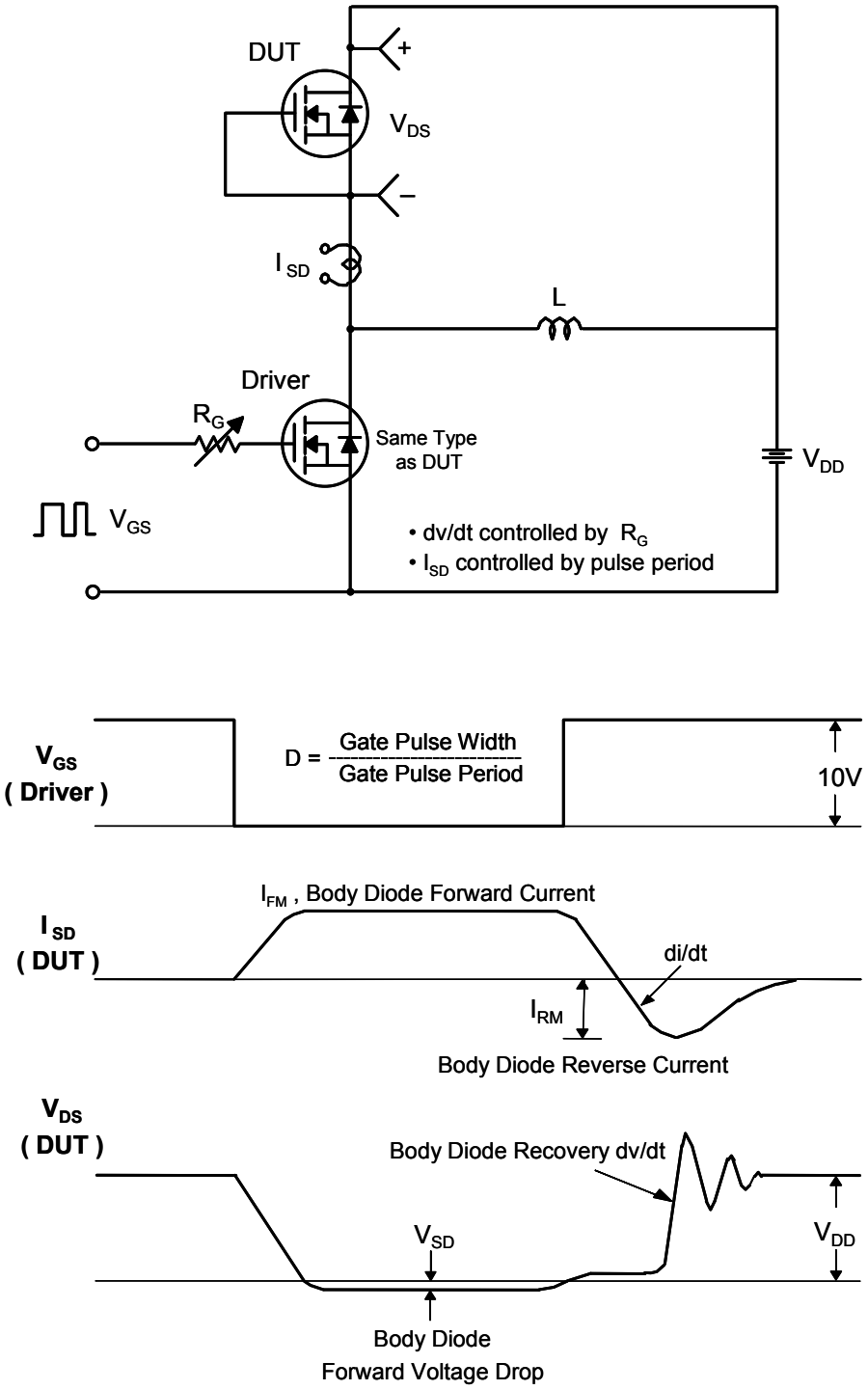
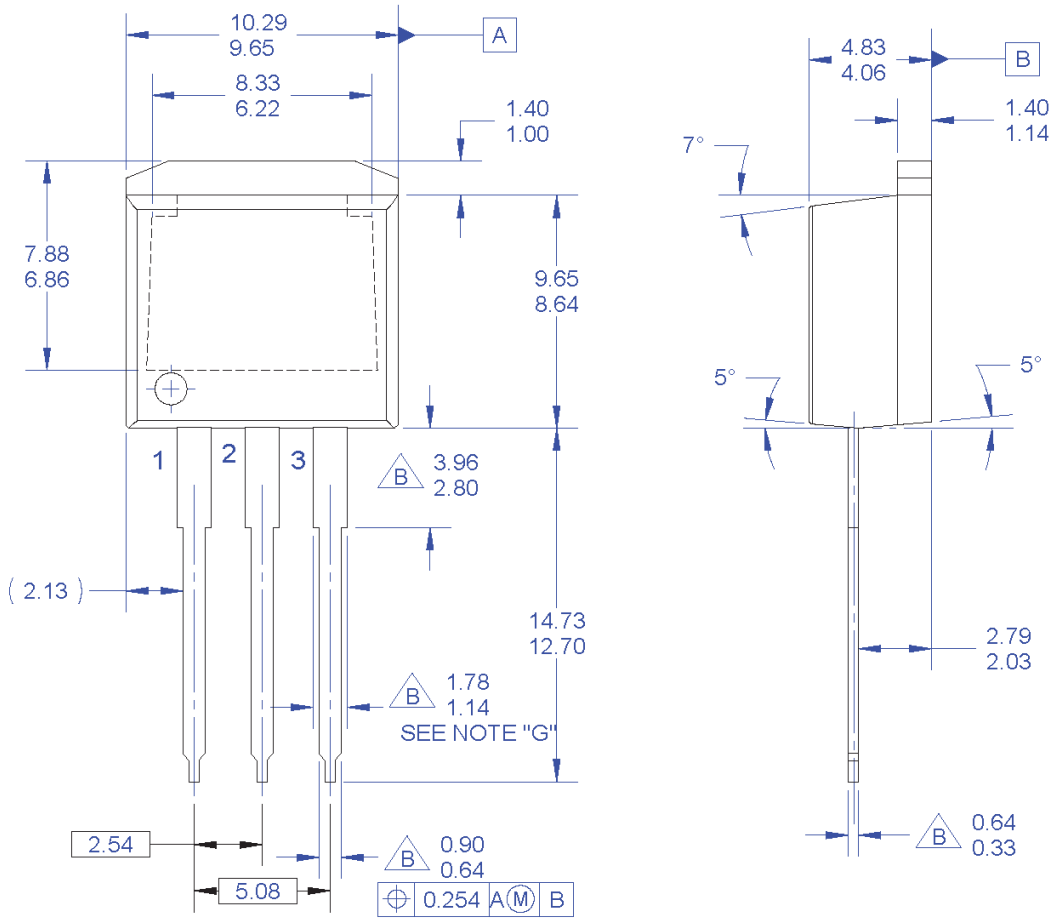


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions



NOTES:

A. EXCEPT WHERE NOTED CONFORMS TO TO262 JEDEC VARIATION AA.
B. DOES NOT COMPLY JEDEC STD. VALUE.
C. ALL DIMENSIONS ARE IN MILLIMETERS.
D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
E. DIMENSION AND TOLERANCE AS PER ANSI Y14.5-1994.
F. LOCATION OF PIN HOLE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF PACKAGE)
G. MAXIMUM WIDTH FOR F102 DEVICE = 1.35 MAX.
H. DRAWING FILE NAME: TO262A03REV5

Figure 16. TO262 (I^2PAK), Molded, 3-Lead, Jedec Variation AA

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