

Standalone Linear Li-Ion Battery Charger with Thermal Regulation in SOT-23-5 and E-SOP-8

General Description

The FC5754 is a complete linear charger for single cell lithium-ion batteries. With small SOT-23-5 package and few external components, FC5754 is well suited for portable applications. In addition, the FC5754 is specifically designed to work within USB power specifications.

No external sense resistor and blocking diode are required. Charging current can be programmed externally with a single resistor. The built-in thermal regulation facilitates charging with maximum power without risk of overheating.

The FC5754 always preconditions the battery with 1/10 of the programmed charge current at the beginning of a charge cycle, until 40 μ s after it verifies that the battery can be fast-charged. The FC5754 automatically terminates the charge cycle when the charge current drops to 1/10th the programmed value after the final float voltage is reached.

The FC5754 consumes zero reverse current from the battery at standby, shutdown and sleep modes. This feature reduces the charge and discharge cycles on the battery, further prolonging the battery life. The FC5754 can also be used as a LDO when battery is removed. Other features include shutdown mode, charge current monitor, under voltage lockout, automatic recharge and status indicator.

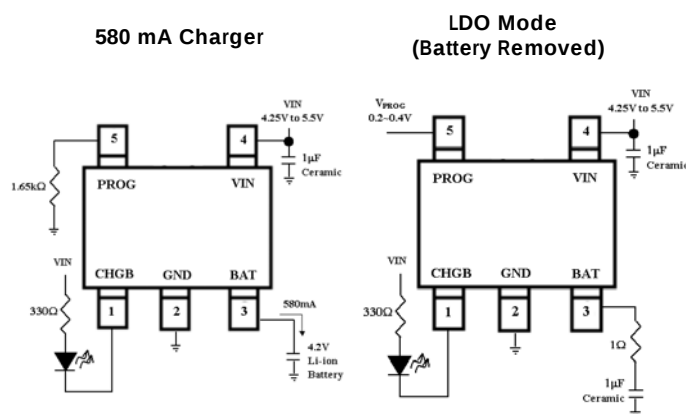
Features

- Programmable charge current up to 1A
- No MOSFET, sense resistor or blocking diode required
- Complete linear charger both in SOT-23-5 and E-SOP-8 for single cell Lithium-ion batteries
- Typical zero reverse current from battery
- Thermal regulation maximizes charge rate without risk of overheating
- Charges single cell Li-ion batteries directly from USB port
- Act as a LDO when battery is removed
- Preset 4.2V/4.35V charge voltage with $\pm 1\%$ accuracy
- Automatic recharge
- Charge status indicator
- C/10 charge termination
- 45 μ A shutdown supply current
- 2.9V trickle charge threshold
- Soft-start limits inrush current
- RoHS compliant and Pb-free

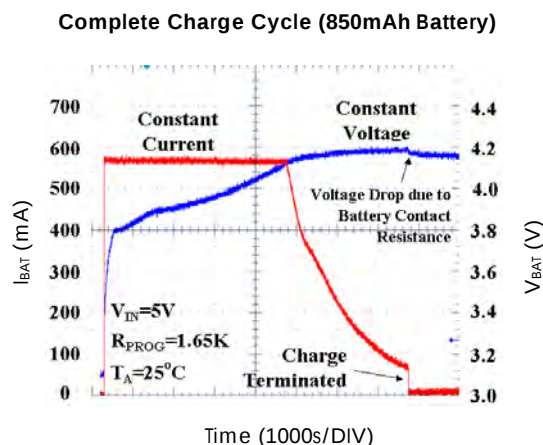
Applications

- Wireless handsets
- Hand-held instruments
- Portable information appliances
- Bluetooth application
- Charging docks and cradles

Typical Application Diagram



Typical Performance Characteristics



Pin Configuration

SOT-23-5(Top View)

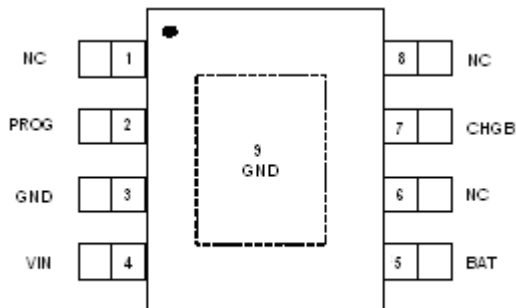


Order information

FC5754--XXVF05GRR

XX Charger Voltage
VF05 SOT-23-5 Package
GRR RoHS &Halogen free
Rating: -40 to 85°C
Package in Tape & Reel

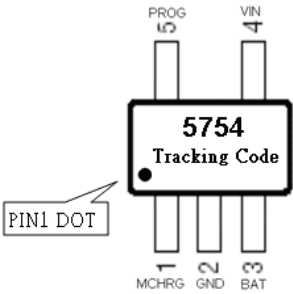
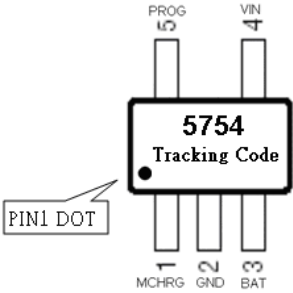
E-SOP-8(Top View)



FC5754-xxSG08NRR

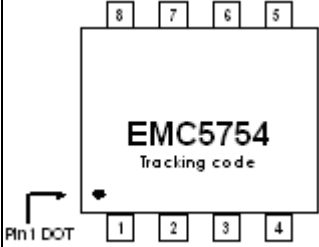
xx Charger Voltage
SG08 E-SOP-8 Package
NRR RoHS & Halogen free package
Rating: -40 to 85°C
Package in Tape & Reel

Order, Mark & Packing Information

Charge Voltage	Package	Marking	Product ID	Packing
4.2V	SOT-23-5		FC5754-00VF05GRR	3K units Tape & Reel
4.35V	SOT-23-5		FC5754-4DVF05GRR	3K units Tape & Reel



FC5754

4.2V	E-SOP-8		FC5754-00SG08NRR	3K units Tape & Reel
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Pin Functions

Name	E-SOP-8	SOT-23-5	Function
NC	1,6,8	NA	Not connected.
PROG	2	5	Charge Current Program, Charge Current Monitor and Shutdown Pin. The charge current is programmed by connecting a 1%resistor, R_{PROG}, to ground. When charging in constant-current mode, this pin serves to 1V. In all modes, the voltage on this pin can be used to measure the charge current using the following formula: $I_{BAT} = (V_{PROG} / R_{PROG}) * 960$ The PROG pin can also be used to shut down the charger. Disconnecting the program resistor from ground allows a 0.4μA current to pull the PROG pin high. When it reaches the 1.18V shutdown threshold voltage, the charger enters shutdown mode. This pin is also clamped to approximately 2.4V. Reconnecting R_{PROG} to ground will return the charger to normal operation. Connecting the PROG pin to a voltage between 0.2V and 0.4V can force FC5754 into LDO mode. The PROG pin must not be directly shorted to ground at any condition.
GND	3	2	Ground
VIN	4	4	Positive Input Supply Voltage. Provides power to the charger. VIN can range from VBAT+0.5V to 5.5V and should be bypassed with at least a 1 μ F capacitor. When VIN drops to within 30mV above the BAT pin voltage, the FC5754 enters shutdown mode, dropping I_{BAT} to less than 1 μ A.
BAT	5	3	Charge Current Output and battery voltage feedback. This pin provides charge current to the battery and regulates the final float voltage to 4.2V/4.35V. An internal precision resistor divider from this pin sets the float voltage which is disconnected in standby, shutdown and sleep modes.
CHGB	7	1	Open-Drain Charge Status Output. An internal N-channel MOSFET connects CHGB pin to ground when the battery is charging. After the charge cycle is completed, the internal N-channel MOSFET is replaced by a weak pull-down of approximately 24 μ A, indicating an “AC present” condition. When the FC5754 detects an under voltage lockout condition, CHGB is forced high impedance.
GND	9	NA	Thermal ground area.



FC5754

Absolute Maximum Ratings (Notes 1, 2)

V_{IN} , V_{BAT} , V_{CHGB}	-0.3V to 6.0V	Junction Temperature (T_J)	150°C
V_{PROG}	(Note 3) -0.3V to $V_{IN} + 0.3V$	Lead Temperature (10 sec.)	260°C
BAT Short-Circuit Duration	Continuous	Thermal Resistance (θ_{JA})	
BAT Pin Current	1A	SOT-23-5	(Note 5)
PROG Pin Current	1mA	E-SOP-8L	75°C/W
Storage Temperature Range	-65°C to 160°C		

Operating Ratings

Temperature Range	-40°C to 85°C
Supply Voltage	$V_{BAT} + 0.5V$ to 5.5V

Note: Devices are ESD sensitive. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the device.

Electrical Characteristics

Unless otherwise specified, $T_A = 25^\circ\text{C}$ and $V_{IN} = 5V$.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{IN}	Input Voltage	$V_{BAT} = 4.2V$	4.25		5.5	V
		$V_{BAT} = 4.35V$	4.4		5.5	
I_{CC}	Input Supply Current	Charge Mode, $R_{PROG} = 10K$ (Note 4)	150	260	330	μA
		Standby Mode (Charge Terminated)	50	106	150	μA
		Shutdown Mode (R_{PROG} Not Connected, $V_{IN} < V_{BAT}$ or $V_{IN} < V_{UV}$)	20	45	75	μA
V_{FLOAT}	Regulated Output (Float) Voltage	$0^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$ ($V_{FLOAT} = 4.2V$)	4.158	4.2	4.242	V
		$0^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$ ($V_{FLOAT} = 4.35V$)	4.3065	4.35	4.3935	V
I_{BAT}	BAT Pin Current	$R_{PROG} = 2K$, Current Mode	447	480	513	mA
		Standby Mode, $V_{BAT} = 4.2V$	-1	0	1	
		Shutdown Mode (R_{PROG} Not Connected)	-1	0	1	μA
		Sleep Mode, $V_{IN} = 0V$	-1	0	1	μA
$I_{TRICKLE}$	Trickle Charge Current	$V_{BAT} < V_{TRICKLE}$, $R_{PROG} = 2K$	21	50	75	mA
$V_{TRICKLE}$	Trickle Charge Threshold Voltage	$R_{PROG} = 10K$, V_{BAT} Rising	2.8	2.9	3.0	V
V_{TRHYS}	Trickle Charge Hysteresis Voltage	$R_{PROG} = 10K$		210		mV
V_{UV}	V_{IN} Under voltage Lockout Threshold	From V_{IN} Low to High	2.45	3.0	3.75	V
V_{UVHYS}	V_{IN} Under voltage Lockout Hysteresis			180		mV
V_{MSD}	Manual Shutdown Threshold Voltage	PROG Pin Rising		1.18	1.25	V
		PROG Pin Falling	1.00	1.04		V
V_{ASD}	$V_{IN} - V_{BAT}$ Lockout Threshold Voltage	V_{IN} from Low to High		80	140	mV
		V_{IN} from High to Low	5	30		mV



FC5754

I_{TERM}	C/10 Termination Current Threshold	$R_{PROG}=10K$		0.1		mA/mA
V_{PROG}	PROG Pin Voltage	$R_{PROG}=10K$, Current Mode	0.93	1.0	1.07	V
I_{CHGB}	CHGB Pin Weak Pull-Down Current	$V_{CHGB}=5V$		24		μA
V_{CHGB}	CHGB Pin Output Low Voltage	$I_{CHGB}=5mA$		0.23	0.6	V
V_{RECHRG}	Recharge Battery Threshold Voltage	$V_{FLOAT}-V_{RECHRG}$		160		mV
T_{ILM}	Junction Temperature in Constant Temperature Mode			120		$^{\circ}C$
R_{ON}	Power FET "ON" Resistance			550		$m\Omega$
T_{SS}	Soft-Start Time	$I_{BAT}=0$ to $I_{BAT}=960V/R_{PROG}$		100		μs
$T_{RECHARGE}$	Recharge Comparator Filter Time	V_{BAT} High to Low	0.75	2.4	4.5	ms
T_{TERM}	Termination Comparator Filter Time	I_{BAT} Falling Below $I_{CHG}/10$	0.4	1.1	2.5	ms
I_{PROG}	PROG Pin Pull-up Current			0.4		μA

Note 1: Absolute Maximum ratings indicate limits beyond which damage may occur. Electrical specifications are not applicable when the device is operated outside of its rated operating conditions.

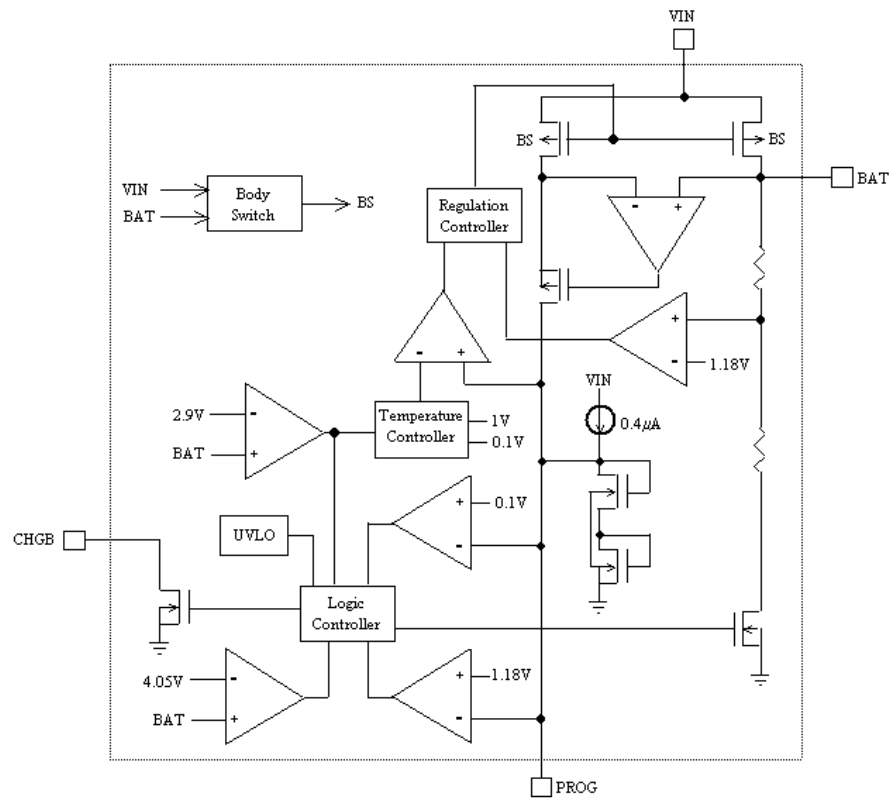
Note 2: All voltages are defined and measured with respect to the potential at the ground pin.

Note 3: Can not exceed 6.0V.

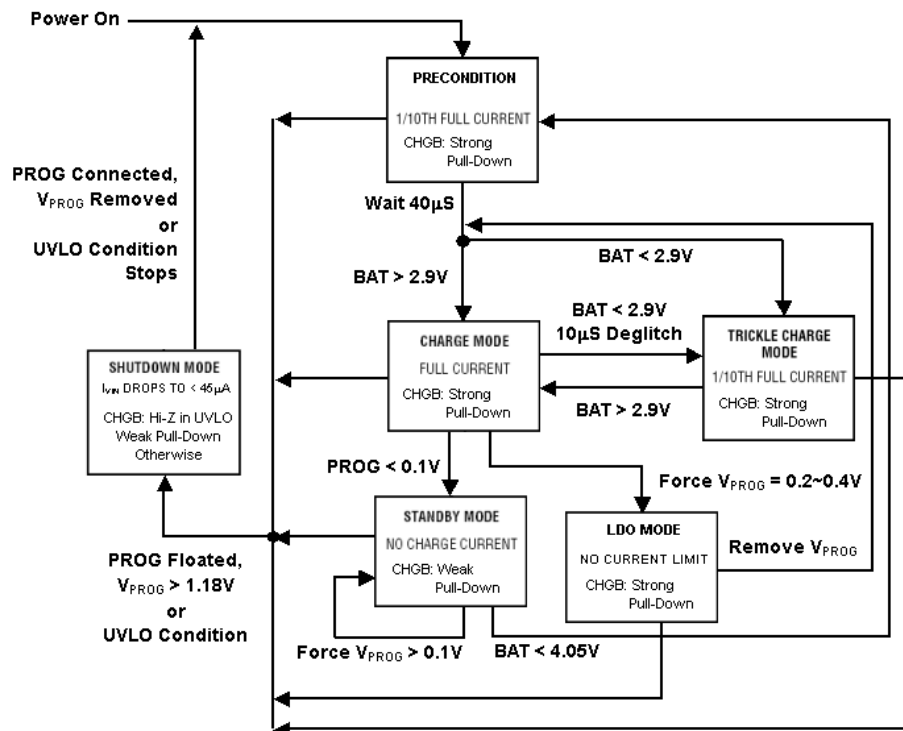
Note 4: Supply current includes PROG pin current (approximately $100\mu A$) but does not include any current delivered to the battery through the BAT pin (approximately 96mA).

Note 5: $80^{\circ}C/W$ to $150^{\circ}C/W$, depending on PCB Layout.

Functional Block Diagram

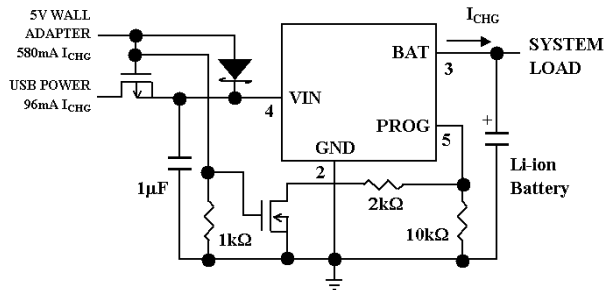


State Diagram

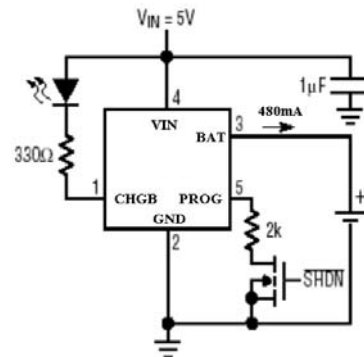


Typical Applications

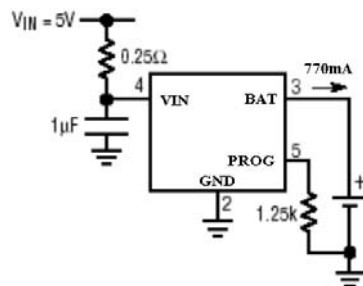
USB/Wall Adapter Power Li-ion Charger



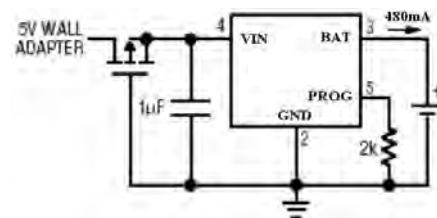
Full Featured Single Cell Li-ion Charger



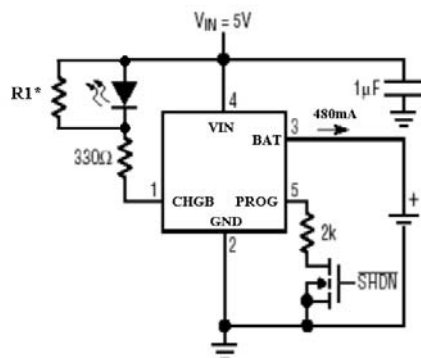
Li-ion Charger with External Power Dissipation



Basic Li-ion Charger with Reverse Polarity Input Protection



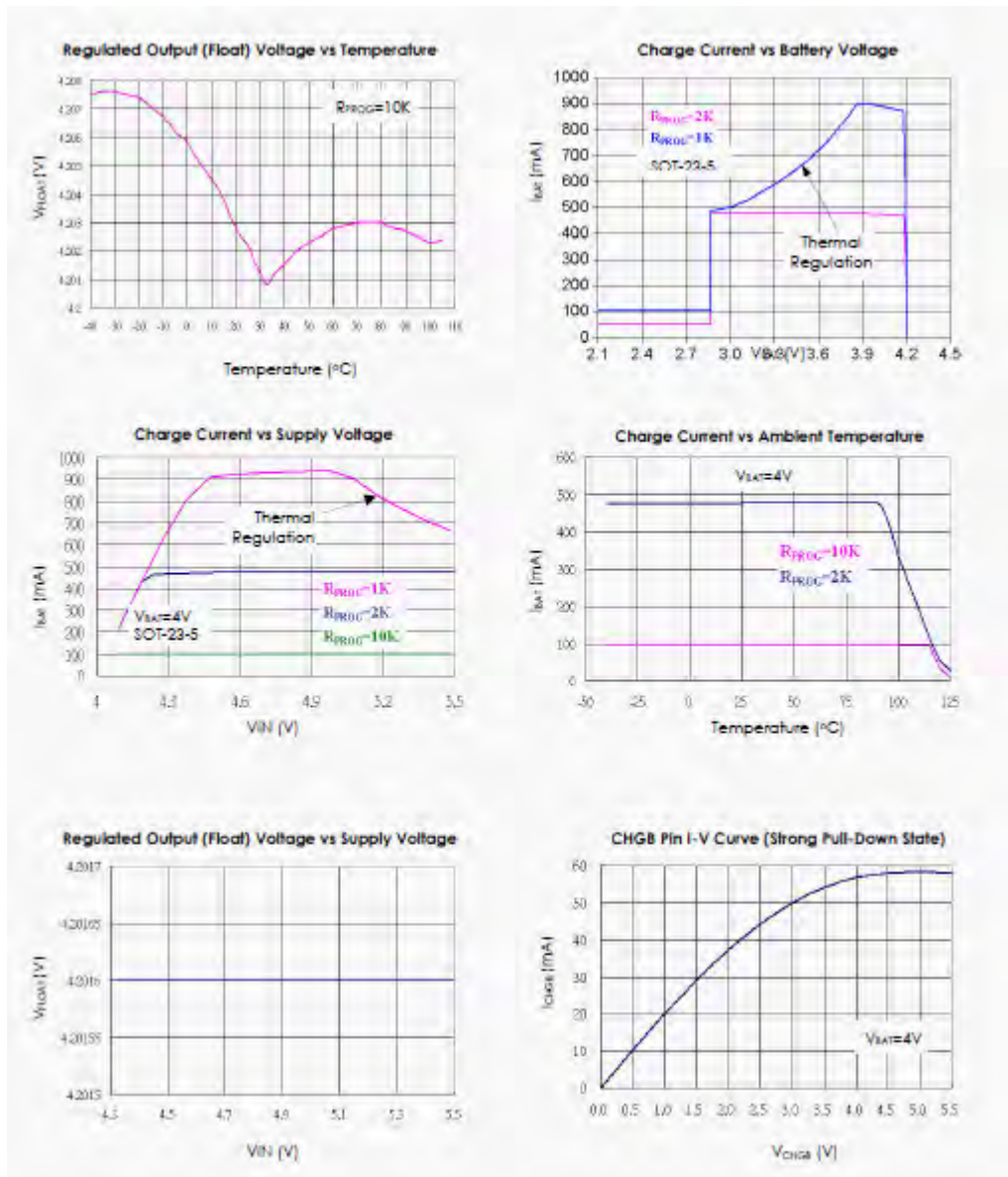
Turn off the LED after Charge is Terminated



*The formula for R1 is $R1 < \frac{V1}{35\mu A}$, where V1 is the turn-on threshold voltage of the LED device.

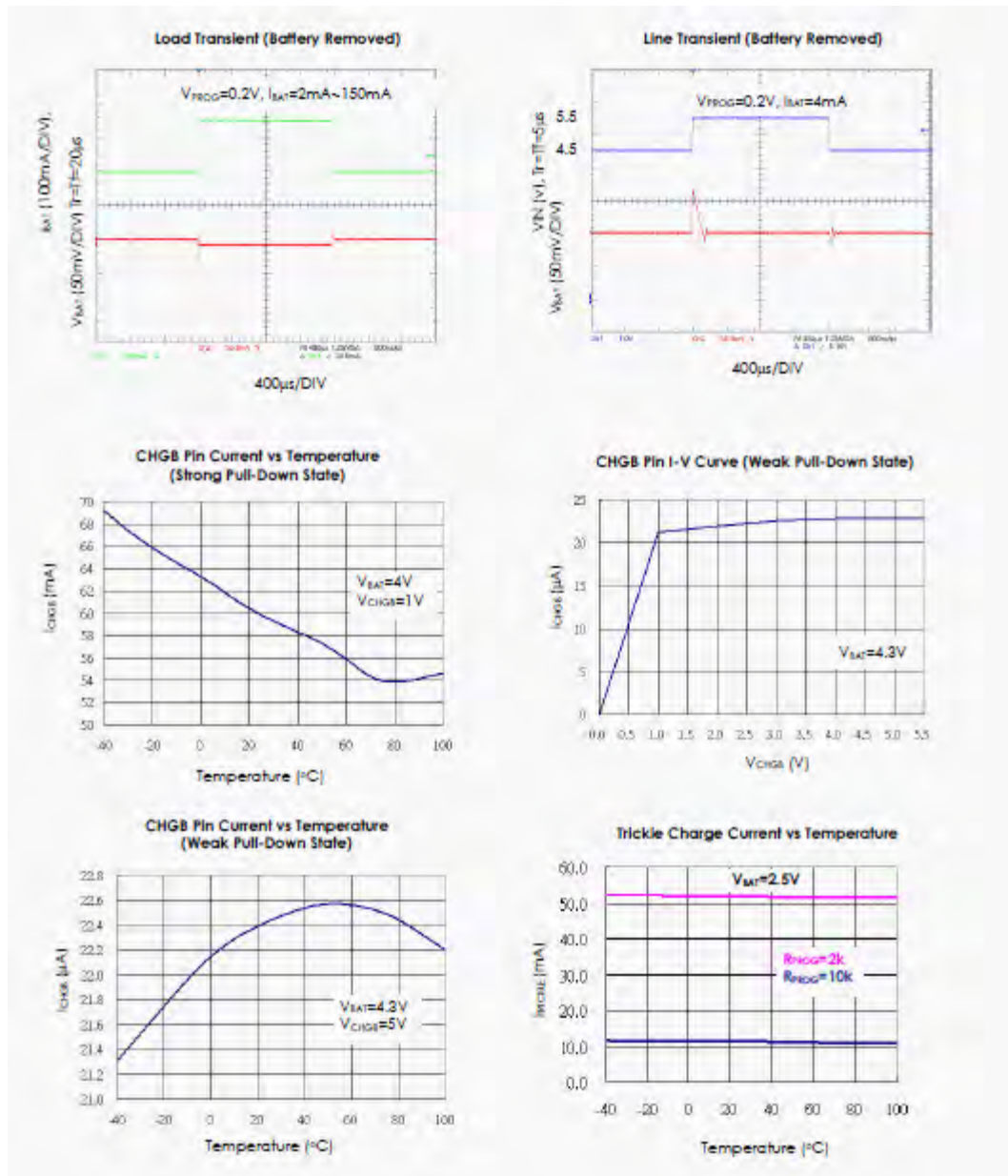
Typical Performance Characteristics

Unless otherwise specified, $V_{IN} = 5V$, $T_A = 25^\circ C$.



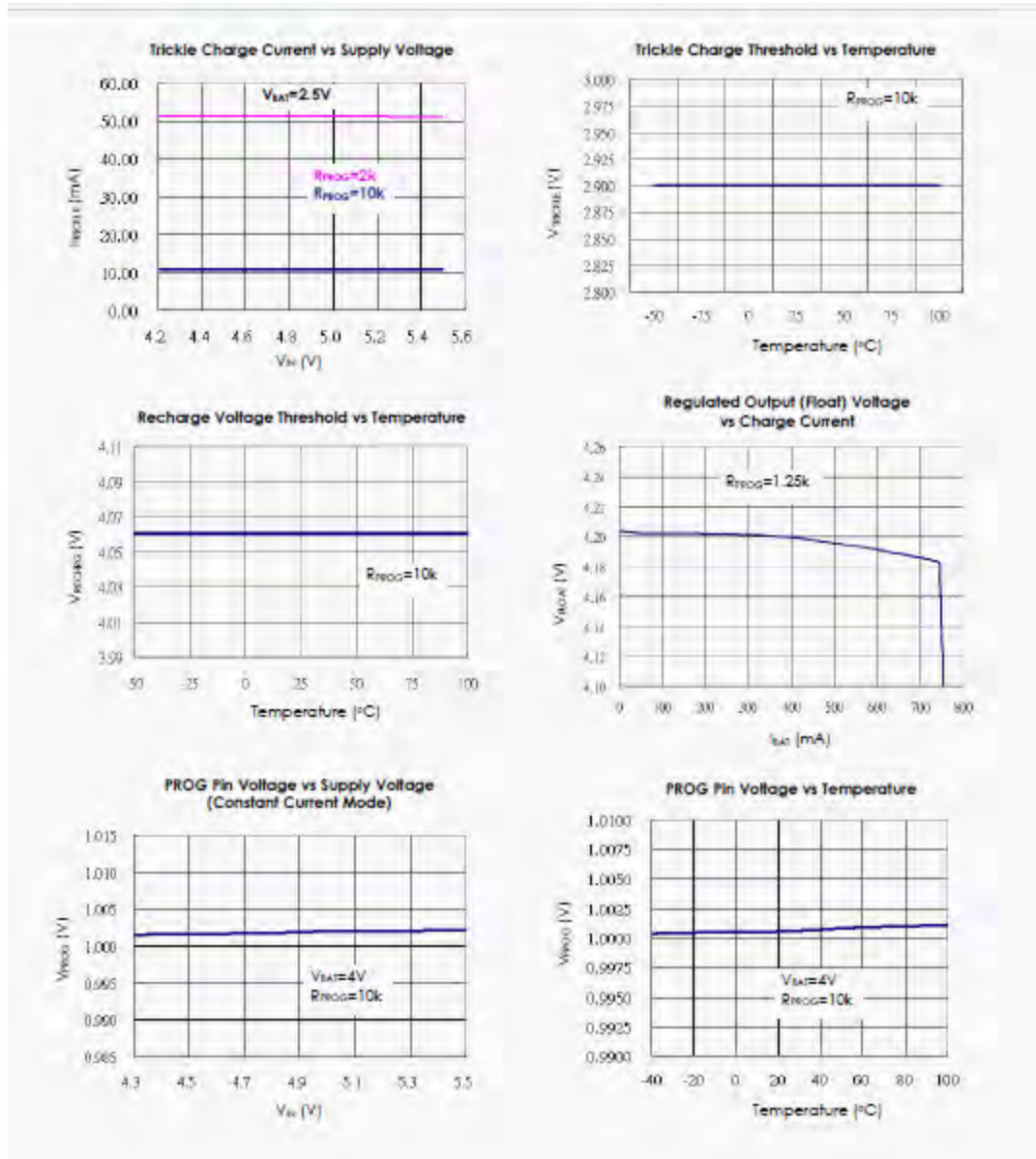
Typical Performance Characteristics

Unless otherwise specified, $V_{IN} = 5V$, $T_A = 25^\circ C$. (Continued)



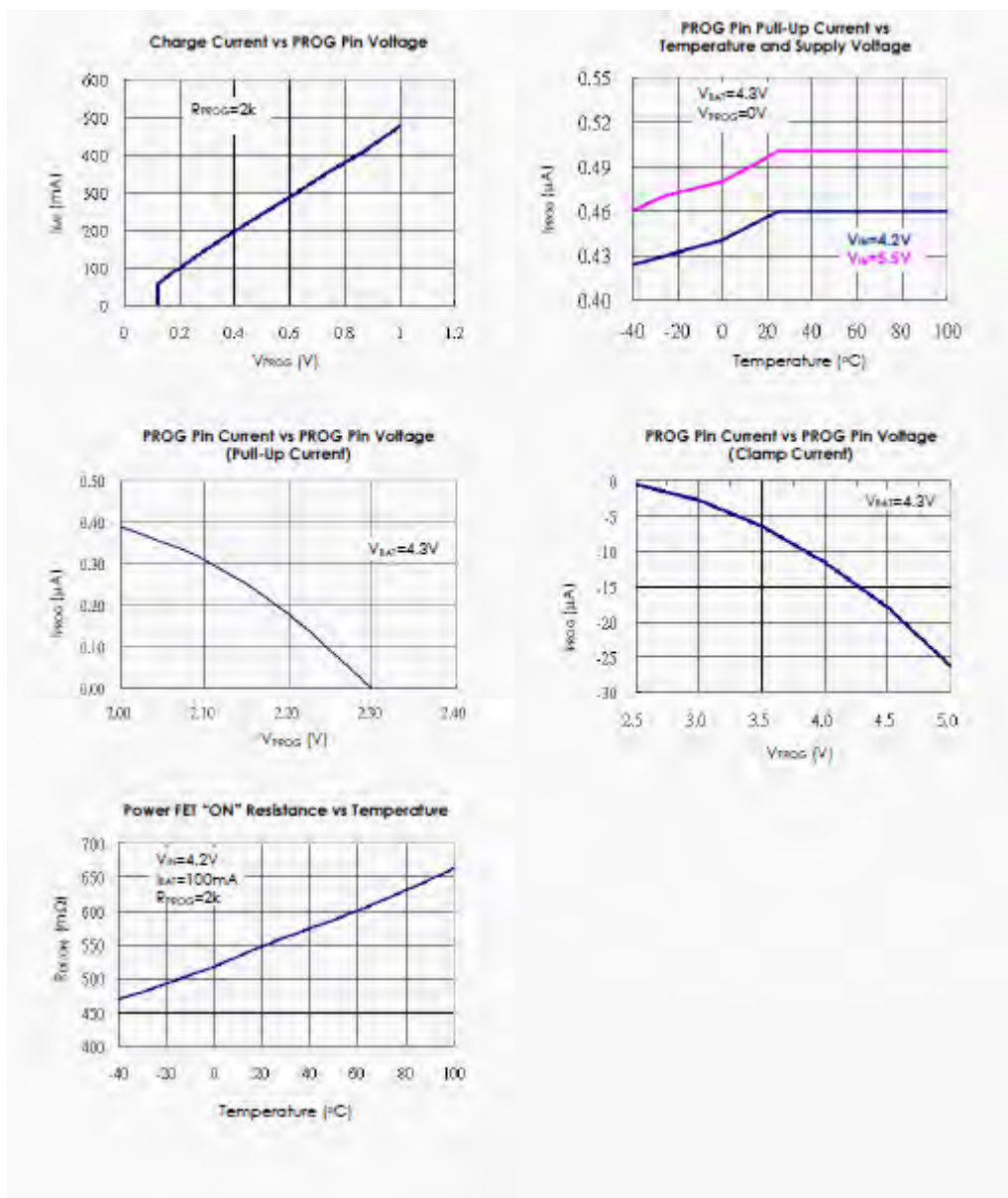
Typical Performance Characteristics

Unless otherwise specified, $V_{IN} = 5V$, $T_A = 25^\circ C$. (Continued)



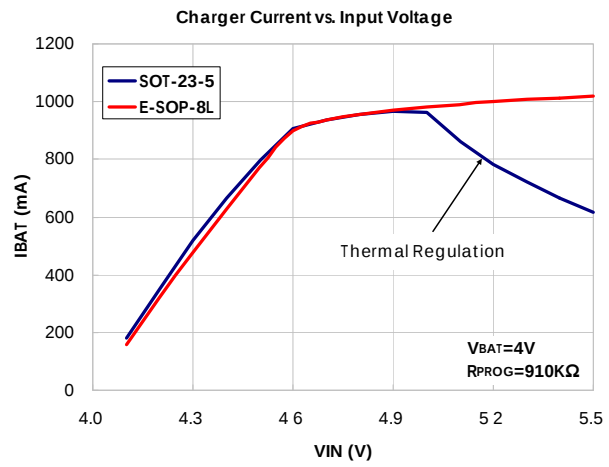
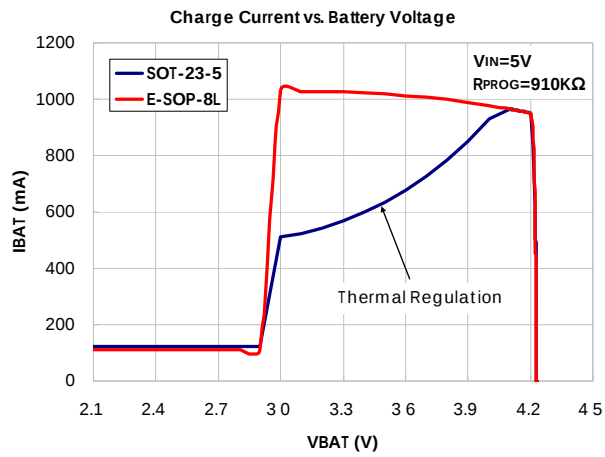
Typical Performance Characteristics

Unless otherwise specified, $V_{IN} = 5V$, $T_A = 25^\circ C$. (Continued)



Typical Performance Characteristics

Unless otherwise specified, $V_{IN} = 5V$, $T_A = 25^\circ C$.





Operation

The FC5754 is a single cell lithium-ion battery charger using a constant-current/constant-voltage algorithm. It can deliver up to 1A of charge current (using a good thermal PCB layout) with a final float voltage accuracy of $\pm 1\%$. The FC5754 includes an internal P-channel power MOSFET and thermal regulation circuitry. No blocking diode or external current sense resistor is required; thus, the basic charger circuit requires only two external components. Furthermore, the FC5754 is capable of operating from a USB power source.

Normal Charge Cycle

A charge cycle begins when the voltage at the VIN pin rises above the UVLO threshold level and a 1% program resistor is connected from the PROG pin to ground or when a battery is connected to the charger output. If the BAT pin is less than 2.9V, the charger enters trickle charge mode. In this mode, the FC5754 supplies approximately 1/10 the programmed charge current to bring the battery voltage up to a safe level for full current charging. When the BAT pin voltage rises above 2.9V, the charger enters constant-current mode, where the programmed charge current is supplied to the battery. When the BAT pin approaches the final float voltage (4.2V), the FC5754 enters constant-voltage mode and the charge current begins to decrease. When the charge current drops to 1/10 of the programmed value, the charge cycle ends.

Programming Charge Current

The charge current is programmed using a single resistor from the PROG pin to ground. The battery charge current is 960 times the current out of the PROG pin. The program resistor and the charge current are calculated using the following equations:

$$R_{PROG} = \frac{960V}{I_{CHG}}, \quad I_{CHG} = \frac{960V}{R_{PROG}}$$

The charge current out of the BAT pin can be determined at any time by monitoring the PROG pin voltage using the following equation:

$$I_{BAT} = \frac{V_{PROG}}{R_{PROG}} * 960$$

Charge Termination

A charge cycle is terminated when the charge current falls to 1/10th the programmed value after the final float voltage is reached. This condition is detected by using an internal, filtered comparator to monitor the PROG pin. When the PROG pin voltage falls below 100mV for longer than T_{TERM} (typically 1.1ms), charging is terminated. The charge current is latched off and the FC5754 enters standby mode, where the input supply current drops to 106 μ A. (Note: C/10 termination is disabled in trickle charging and thermal limiting modes). **The FC5754 draws no current from the battery in standby mode.** This feature reduces the charge and discharge cycles on the battery, further prolonging the battery life.

Any external source (V_{PROG}) that holds the PROG pin

above 100mV will prevent the FC5754 from terminating a charge cycle. However, if the PROG pin is controlled by external source, current sourcing from the BAT pin can be infinity (until the internal power MOSFET is burned out or the BAT pin voltage is close to its final float voltage), and the formula for charge current is not valid anymore. Therefore, controlling the PROG pin by external source below 1.1V should be avoided when a battery is connected to BAT pin. However, with no battery present, forcing V_{PROG} to 0.2V~0.4V enables FC5754 to act as a LDO (LDO mode). In the LDO mode, load current at the BAT pin is recommended to be below 150mA to avoid overheating.

When charging, transient loads on the BAT pin can cause the PROG pin to fall below 100mV for short periods of time before the DC charge current has dropped to 1/10th the programmed value. The 1.1ms filter time (T_{TERM}) on the termination comparator ensures that transient loads of this nature do not result in premature charge cycle termination. Once the average charge current drops below 1/10th the programmed value, the FC5754 terminates the charge cycle and ceases to provide any current through the BAT pin. This is the standby mode, and all loads on the BAT pin must be supplied by the battery. In the standby mode, any signal below the manual shutdown threshold voltage (typically 1.18V) on the PROG pin is transparent to FC5754.

The FC5754 constantly monitors the BAT pin voltage in standby mode. If this voltage drops below the 4.05V recharge threshold (V_{RECHRG}), another charge cycle begins and current is once again supplied to the battery. To manually restart a charge cycle when in standby mode, the input voltage must be removed and reapplied, or the charger must be shut down and restarted using the PROG pin.

Charge Status Indicator (CHGB)

The charge status output has three different states: strong pull-down (~10mA), weak pull-down (~24 μ A) and high impedance. The strong pull-down state indicates that the FC5754 is in a charge cycle. Once the charge cycle has terminated, the pin state is determined by undervoltage lockout conditions. A weak pull-down indicates that VIN meets the UVLO conditions and the FC5754 is ready to charge. High impedance indicates that the FC5754 is in undervoltage lockout mode: either VIN is less than 80mV above the BAT pin voltage or insufficient voltage is applied to the VIN pin. A microprocessor can be used to distinguish between these three states—this method is discussed in the Applications Information section.

Thermal Limiting

An internal thermal feedback loop reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 120°C. This feature protects the FC5754 from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the FC5754. The charge current can be set according to typical (not worst-case) ambient temperature with the assurance that the charger will automatically reduce the current in worst-case conditions. SOT power considerations are

discussed further in the Applications Information section.

Undervoltage Lockout (UVLO)

An internal undervoltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until VIN rises above the undervoltage lockout threshold. The UVLO circuit has a built-in hysteresis of 200mV. Furthermore, to protect against reverse current in the power MOSFET, the UVLO circuit keeps the charger in shutdown mode if VIN falls to within 30mV of the battery voltage. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until VIN rises 80mV above the battery voltage.

Manual Shutdown

At any point in the charge cycle, the FC5754 can be put into shutdown mode by removing R_{PROG} thus floating the PROG pin. This reduces the battery drain current to about to 0 μ A and the supply current to less than 45 μ A. A new charge cycle can be initiated by reconnecting the

program resistor.

In manual shutdown, the CHGB pin is in a weak pull-down state as long as VIN is high enough to exceed the UVLO conditions. The CHGB pin is in a high impedance state if the FC5754 is in undervoltage lockout mode: either VIN is within 80mV of the BAT pin voltage or insufficient voltage is applied to the VIN pin.

Automatic Recharge

Once the charge cycle is terminated, the FC5754 continuously monitors the voltage on the BAT pin using a comparator with a 2ms filter time (T_{RECHARGE}). A charge cycle restarts when the battery voltage falls below 4.05V (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations. CHGB output enters a strong pulldown state during recharge cycles.

Application Information

Stability Considerations

The constant-voltage mode feedback loop is stable without an output capacitor provided a battery is connected to the charger output. With no battery present, an output capacitor is recommended to reduce ripple voltage. When using high value, low ESR ceramic capacitors, it is recommended to add a 1 Ω resistor in series with the capacitor. No series resistor is needed if tantalum capacitors are used.

In constant-current mode, the PROG pin is in the feedback loop, not the battery. The constant-current mode stability is affected by the impedance at the PROG pin. With no additional capacitance on the PROG pin, the charger is stable with program resistor values as high as 100k. However, additional capacitance on this node reduces the maximum allowed program resistor. The pole frequency at the PROG pin should be kept above 100kHz. Therefore, if the PROG pin is loaded with a capacitance, C_{PROG}, the following equation can be used to calculate the maximum resistance value for R_{PROG}:

$$R_{PROG} = \frac{1}{2\pi \cdot 10^5 \cdot C_{PROG}}$$

Average, rather than instantaneous, charge current may be of interest to the user. For example, if a switching power supply operating in low current mode is connected in parallel with the battery, the average current being pulled out of the BAT pin is typically of more interest than the instantaneous current pulses. In such a case, a simple RC filter can be used on the PROG pin to measure the average battery current as shown in Figure 1. A 10kHz resistor has been added between the PROG pin and the filter capacitor to ensure stability.

Power Dissipation

The conditions that cause the FC5754 to reduce charge current through thermal feedback can be approximated by considering the power dissipated in the IC. Nearly all of this power dissipation is generated by the internal MOSFET—this is calculated to be approximately:

$$P_D = (V_{IN} - V_{BAT}) \cdot I_{BAT}$$

where P_D is the power dissipated, V_{IN} is the input supply voltage, V_{BAT} is the battery voltage and I_{BAT} is the charge current. The approximate ambient temperature at which the thermal feedback begins to protect the IC is:

$$T_A = 120^\circ\text{C} - P_D \Theta_{JA}$$

$$T_A = 120^\circ\text{C} - (V_{IN} - V_{BAT}) \cdot I_{BAT} \cdot \Theta_{JA}$$

Example: An FC5754 operating from a 5V USB supply is programmed to supply 500mA full-scale current to a discharged Li-Ion battery with a voltage of 3.7V. Assuming Θ_{JA} is 100°C/W, the ambient temperature at which the FC5754 will begin to reduce the charge current is approximately:

$$T_A = 120^\circ\text{C} - (5V - 3.7V) \cdot (500\text{mA}) \cdot 100^\circ\text{C/W}$$

$$T_A = 120^\circ\text{C} - 0.65W \cdot 100^\circ\text{C/W} = 120^\circ\text{C} - 65^\circ\text{C}$$

$$T_A = 55^\circ\text{C}$$

The FC5754 can be used above 55°C ambient, but the charge current will be reduced from 500mA. The

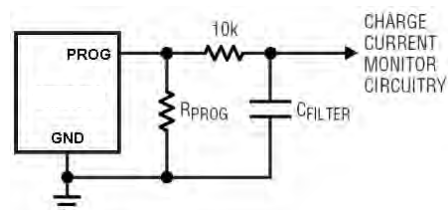


Figure 1. Isolating Capacitive load on PROG Pin and Filtering

approximate current at a given ambient temperature can be approximated by:

$$I_{BAT} = \frac{120^{\circ}C - T_A}{(V_{IN} - V_{BAT}) * \Theta_{JA}}$$

Using the previous example with an ambient temperature of 70°C, the charge current will be reduced to approximately:

$$I_{BAT} = \frac{120^{\circ}C - 70^{\circ}C}{(5 - 3.7) * 100^{\circ}C/W} = \frac{50^{\circ}C}{130^{\circ}C/A}$$

$$I_{BAT} = 384mA$$

Moreover, when thermal feedback reduces the charge current, the voltage at the PROG pin is also reduced proportionally as discussed in the Operation section. It is important to remember that FC5754 applications do not need to be designed for worst-case thermal conditions since the IC will automatically reduce power dissipation when the junction temperature reaches approximately 120°C.

Thermal Considerations

Because of the small size of the SOT package, it is very important to use a good thermal PC board layout to maximize the available charge current. The thermal path for the heat generated by the IC is from the die to the copper lead frame, through the package leads, (especially the ground lead) to the PC board copper. The PC board copper is the heat sink. The footprint copper pads should be as wide as possible and expand out to larger copper areas to spread and dissipate the heat to the surrounding ambient. Feedthrough vias to inner or backside copper layers are also useful in improving the overall thermal performance of the charger. Other heat sources on the board, not related to the charger, must also be considered when designing a PC board layout because they will affect overall temperature rise and the maximum charge current.

Increasing Thermal Regulation Current

Reducing the voltage drop across the internal MOSFET can significantly decrease the power dissipation in the IC. This has the effect of increasing the current delivered to the battery during thermal regulation. One method is by dissipating some of the power through an external component, such as a resistor or diode.

Example: An FC5754 operating from a 5V wall

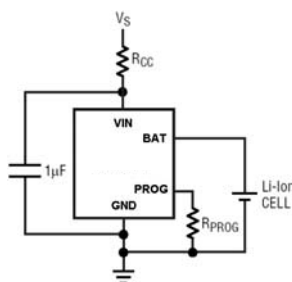


Figure 2. A Circuit to Maximize Thermal Mode Charge Current

adapter is programmed to supply 1A full-scale current

to a discharged Li-Ion battery with a voltage of 3.7V. Assuming Θ_{JA} is 100°C/W, the approximate charge current at an ambient temperature of 25°C is:

$$I_{BAT} = \frac{120^{\circ}C - 25^{\circ}C}{(5V - 3.7V) * 100^{\circ}C/W} = 730mA$$

By dropping voltage across a resistor in series with a 5V wall adapter (shown in Figure 2), the on-chip power dissipation can be decreased, thus increasing the thermally regulated charge current

$$I_{BAT} = \frac{120^{\circ}C - 25^{\circ}C}{(V_S - I_{BAT}R_{CC} - V_{BAT}) * \Theta_{JA}}$$

Solving for IBAT using the quadratic formula.

$$I_{BAT} = \frac{1}{2R_{CC}}[(V_S - V_{BAT}) - \sqrt{(V_S - V_{BAT})^2 - \frac{4R_{CC}(120^{\circ}C - T_A)}{\Theta_{JA}}}]$$

(Note: Large values of R_{CC} will result in no solution for I_{BAT} . This indicates that the FC5754 will not generate enough heat to require thermal regulation.)

Using $R_{CC} = 0.25\Omega$, $V_S = 5V$, $V_{BAT} = 3.7V$, $T_A = 25^{\circ}C$ and $\Theta_{JA} = 100^{\circ}C/W$ we can calculate the thermally regulated charge current to be:

$$I_{BAT} = 879.5mA$$

While this application delivers more energy to the battery and reduces charge time in thermal mode, it may actually lengthen charge time in voltage mode if V_{IN} becomes low enough to put the FC5754 into dropout.

This technique works best when R_{CC} values are minimized to keep component size small and avoid dropout. Remember to choose a resistor with adequate power handling capability.

VIN Bypass Capacitor

Many types of capacitors can be used for input bypassing, however, caution must be exercised when using multilayer ceramic capacitors. Because of the self-resonant and high Q characteristics of some types of ceramic capacitors, high voltage transients can be generated under some start-up conditions, such as connecting the charger input to a live power source. Adding a 1.5 Ω resistor in series with an X5R ceramic capacitor will minimize start-up voltage transients.

Charge Current Soft-Start

The FC5754 includes a soft-start circuit to minimize the inrush current at the start of a charge cycle. When a charge cycle is initiated, the charge current ramps from zero to the full-scale current over a period of approximately 100 μ s. This has the effect of minimizing the transient current load on the power supply during start-up.

CHGB Status Output Pin

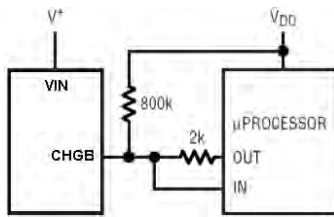


Figure 3. Using a Microprocessor to Determine CHGB State

The CHGB pin can provide an indication that the input voltage is greater than the undervoltage lockout threshold level. A weak pull-down current approximately 24μA indicates that sufficient voltage is applied to VIN to begin charging. When a discharged battery is connected to the charger, the constant current portion of the charge cycle begins and the CHGB pin pulls to ground. The CHGB pin can sink up to 10mA to drive an LED that indicates that a charge cycle is in progress.

When the battery is nearing full charge, the charger enters the constant-voltage portion of the charge cycle and the charge current begins to drop. When the charge current drops below 1/10 of the programmed current, the charge cycle ends and the strong pull-down is replaced by the 24μA pull-down, indicating that the charge cycle has ended. If the input voltage is removed or drops below the undervoltage lockout threshold, the CHGB pin becomes high impedance. Figure 3 shows that by using two different value pull-up resistors, a microprocessor can detect all three states from this pin.

To detect when the FC5754 is in charge mode, force the digital output pin (OUT) high and measure the voltage at the CHGB pin. The internal N-channel MOSFET will pull the pin voltage low even with the 2k pull-up resistor. Once the charge cycle terminates, the N-channel MOSFET is turned off and a 24μA current source is connected to the CHGB pin. The IN pin will then be pulled high by the 2k pull-up resistor. To determine if there is a weak pull-down current, the OUT pin should be forced to a high impedance state. The weak current source will pull the IN pin low through the 800k resistor; if CHGB is high impedance, the IN pin will be pulled high, indicating that the part is in a UVLO state.

Reverse Polarity Input Voltage Protection

In some applications, protection from reverse polarity voltage on VIN is desired. If the supply voltage is high enough, a series blocking diode can be used. In other cases, where the voltage drop must be kept low a P-channel MOSFET can be used (as shown in Figure 4).

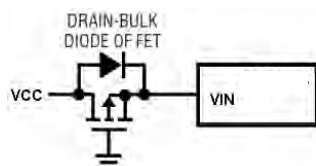


Figure 4. Low Loss Input Reverse Polarity Protection

USB and Wall Adapter Power

The FC5754 allows charging from both a wall adapter and a USB port. Figure 5 shows an example of how to combine wall adapter and USB power inputs. A P-channel MOSFET, MP1, is used to prevent back conducting into the USB port when a wall adapter is present and a Schottky diode, D1, is used to prevent USB power loss through the 1k pull-down resistor. Typically a wall adapter can supply more current than the 500mA-limited USB port. Therefore, an N-channel MOSFET, MN1, and an extra 10k program resistor are used to increase the charge current to 580mA when the wall adapter is present.

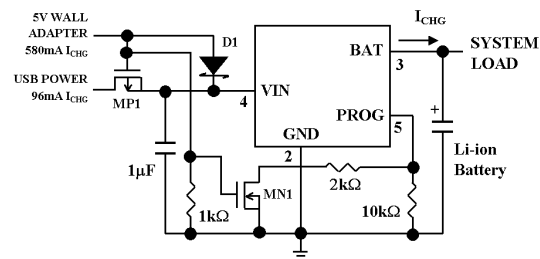
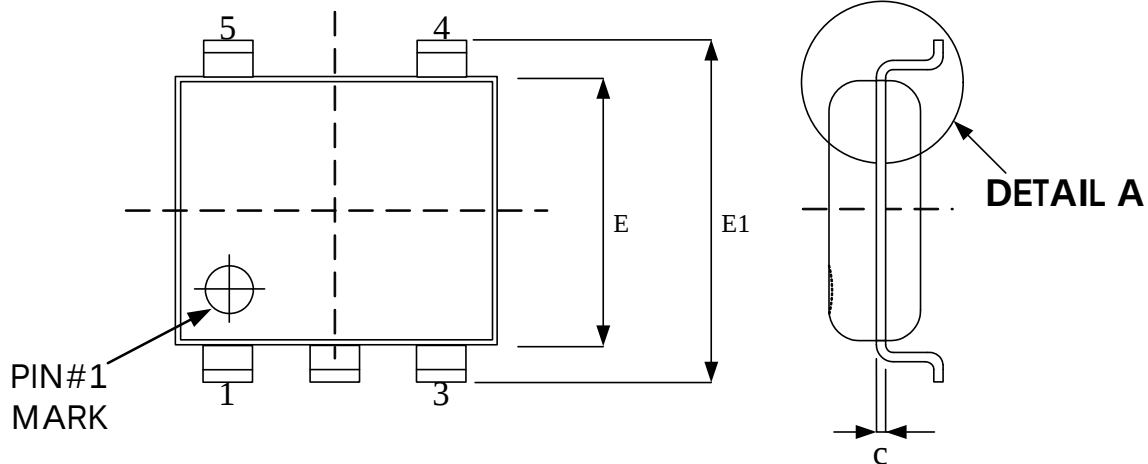


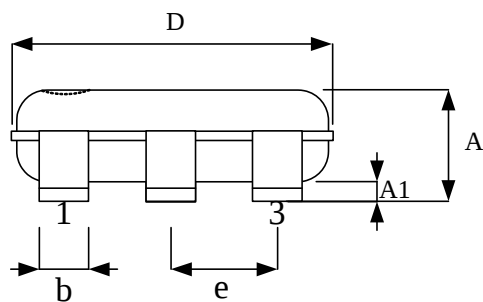
Figure 5. Combining Wall Adapter and USB Power

Package Outline Drawing

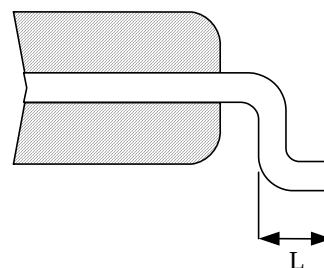
SOT-23-5



TOP VIEW



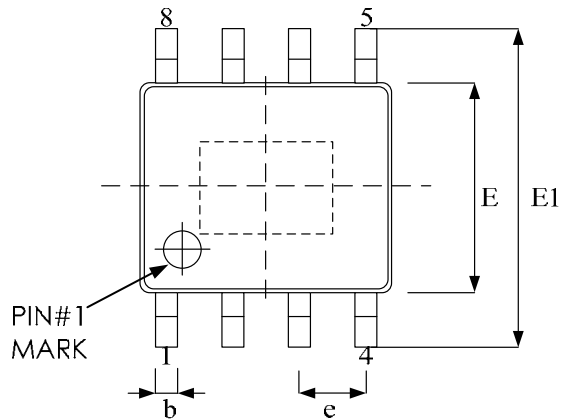
SIDE VIEW



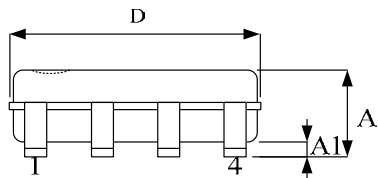
DETAIL A

Symbol	Dimension in mm	
	Min.	Max.
A	0.90	1.45
A1	0.00	0.15
b	0.30	0.50
c	0.08	0.25
D	2.70	3.10
E	1.40	1.80
E1	2.60	3.00
e	0.95 BSC	
L	0.30	0.60

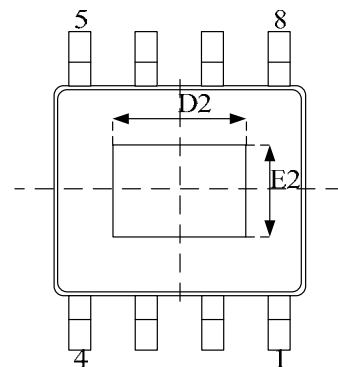
Package Outline Drawing SOP-8 (E) (150 mil)



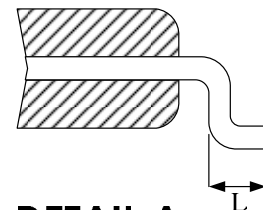
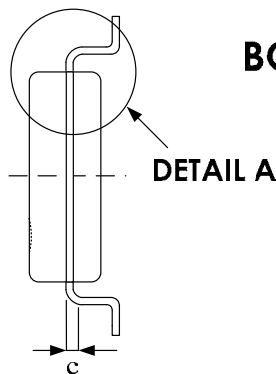
TOP VIEW



SIDE VIEW



BOTTOM VIEW



DETAIL A

Symbol	Dimension in mm	
	Min	Max
A	1.35	1.75
A1	0.00	0.25
b	0.33	0.51
c	0.17	0.25
D	4.80	5.00
E	3.81	4.00
E1	5.79	6.20
e	1.27 BSC	
L	0.41	1.27

Exposed pad

	Dimension in mm	
	Min	Max
D2	1.93	2.39
E2	1.93	2.39