Leiditech

ES2A-L THRU ES2J-L

Features

- Lead Free Finish/RoHS Compliant(Note 1) ("P" Suffix designates RoHS Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Glass Passivated Die Construction
- Super fast switching speed under 35ns
- Marking : Cathode band and type number (No '-L' Suffix)

2 Amp Super Fast Recovery Rectifier 50 to 600 Volts

Maximum Ratings

- Operating Temperature: -65℃ to +175℃
- Storage Temperature: -65℃ to +175℃

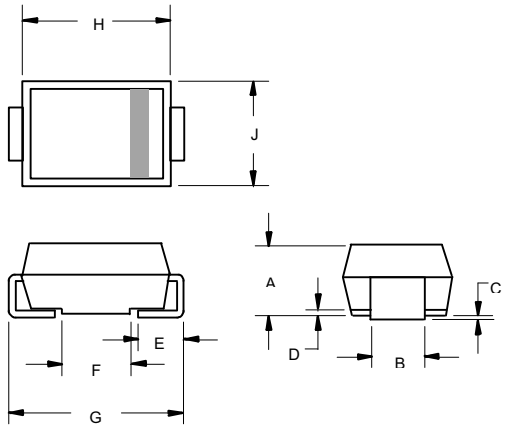
MCC Part Number	Maximum Recurrent Peak Reverse Voltage	Maximum RMS Voltage	Maximum DC Blocking Voltage
ES2A-L	50V	35V	50V
ES2B-L	100V	70V	100V
ES2D-L	200V	140V	200V
ES2G-L	400V	280V	400V
ES2J-L	600V	420V	600V

Electrical Characteristics @ 25°C Unless Otherwise Specified

Average Forward Current	$I_{F(AV)}$	2.0A	T_L= 110℃
Peak Forward Surge Current	I_{FSM}	50A	8.3ms, half sine
Maximum Instantaneous Forward Voltage ES2A-L-ES2D-L ES2G-L ES2J-L ES2J-L	V_F	.95V 1.25V 1.70V .975V(Typ) 1.25V(Max)	I_{FM} = 2.0A;T_J=25°C I_{FM} = 2.0A;T_J=150°C
Maximum DC Reverse Current At Rated DC Blocking Voltage	I_R	5μA 1mA	T_A = 25℃ T_A = 100℃
Maximum Reverse Recovery Time	T_{rr}	35ns	I_F=0.5A, I_R=1.0A, I_{rr}=0.25A
Typical Junction Capacitance	C_J	15pF	Measured at 1.0MHz, V_R=4.0V

Notes: 1. High Temperature Solder Exemption Applied, see EU Directive Annex Notes 7.

DO-214AC
(SMA) (LEAD FRAME)



DIM	INCHES MIN	INCHES MAX	MM MIN	MM MAX	NOTE
A	.079	.096	2.00	2.44	
B	.050	.064	1.27	1.63	
C	.002	.008	.05	.20	
D	---	.02	---	.51	
E	.030	.060	.76	1.52	
F	.065	.091	1.65	2.32	
G	.189	.220	4.80	5.59	
H	.157	.181	4.00	4.60	
J	.090	.115	2.25	2.92	

SUGGESTED SOLDER PAD LAYOUT

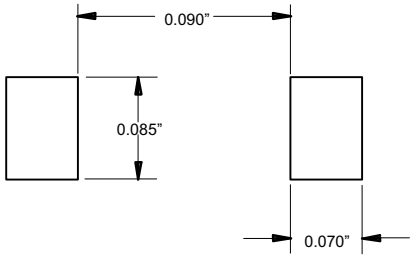


FIG.1-TYPICAL FORWARD CHARACTERISTICS

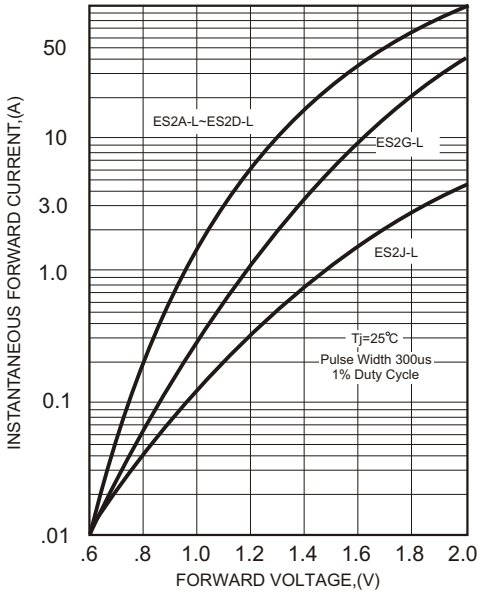


FIG.2-TYPICAL FORWARD CURRENT DERATING CURVE

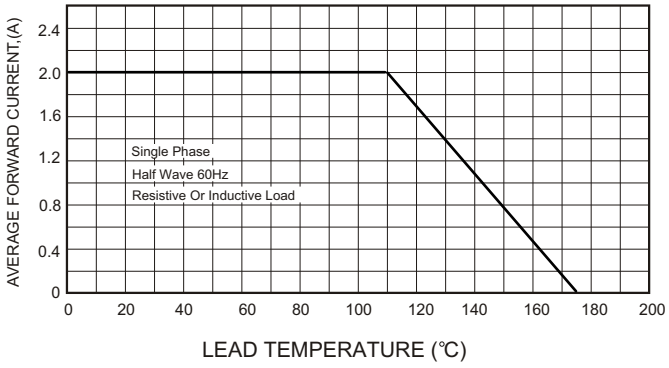
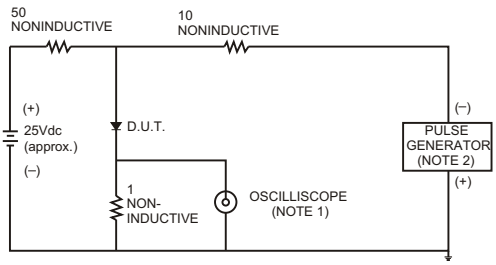
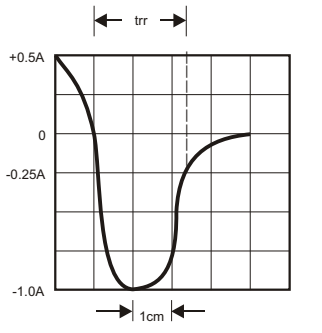


FIG.3- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTICS



NOTES: 1. Rise Time= 7ns max., Input Impedance= 1 megohm.22pF.

2. Rise Time= 10ns max., Source Impedance= 50 ohms.



SET TIME BASE FOR
50 / 10ns / cm

FIG.4-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

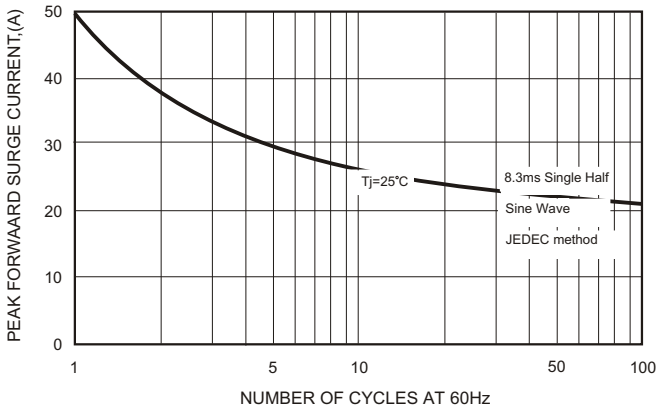
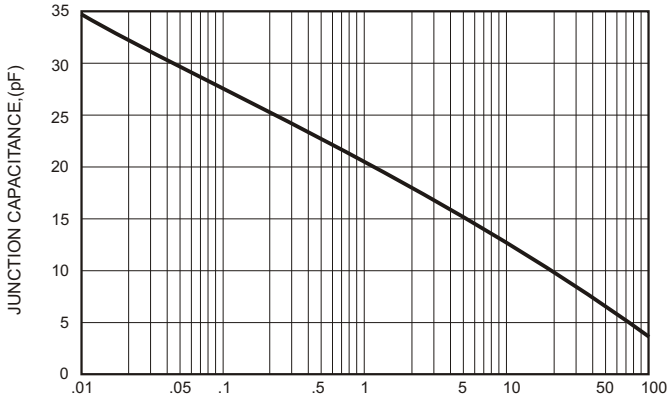


FIG.5-TYPICAL JUNCTION CAPACITANCE



Shanghai Leiditech Electronic Co.,Ltd
Email: sale1@leiditech.com
Tel : +86- 021 50828806
Fax : +86- 021 50477059