

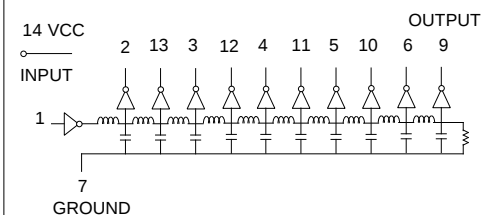
14 Pin DIP 10 Tap TTL Compatible Active Delay Lines

TAP DELAYS ±5% or ±2 nS†	TOTAL DELAYS ±5% or ±2 nS†	PART NUMBER	TAP DELAYS ±5% or ±2 nS†	TOTAL DELAYS ±5% or ±2 nS†	PART NUMBER
5	50	EPA619-50	44	440	EPA619-440
6	60	EPA619-60	45	450	EPA619-450
7.5	75	EPA619-75	47	470	EPA619-470
10	100	EPA619-100	50	500	EPA619-500
12.5	125	EPA619-125	55	550	EPA619-550
15	150	EPA619-150	60	600	EPA619-600
17.5	175	EPA619-175	65	650	EPA619-650
20	200	EPA619-200	70	700	EPA619-700
22.5	225	EPA619-225	75	750	EPA619-750
25	250	EPA619-250	80	800	EPA619-800
30	300	EPA619-300	85	850	EPA619-850
35	350	EPA619-350	90	900	EPA619-900
40	400	EPA619-400	95	950	EPA619-950
42	420	EPA619-420	100	1000	EPA619-1000

†Whichever is greater. Delay times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics					
Parameter	Test Conditions	Min	Max	Unit	
V _{OH}	High-Level Output Voltage	V _{CC} = min. V _{IL} = max. I _{OH} = max	2.7	0.5	V
V _{OL}	Low-Level Output Voltage	V _{CC} = min. V _{IH} = min. I _{OL} = max		-1.2V	V
V _{IK}	Input Clamp Voltage	V _{CC} = min. I _I = I _{IK}		50	µA
I _{IH}	High-Level Input Current	V _{CC} = max. V _{IN} = 2.7V		1.0	mA
		V _{CC} = max. V _{IN} = 5.25V		-2	mA
I _{IL}	Low-Level Input Current	V _{CC} = max. V _{IN} = 0.5V		-40	mA
I _{OS}	Short Circuit Output Current	V _{CC} = max. V _{OUT} = 0. (One output at a time)		-100	mA
I _{CC}	High-Level Supply Current	V _{CC} = max. V _{IN} = OPEN		150	mA
I _{CC}	Low-Level Supply Current	V _{CC} = max. V _{IN} = 0		150	mA
T _{RO}	Output Rise Time	T _d ≤ 500 nS (0.75 to 2.4 Volts) T _d > 500 nS		4	nS
				5	nS
N _H	Fanout High-Level Output	V _{CC} = max. V _{OH} = 2.7V		20 TTL LOAD	
N _L	Fanout Low-Level Output	V _{CC} = max. V _{OL} = 0.5V		10 TTL LOAD	

Schematic

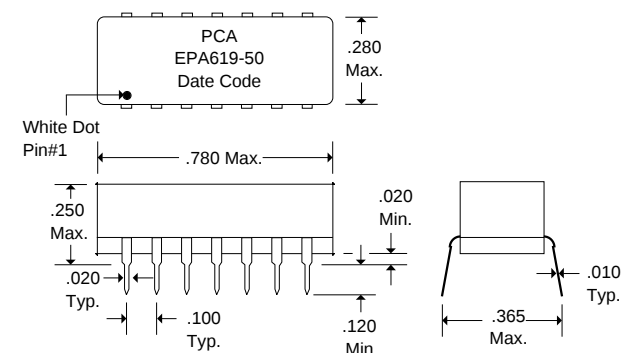


Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-Level Input Voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T _A	Operating Free-Air Temperature	0	+70	°C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C			Unit
E _{IN}	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	110	%
T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
P _{RR}	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ T _d > 200 nS	100	KHz
V _{CC}	Supply Voltage	5.0	Volts

Package Dimensions



DSA619 Rev. A 2/5/96

QAF-CS01 Rev. B 8/25/94

Unless Otherwise Noted Dimensions in Inches

Tolerances:
Fractional = ± 1/32
.XX = ± .030 .XXX = ± .010



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