

14 Pin DIP High Precision Single Output TTL Compatible Active Delay Lines

EPA500-XX & EPA500-XX-RC

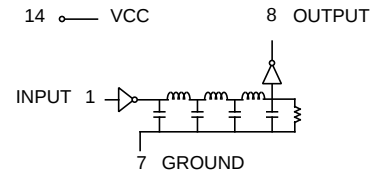
Add "-RC" after part number for RoHS Compliant

PCA Part Number	Time Delays (nS)	PCA Part Number	Time Delays (nS)	PCA Part Number	Time Delays (nS)
EPA500-5(-RC)	5 ± 1	EPA500-23(-RC)	23 ± 1	EPA500-125(-RC)	125 ± 4
EPA500-6(-RC)	6 ± 1	EPA500-24(-RC)	24 ± 1	EPA500-150(-RC)	150 ± 4.5
EPA500-7(-RC)	7 ± 1	EPA500-25(-RC)	25 ± 1	EPA500-175(-RC)	175 ± 5
EPA500-8(-RC)	8 ± 1	EPA500-30(-RC)	30 ± 1.5	EPA500-200(-RC)	200 ± 6
EPA500-9(-RC)	9 ± 1	EPA500-35(-RC)	35 ± 1.5	EPA500-225(-RC)	225 ± 7
EPA500-10(-RC)	10 ± 1	EPA500-40(-RC)	40 ± 1.5	EPA500-250(-RC)	250 ± 8
EPA500-11(-RC)	11 ± 1	EPA500-45(-RC)	45 ± 2	EPA500-275(-RC)	275 ± 9
EPA500-12(-RC)	12 ± 1	EPA500-50(-RC)	50 ± 2	EPA500-300(-RC)	300 ± 10
EPA500-13(-RC)	13 ± 1	EPA500-55(-RC)	55 ± 2	EPA500-350(-RC)	350 ± 11
EPA500-14(-RC)	14 ± 1	EPA500-60(-RC)	60 ± 2	EPA500-400(-RC)	400 ± 12
EPA500-15(-RC)	15 ± 1	EPA500-65(-RC)	65 ± 2.5	EPA500-450(-RC)	450 ± 14
EPA500-16(-RC)	16 ± 1	EPA500-70(-RC)	70 ± 2.5	EPA500-500(-RC)	500 ± 15
EPA500-17(-RC)	17 ± 1	EPA500-75(-RC)	75 ± 2.5	EPA500-600(-RC)	600 ± 18
EPA500-18(-RC)	18 ± 1	EPA500-80(-RC)	80 ± 2.5	EPA500-700(-RC)	700 ± 20
EPA500-19(-RC)	19 ± 1	EPA500-85(-RC)	85 ± 3	EPA500-800(-RC)	800 ± 22
EPA500-20(-RC)	20 ± 1	EPA500-90(-RC)	90 ± 3	EPA500-900(-RC)	900 ± 24
EPA500-21(-RC)	21 ± 1	EPA500-95(-RC)	95 ± 3	EPA500-1000(-RC)	1000 ± 26
EPA500-22(-RC)	22 ± 1	EPA500-100(-RC)	100 ± 3		

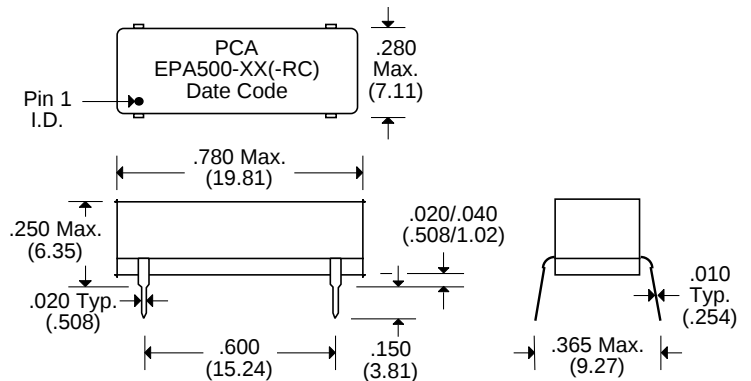
Delay times referenced from input to leading and trailing edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics		Test Conditions	Min.	Max.	Unit
Parameter					
V _{OH}	High-Level Output Voltage	V _{CC} = min. V _{IL} = max. I _{OH} = max	2.7		V
V _{OL}	Low-Level Output Voltage	V _{CC} = min. V _{IH} = min. I _{OL} = max		0.5	V
V _{IK}	Input Clamp Voltage	V _{CC} = min. I _I = I _{IK}		-1.2	V
I _{IH}	High-Level Input Current	V _{CC} = max. V _{IN} = 2.7V		50	μA
		V _{CC} = max. V _{IN} = 5.25V		1.0	mA
I _{IL}	Low-Level Input Current	V _{CC} = max. V _{IN} = 0.5V		-2	mA
I _{OS}	Short Circuit Output Current	V _{CC} = max. V _{OUT} = 0. (One output at a time)	-40	-100	mA
I _{CC} _H	High-Level Supply Current	V _{CC} = max. V _{IN} = OPEN		75	mA
I _{CC} _L	Low-Level Supply Current	V _{CC} = max. V _{IN} = 0		75	mA
T _{RO}	Output Rise Time	T _d ≤ 500 nS (0.75 to 2.4 Volts)		4	nS
N _H	Fanout High-Level Output	V _{CC} = max. V _{OH} = 2.7V		20 TTL Load	
N _L	Fanout Low-Level Output	V _{CC} = max. V _{OL} = 0.5V		10 TTL Load	

Schematic



Package



Recommended Operating Conditions		Min.	Max.	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-Level Input Voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T _A	Operating Free-Air Temperature	0	+70	°C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C			Unit
E _{IN}	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	110	%
T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
P _{RR}	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ T _d > 200 nS	100	KHz
V _{CC}	Supply Voltage	5.0	Volts

Notes :	EPA500-XX	EPA500-XX-RC
1. Lead Finish	SnPb	Hot Tin Dip (Sn) †
2. Peak Temperature Rating	225°C	245°C
3. Moisture Sensitive Levels	MSL = 3 (168 Hours, £30°C/60%RH)	MSL = 4 (72 Hours, £30°C/60%RH)
4. Weight	TBD grams	TBD grams
5. Packaging Information (Tube)	TBD pieces/tube	TBD pieces/tube

† Lead Material : Matte Tin with Ni Barrier

Unless Otherwise Specified Dimensions are in Inches /mm ± .010 / .25