

14 Pin DIP and SMD 5 Tap Low-Profile Fast Logic TTL Compatible Active Delay Lines

Compatible with standard auto-insertable equipment and can be used in either infrared or vapor phase process.

Delays are $\pm 5\%$ or ± 2 nS†		DIP Part Number	SMD Part Number	Delays are $\pm 5\%$ or ± 2 nS†		DIP Part Number	SMD Part Number
Tap	Total			Tap	Total		
5, 10, 15, 20	25	EPA3368-25	EPA3368G-25	40, 80, 120, 160	200	EPA3368-200	EPA3368G-200
6, 12, 18, 24	30	EPA3368-30	EPA3368G-30	45, 90, 135, 180	225	EPA3368-225	EPA3368G-225
7, 14, 21, 28	35	EPA3368-35	EPA3368G-35	50, 100, 150, 200	250	EPA3368-250	EPA3368G-250
8, 16, 24, 32	40	EPA3368-40	EPA3368G-40	60, 120, 180, 240	300	EPA3368-300	EPA3368G-300
9, 18, 27, 36	45	EPA3368-45	EPA3368G-45	70, 140, 210, 280	350	EPA3368-350	EPA3368G-350
10, 20, 30, 40	50	EPA3368-50	EPA3368G-50	80, 160, 240, 320	400	EPA3368-400	EPA3368G-400
12, 24, 36, 48	60	EPA3368-60	EPA3368G-60	84, 168, 252, 336	420	EPA3368-420	EPA3368G-420
15, 30, 45, 60	75	EPA3368-75	EPA3368G-75	88, 176, 264, 352	440	EPA3368-440	EPA3368G-440
20, 40, 60, 80	100	EPA3368-100	EPA3368G-100	90, 180, 270, 360	450	EPA3368-450	EPA3368G-450
25, 50, 75, 100	125	EPA3368-125	EPA3368G-125	84, 168, 252, 336	470	EPA3368-470	EPA3368G-470
30, 60, 90, 120	150	EPA3368-150	EPA3368G-150	100, 200, 300, 400	500	EPA3368-500	EPA3368G-500
35, 70, 105, 140	175	EPA3368-175	EPA3368G-175				

†Whichever is greater. Delay times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics					Schematic	
Parameter	Test Conditions	Min.	Max.	Unit		
V _{OH}	High-Level Output Voltage	V _{CC} = Min. V _{IL} = Max. I _{OH} = Max.	2.7	V		
V _{OL}	Low-Level Output Voltage	V _{CC} = Min. V _{IH} = Min. I _{OL} = Max.	0.5	V		
V _{IK}	Input Clamp Voltage	V _{CC} = Min. I _I = I _{IK}	-1.2	V		
I _{IH}	High-Level Input Current	V _{CC} = Max. V _{IN} = 2.7V	20	μA		
I _{IL}	Low-Level Input Current	V _{CC} = Max. V _{IN} = 0.5V	-0.6	mA		
I _{OS}	Short Circuit Output Current	V _{CC} = Max. V _{OUT} = 0. (One output at a time)	-80	mA		
I _{CC}	High-Level Supply Current	V _{CC} = Max. V _{IN} = OPEN	25	mA		
I _{CL}	Low-Level Supply Current	V _{CC} = Max. V _{IN} = 0	40	mA		
T _{RO}	Output Rise Time	T _d ≤ 500 nS (0.75 to 2.4 Volts)	4	nS		
		T _d > 500 nS	5	nS		
N _H	Fanout High-Level Output	V _{CC} = Max. V _{OH} = 2.7V	20	TTL LOAD		
N _L	Fanout Low-Level Output	V _{CC} = Max. V _{OL} = 0.5V	10	TTL LOAD		

Recommended Operating Conditions					Input Pulse Test Conditions @ 25° C			Unit
		Min.	Max.	Unit				
V _{CC}	Supply Voltage	4.75	5.25	V	E _{IN}	Pulse Input Voltage	3.2	Volts
V _{IH}	High-Level Input Voltage	2.0		V	P _W	Pulse Width % of Total Delay	110	%
V _{IL}	Low-Level Input Voltage		0.8	V	T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
I _{IK}	Input Clamp Current		-18	mA	PRR	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0	MHz
I _{OH}	High-Level Output Current		-1.0	mA		Pulse Repetition Rate @ T _d > 200 nS	100	KHz
I _{OL}	Low-Level Output Current		20	mA	V _{CC}	Supply Voltage	5.0	Volts
P _W *	Pulse Width of Total Delay	20		%				
d*	Duty Cycle		50	%				
T _A	Operating Free-Air Temperature	0	+70	°C				

*These two values are inter-dependent.

SMD Package	DIP Package

DSA3368G-XX & DSA3368-XX Rev. - 3/18/97

QAF-CS01 Rev. B 8/25/94

Unless Otherwise Noted Dimensions in Inches

Tolerances:

Fractional = $\pm 1/32$

.XX = $\pm .030$.XXX = $\pm .010$



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