14 Pin DIL Triple TTL Compatible Active Delay Lines

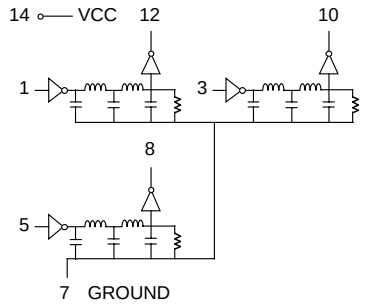
DELAY TIME ±5% or 2 nS†	PART NUMBER	DELAY TIME ±5 or 2 nS†	PART NUMBER	DELAY TIME ±5% or 2 nS†	PART NUMBER
5	EPA189-5	19	EPA189-19	65	EPA189-65
6	EPA198-6	20	EPA189-20	70	EPA189-70
7	EPA189-7	21	EPA189-21	75	EPA189-75
8	EPA189-8	22	EPA189-22	80	EPA189-80
9	EPA189-9	23	EPA189-23	85	EPA189-85
10	EPA18910	24	EPA189-24	90	EPA189-90
11	EPA189-11	25	EPA189-25	95	EPA189-95
12	EPA189-12	30	EPA189-30	100	EPA189-100
13	EPA189-13	35	EPA189-35	125	EPA189-125
14	EPA189-14	40	EPA189-40	150	EPA189-150
15	EPA189-15	45	EPA189-45	175	EPA189-175
16	EPA189-16	50	EPA189-50	200	EPA189-200
17	EPA189-17	55	EPA189-55	225	EPA189-225
18	EPA189-18	60	EPA189-60	250	EPA189-250

†Whichever is greater. Delay Times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics

	Parameter	Test Conditions	Min	Max	Unit
V_{OH}	High-Level Output Voltage	V_{CC} = min. V_{IL} = max. I_{OH} = max	2.7		V
V_{OL}	Low-Level Output Voltage	V_{CC} = min. V_{IH} = min. I_{OL} = max		0.5	V
V_{IK}	Input Clamp Voltage	V_{CC} = min. I_I = II_K		-1.2V	V
I_{IH}	High-Level Input Current	V_{CC} = max. V_{IN} = 2.7V		50	µA
		V_{CC} = max. V_{IN} = 5.25V		1.0	mA
I_{IL}	Low-Level Input Current	V_{CC} = max. V_{IN} = 0.5V		-2	mA
I_{OS}	Short Circuit Output Current	V_{CC} = max. V_{OUT} = 0. (One output at a time)	-40	-100	mA
I_{CCH}	High-Level Supply Current	V_{CC} = max. V_{IN} = OPEN		115	mA
I_{CCL}	Low-Level Supply Current	V_{CC} = max. V_{IN} = 0		115	mA
T_{RO}	Output Rise Time	Td ≤ 500 nS (0.75 to 2.4 Volts)		4	nS
N_H	Fanout High-Level Output	V_{CC} = max. V_{OH} = 2.7V		20 TTL LOAD	
N_L	Fanout Low-Level Output	V_{CC} = max. V_{OL} = 0.5V		10 TTL LOAD	

Schematic



Recommended Operating Conditions

		Min	Max	Unit
V_{CC}	Supply Voltage	4.75	5.25	V
V_{IH}	High-Level Input Voltage	2.0		V
V_{IL}	Low-Level Input Voltage		0.8	V
I_{IK}	Input Clamp Current		-18	mA
I_{OH}	High-Level Output Current		-1.0	mA
I_{OL}	Low-Level Output Current		20	mA
P_W*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T_A	Operating Free-Air Temperature	0	+70	°C

**These two values are inter-dependent.*

Input Pulse Test Conditions @ 25° C

			Unit
E_{IN}	Pulse Input Voltage	3.2	Volts
P_W	Pulse Width % of Total Delay	110	%
T_{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
P_{RR}	Pulse Repetition Rate @ Td ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ Td > 200 nS	100	KHz
V_{CC}	Supply Voltage	5.0	Volts

Package Dimensions

