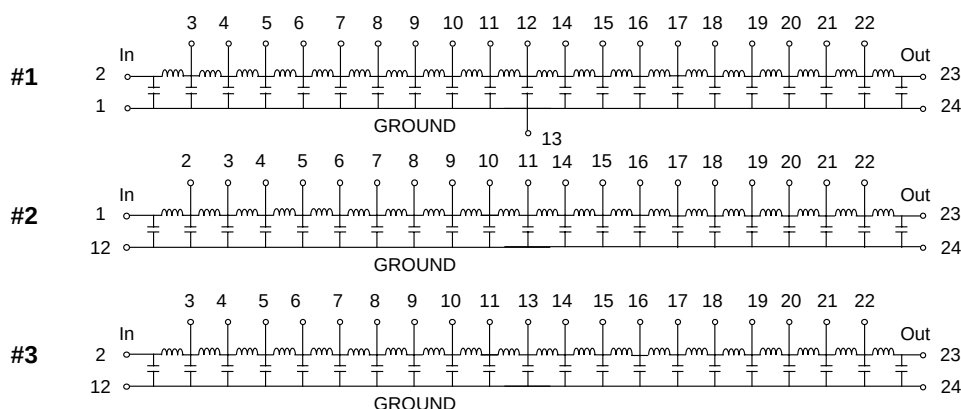


24 Pin DIP Passive Delay Lines

Zo OHMS ±10%	TOTAL DELAY nS ±5% or ±2 nS†	TAP TO TAP DELAYS nS	OUTPUT RISE TME nS Max.	ATTEN Max. %	PART NUMBER		
					SCHEMATIC #1	SCHEMATIC #2	SCHEMATIC #3
50	20	1.0 ± 0.2	3	10	EPA059-20A	EPA060-20A	EPA061-20A
50	50	2.5 ± 0.5	5	10	EPA059-50A	EPA060-50A	EPA061-50A
50	100	5.0 ± 1.0	10	10	EPA059-100A	EPA060-100A	EPA061-100A
50	200	10.0 ± 1.0	20	10	EPA059-200A	EPA060-200A	EPA061-200A
100	20	1.0 ± 0.2	3	10	EPA059-20B	EPA060-20B	EPA061-20B
100	50	2.5 ± 0.5	5	10	EPA059-50B	EPA060-50B	EPA061-50B
100	100	5.0 ± 1.0	10	10	EPA059-100B	EPA060-100B	EPA061-100B
100	200	10.0 ± 1.0	20	10	EPA059-200B	EPA060-200B	EPA061-200B
100	500	25.0 ± 2.5	50	20	EPA059-500B	EPA060-500B	EPA061-500B
100	1000	50.0 ± 5.0	100	20	EPA059-1000B	EPA060-1000B	EPA061-1000B
200	20	1.0 ± 0.2	3	10	EPA059-20C	EPA060-20C	EPA061-20C
200	50	2.5 ± 0.5	5	10	EPA059-50C	EPA060-50C	EPA061-50C
200	100	5.0 ± 1.0	10	10	EPA059-100C	EPA060-100C	EPA061-100C
200	500	25.0 ± 2.5	50	20	EPA059-500C	EPA060-500C	EPA061-500C
200	1000	50.0 ± 5.0	100	20	EPA059-1000C	EPA060-1000C	EPA061-1000C
250	400	20 ± 2.5	40	20	EPA059-400L	EPA060-400L	EPA061-400L

†Whichever is greater.

Schematic



DC Electrical Characteristics

	Min	Max	Unit
Distortion		±10	%
Temperature Coefficient of Delay		100	PPM/°C
Insulation Resistance @ 100 Vdc	1K		Meg Ohms
Dielectric Strength		100	Vdc

Recommended Operating Conditions

*These two values are inter-dependent.

	Min	Max	Unit
PW*	200		%
D*		40	%
TA	0	70	°C

Input Pulse Test Conditions @ 25°C

V _{IN}	Pulse Input Voltage	3 Volts
PW	Pulse Width % of Total Delay	300 %
TRI	Input Rise Time (10 - 90%)	2.0 nS
PRR	Pulse Repetition Rate @ Td ≤ 150 nS	1.0 MHz
	Pulse Repetition Rate @ Td > 150 nS	300 KHz

Package Dimensions

