

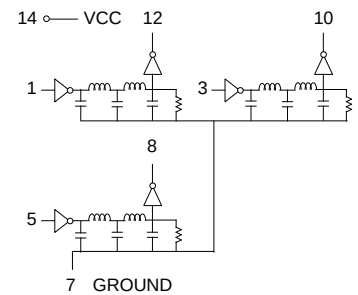
14 Pin DIP Triple TTL Compatible Active Delay Lines

DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER
5	EP9206-005	19	EP9206-019	65	EP9206-065
6	EP9206-006	20	EP9206-020	70	EP9206-070
7	EP9206-007	21	EP9206-021	75	EP9206-075
8	EP9206-008	22	EP9206-022	80	EP9206-080
9	EP9206-009	23	EP9206-023	85	EP9206-085
10	EP9206-010	24	EP9206-024	90	EP9206-090
11	EP9206-011	25	EP9206-025	95	EP9206-095
12	EP9206-012	30	EP9206-030	100	EP9206-100
13	EP9206-013	35	EP9206-035	125	EP9206-125
14	EP9206-014	40	EP9206-040	150	EP9206-150
15	EP9206-015	45	EP9206-045	175	EP9206-175
16	EP9206-016	50	EP9206-050	200	EP9206-200
17	EP9206-017	55	EP9206-055	225	EP9206-225
18	EP9206-018	60	EP9206-060	250	EP9206-250

†Whichever is greater. Delay Times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics					
Parameter	Test Conditions	Min	Max	Unit	
V _{OH}	High-Level Output Voltage	V _{CC} = min. V _{IL} = max. I _{OH} = max	2.7	V	
V _{OL}	Low-Level Output Voltage	V _{CC} = min. V _{IH} = min. I _{OL} = max	0.5	V	
V _{IK}	Input Clamp Voltage	V _{CC} = min. I _I = I _{IK}	-1.2V	V	
I _{IH}	High-Level Input Current	V _{CC} = max. V _{IN} = 2.7V	50	μA	
		V _{CC} = max. V _{IN} = 5.25V	1.0	mA	
I _{IL}	Low-Level Input Current	V _{CC} = max. V _{IN} = 0.5V	-2	mA	
I _{OS}	Short Circuit Output Current	V _{CC} = max. V _{OUT} = 0. (One output at a time)	-40	mA	
I _{CCH}	High-Level Supply Current	V _{CC} = max. V _{IN} = OPEN	115	mA	
I _{CCL}	Low-Level Supply Current	V _{CC} = max. V _{IN} = 0	115	mA	
T _{RO}	Output Rise Time	T _d ≤ 500 nS (0.75 to 2.4 Volts)	4	nS	
N _H	Fanout High-Level Output	V _{CC} = max. V _{OH} = 2.7V	20 TTL LOAD		
N _L	Fanout Low-Level Output	V _{CC} = max. V _{OL} = 0.5V	10 TTL LOAD		

Schematic



Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-Level Input Voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T _A	Operating Free-Air Temperature	0	+70	°C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C			Unit
E _{IN}	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	110	%
T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
PRR	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0	MHz
	Pulse Repetition Rate @ T _d > 200 nS	100	KHz
V _{CC}	Supply Voltage	5.0	Volts

Package Dimensions

