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Bulletin Date: 11/16/2016	Bulletin Effective Date: 11/16/2016
Title: EFM8BB1 Datasheet update to v1.5	
Bulletin Details	
Description: Silicon Labs is pleased to announce version 1.5 of the EFM8BB1 datasheet. The release corresponds to the launch of EFM8BB1 Automotive grade devices. Summary of changes: - The addition of automotive devices in the product selection table and ordering information. - Addition of AEC-Q100 under the key features and specifications. - Added a table on SMBus timing and operating frequency in master mode. The table details minimum and maximum operating frequency for each class, hold times, start times, clock low and clock high periods, and start and stop conditions. - Added a brief table on the pins used by the bootloader. - Added typical Cyclic Redundancy Check (CRC) Calculation Time for a read or write of a 256 byte block of flash at 24.5 MHz. - Added thermal resistance (junction to case) for the QFN20 package. - Added a note linking to the output low voltage and output high voltage table of the port I/Os to the performance curves. - Added a line in the introduction section to mention the reference manual where an individual can find more technical information on registers and blocks.	
Reason: EFM8BB1 V1.5 datasheet release	
Product Identification: EFM8BB10F2A-A-QFN20 EFM8BB10F4A-A-QFN20 EFM8BB10F8A-A-QFN20 EFM8BB10F2A-A-QFN20R EFM8BB10F4A-A-QFN20R EFM8BB10F8A-A-QFN20R EFM8BB10F4G-A-QFN20 EFM8BB10F4G-A-QFN20R EFM8BB10F4I-A-QFN20 EFM8BB10F4I-A-QFN20R EFM8BB10F8A-A-QFN20 EFM8BB10F8A-A-QFN20R EFM8BB10F8G-A-QFN20 EFM8BB10F8G-A-QFN20R EFM8BB10F8G-A-SOIC16	



Bulletin #1611166

EFM8BB10F8G-A-SOIC16R
EFM8BB10F8G-A-QSOP24
EFM8BB10F8G-A-QSOP24R
EFM8BB10F8I-A-QFN20
EFM8BB10F8I-A-QFN20R
EFM8BB10F8I-A-SOIC16
EFM8BB10F8I-A-SOIC16R
EFM8BB10F8I-A-QSOP24
EFM8BB10F8I-A-QSOP24R
EFM8BB10F8Y-A-QFN20
EFM8BB10F8Y-A-QFN20R
EFM8BB10F2A-A-QFN20
EFM8BB10F2A-A-QFN20R
EFM8BB10F2G-A-QFN20
EFM8BB10F2G-A-QFN20R
EFM8BB10F2I-A-QFN20
EFM8BB10F2I-A-QFN20R

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Customer Actions Needed:

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