



## **ERRATA SHEET DS2148/DS21Q48**

### **Revision A2 Errata**

The errata listed below describe situations where DS2148/DS21Q48 revision A2 components perform differently than expected or differently than described in the data sheet. Maxim Integrated Products, Inc., intends to correct these errata in subsequent die revisions.

This errata sheet only applies to DS2148/DS21Q48 revision A2 components. Revision A2 components are branded on the topside of the package with a six-digit code in the form yywwA2, where yy and ww are two-digit numbers representing the year and work week of manufacture, respectively. To obtain an errata sheet on another DS2148/DS21Q48 die revision, visit our website at [www.maxim-ic.com/errata](http://www.maxim-ic.com/errata).

#### **1) TRANSMIT OPEN-CIRCUIT DETECTION**

**Description:**

The transmit open-circuit detect bit (TOCD) in the status register should be set when the device detects an open circuit at TTIP and TRING. The performance of this bit (SR.1) is unreliable, especially with sparse data patterns when B8ZS is not used, and it is not tested.

**Workaround:**

None.

#### **2) RECEIVE LEVEL INDICATION**

**Description:**

The DS2148 and DS21Q48 should report the signal strength in RTIP and RRING in 2.5dB steps through the RL[3:0] bits in receive information register 2. The measurement of the receive level is accurate to  $\pm 2$  LSBs (i.e.,  $\pm 5$ dB).

**Workaround:**

None.

#### **3) AUTOMATIC TRANSMIT UNFRAMED ALL ONES**

**Description:**

When ATUA1 is enabled (CCR3.6 = 1), these devices do not automatically transmit an unframed all-ones pattern at TTIP and TRING during a carrier loss (RCL) condition.

**Workaround:**

None.