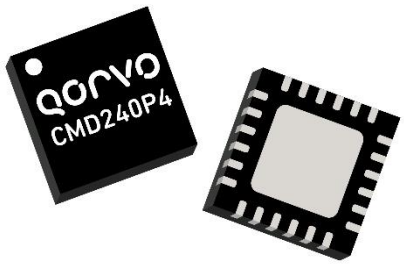
CMD240P4

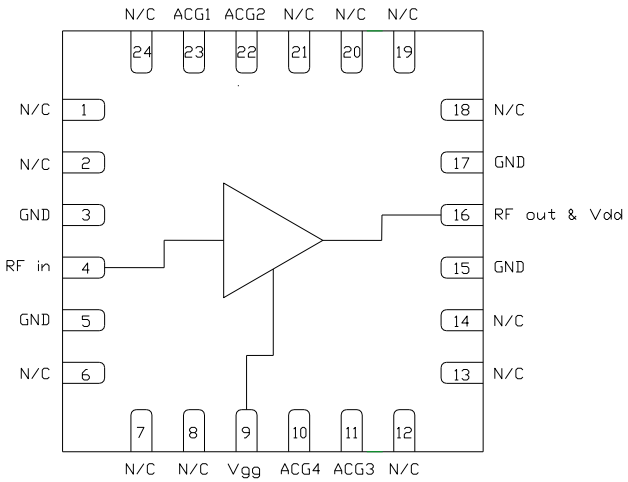
DC-22 GHz Distributed Amplifier

Product Overview

The CMD240P4 is wideband GaAs MMIC distributed amplifier housed in a leadless 4x4 mm surface mount package. The amplifier operates from DC to 22 GHz and delivers greater than 15 dB of gain with a corresponding noise figure of 2.2 dB and output 1 dB compression point of +19 dBm at 10 GHz. The CMD240P4 is a 50 ohm matched design which eliminates the need for RF port matching.



Functional Block Diagram



Key Features

- Ultra Wideband Performance
- Low Noise Figure
- Low Current Consumption
- Excellent Return Losses
- Pb-Free RoHS Compliant 4x4 QFN Package

Ordering Information

Part No.	Description
CMD240P4	100 pcs on 7” reel

Electrical Performance (V_{dd} = 5.0 V, I_{dd} = 80 mA, T_A = 25° C, F = 10 GHz)

Parameter	Min	Typ	Max	Units
Frequency Range		DC - 22		GHz
Gain		15		dB
Noise Figure		2.2		dB
Input Return Loss		19		dB
Output Return Loss		18		dB
Output P1dB		19		dBm
Output IP3		26		dBm
Output IP2		31		dBm
Supply Current		80		mA

Absolute Maximum Ratings

Parameter	Rating
Drain Voltage, V_{dd}	10 V
Gate Voltage, V_{gg}	-2.5 to 0 V
RF Input Power	+20 dBm
Channel Temperature, Tch	150°C
Power Dissipation, Pdiss	1.7 W
Thermal Resistance, Q_{JC}	38°C/W
Operating Temperature	-40 to 85°C
Storage Temperature	-55 to 150°C

Exceeding any one or combination of the maximum ratings may cause permanent damage to the device.

Recommended Operating Conditions

Parameter	Min	Typ	Max	Units
V_{dd}	5.0	5.0	8.0	V
I_{dd}		80		mA
V_{gg}		-0.6		V

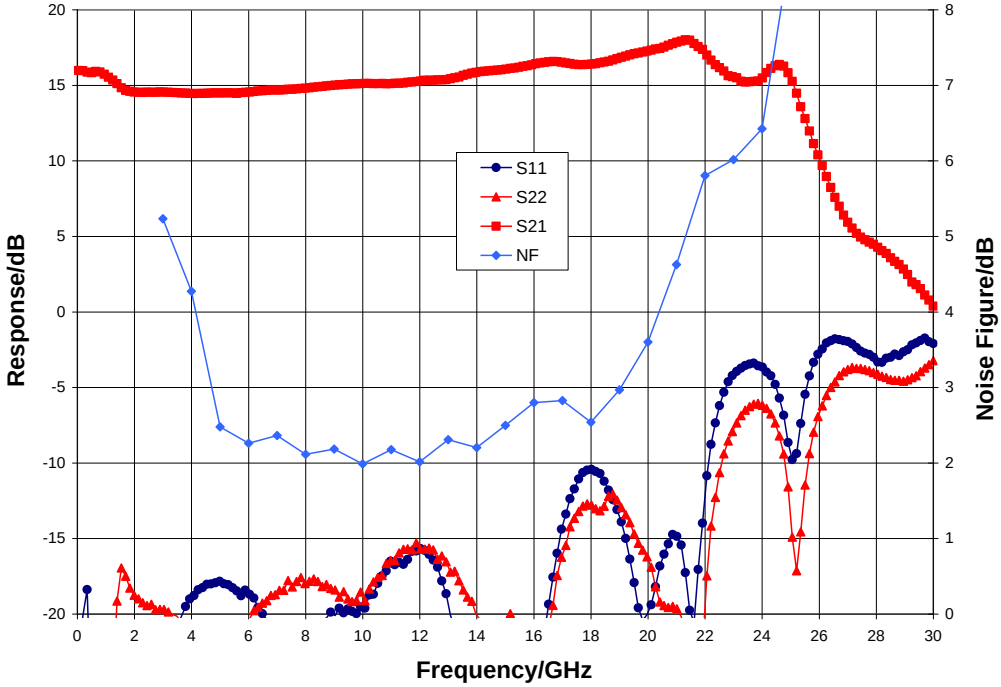
Electrical performance is measured at specific test conditions. Electrical specifications are not guaranteed over all recommended operating conditions.

Electrical Specifications (V_{dd} = 5.0 V, I_{dd} = 80 mA, T_A = 25°C)

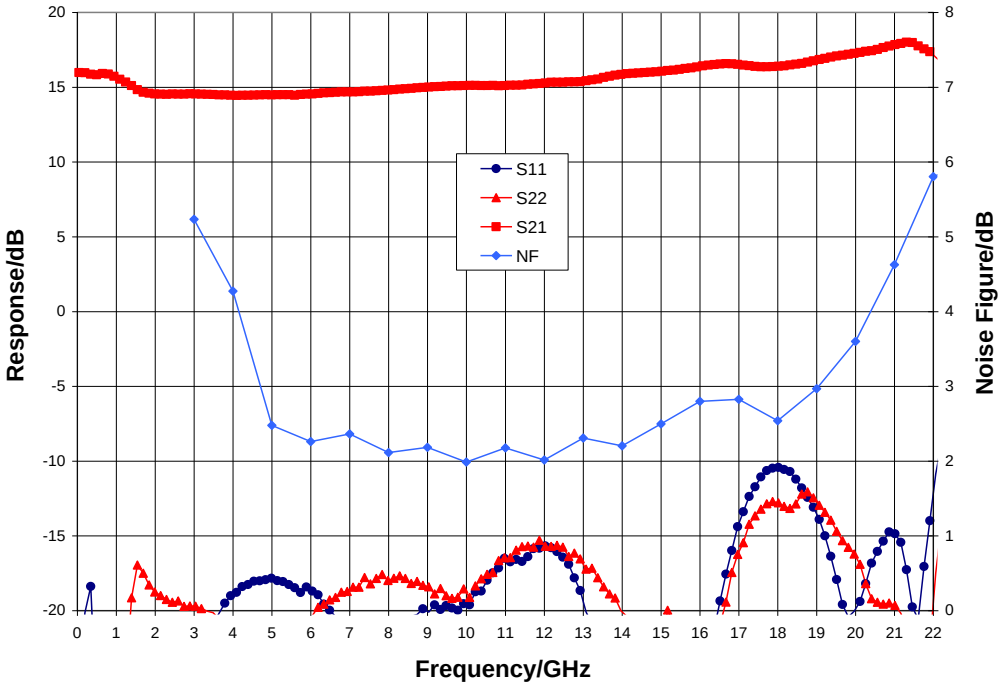
Parameter	Min	Typ	Max	Min	Typ	Max	Units
Frequency Range		DC - 22			6 - 18		GHz
Gain	12	15		12	15		dB
Noise Figure		2.5			2.2		dB
Input Return Loss		15			15		dB
Output Return Loss		13			15		dB
Output P1dB	13	18		15	19		dBm
Output IP3		26			26		dBm
Output IP2		33			33		dBm
Supply Current	55	80	105	55	80	105	mA
Gain Temperature Coefficient		0.008			0.008		dB/°C
Noise Figure Temperature Coefficient		0.009			0.009		dB/°C

Typical Performance

Broadband Performance, V_{dd} = 5 V, I_{dd} = 80 mA, T_A = 25° C

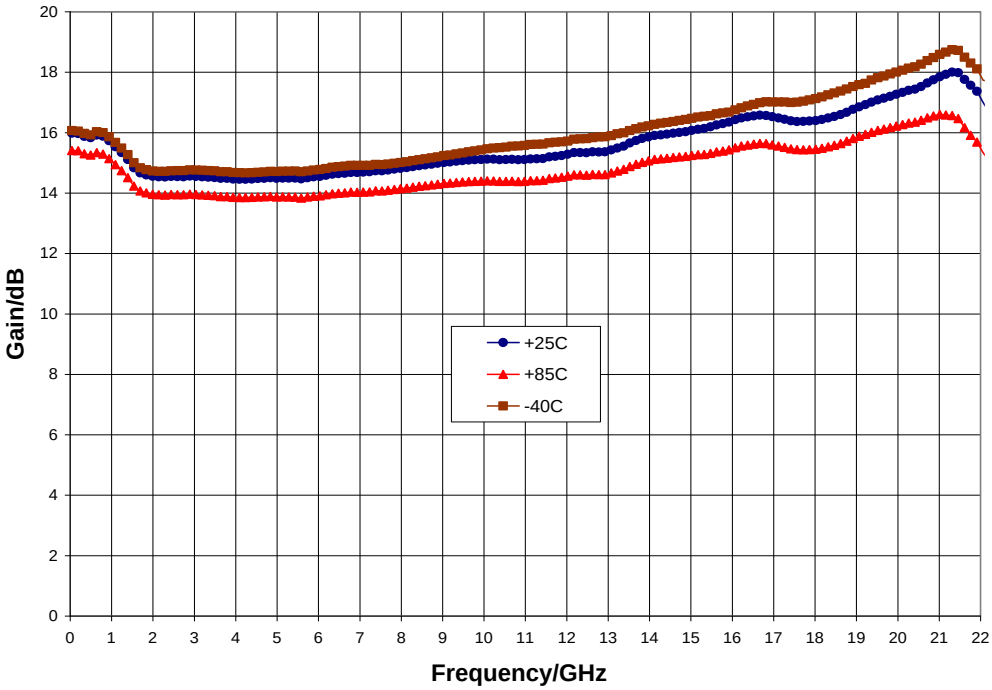


Narrow-band Performance, V_{dd} = 5 V, I_{dd} = 80 mA, T_A = 25° C

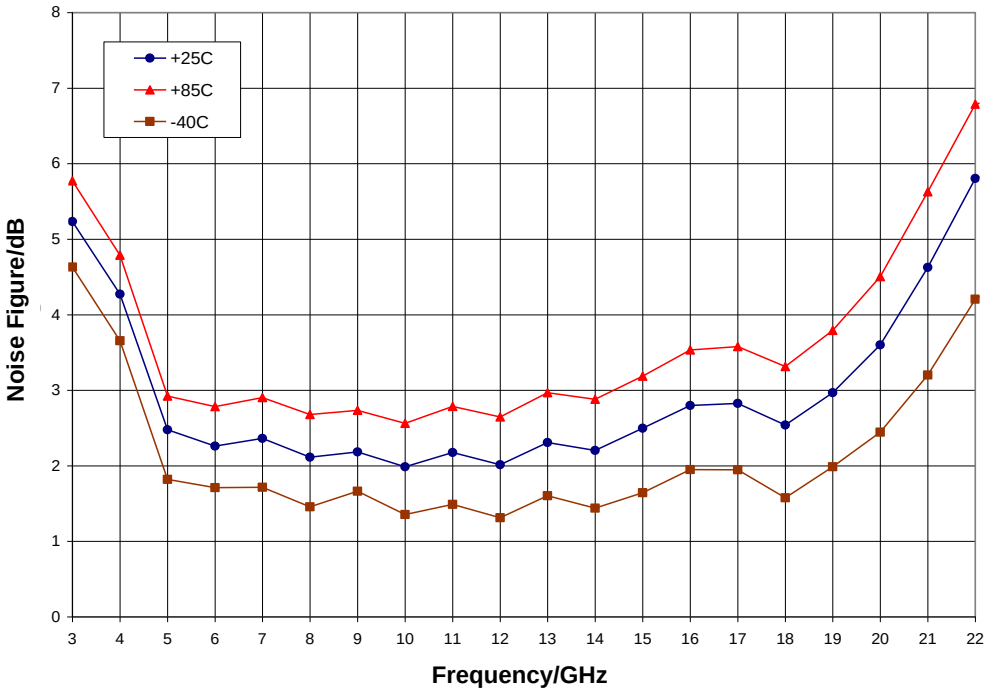


Typical Performance

Gain vs. Temperature, V_{dd} = 5 V

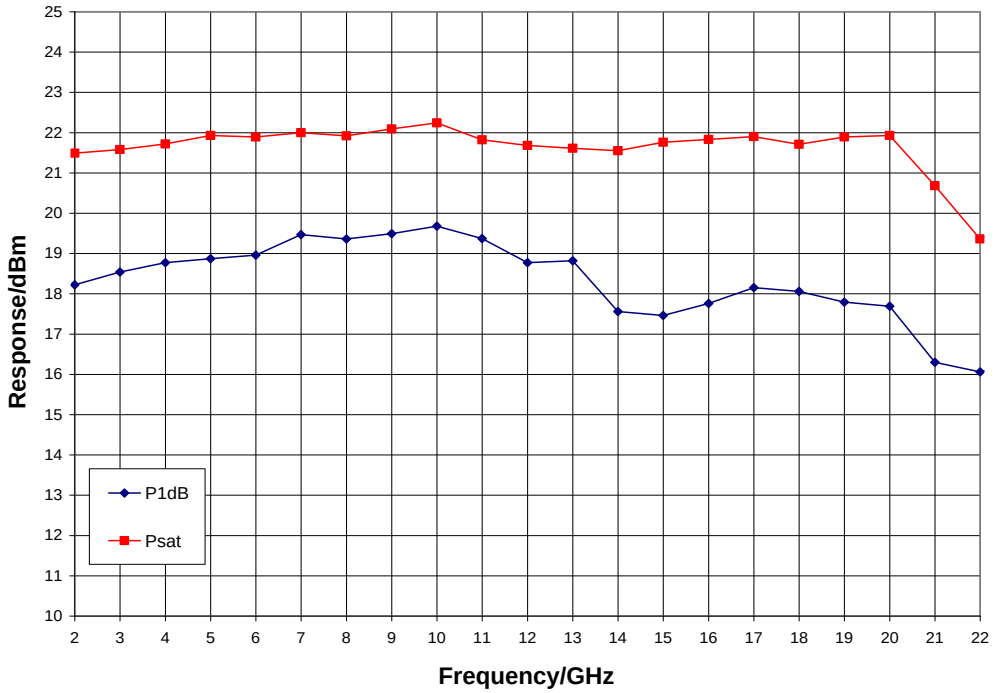


Noise Figure vs. Temperature, V_{dd} = 5 V

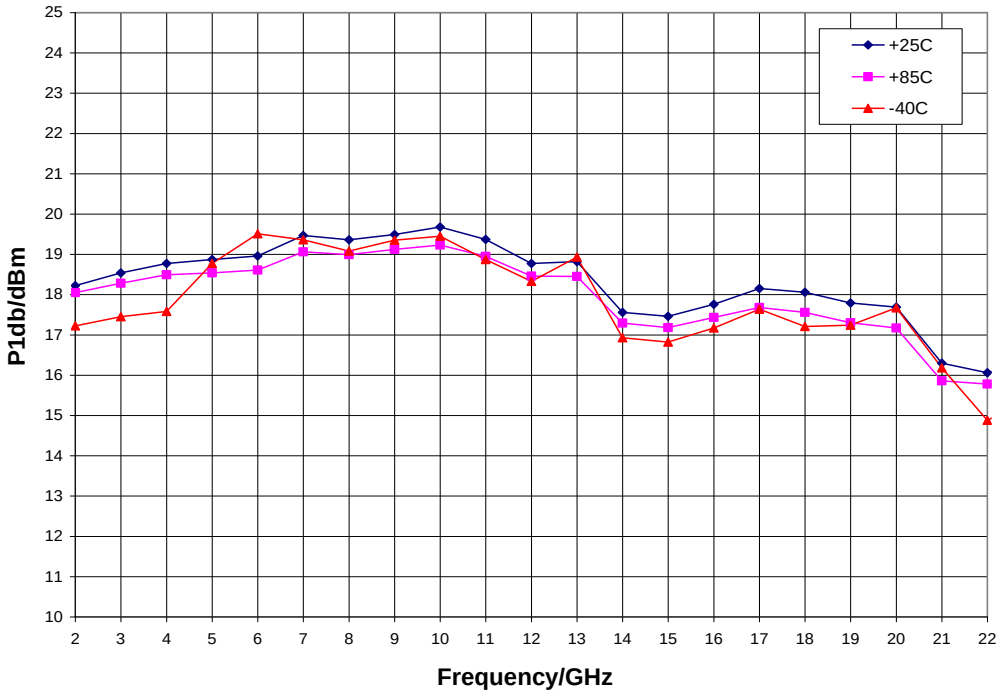


Typical Performance

Output Power, V_{dd} = 5 V, T_A = 25° C

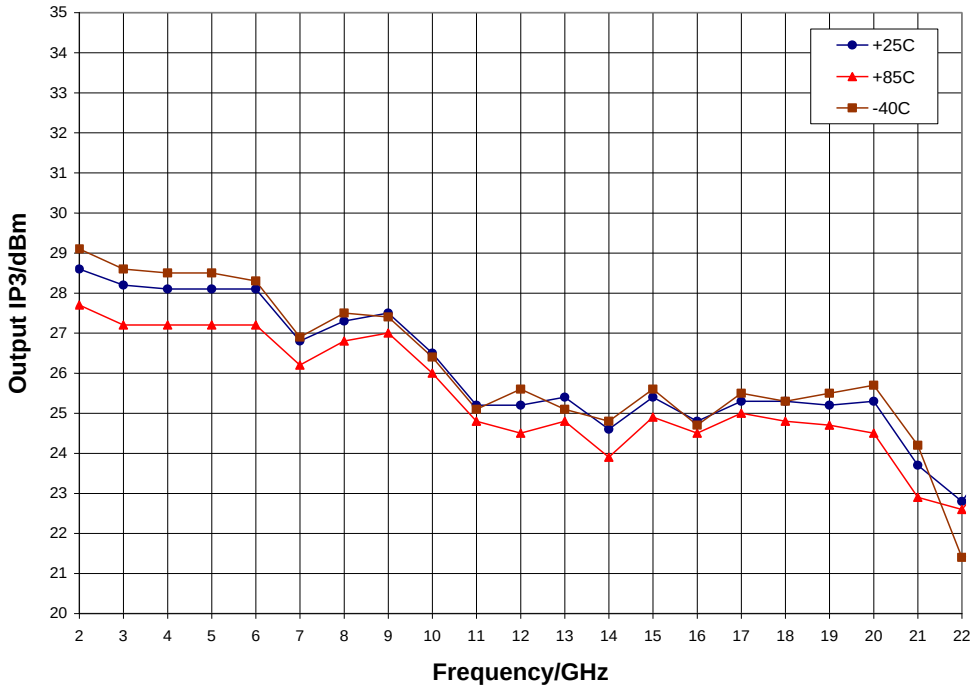


P1dB vs. Temperature, V_{dd} = 5 V

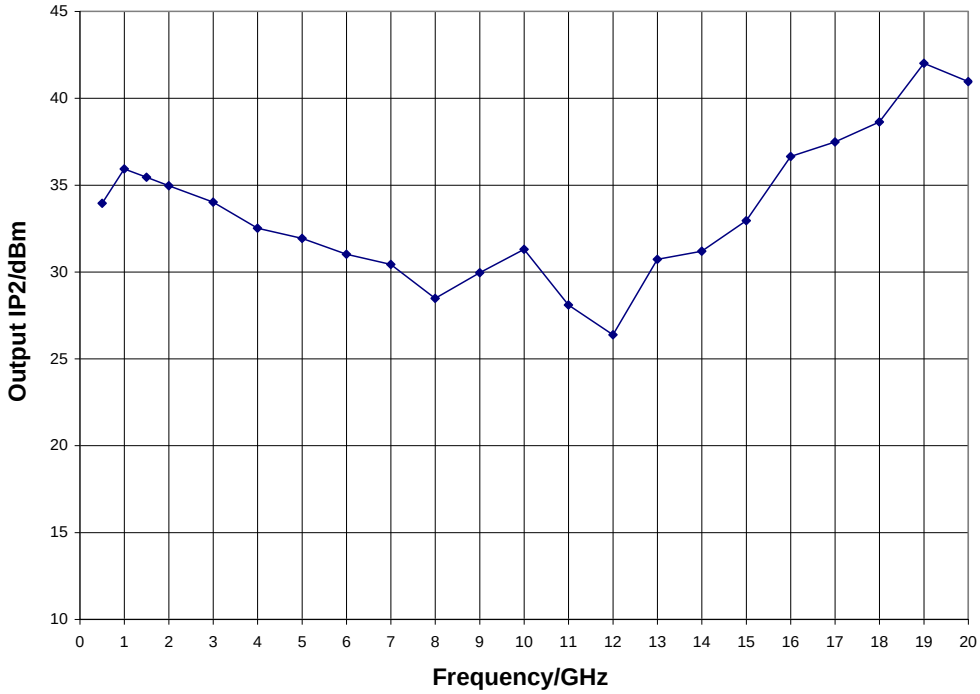


Typical Performance

Output IP3 vs. Temperature, V_{dd} = 5 V

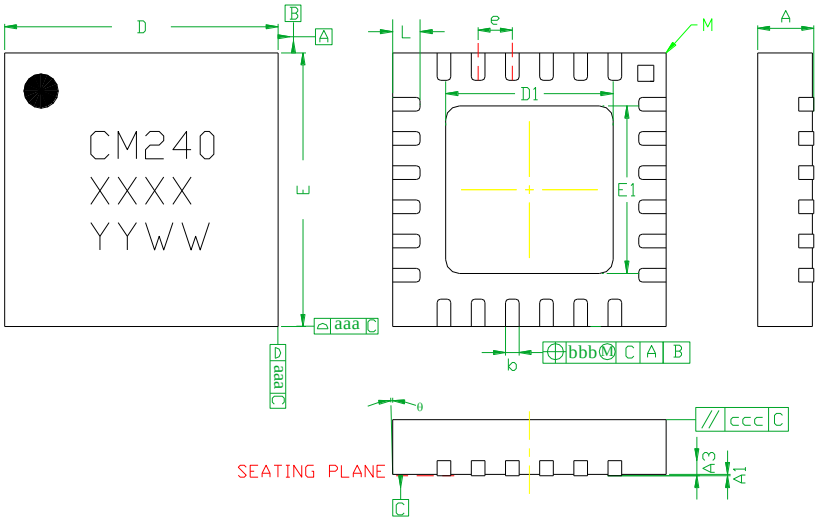


Output IP2, V_{dd} = 5 V, T_A = 25° C



Mechanical Information

Package Information and Dimensions



SYMBOLS	DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MAX
A	0.80	0.90	1.00
A1	0	0.02	0.05
A3	---	0.25REF.	---
b	0.18	0.23	0.30
D	3.85	4.00	4.15
D1	2.40	2.50	2.60
E	3.85	4.00	4.15
E1	2.40	2.50	2.60
e	---	0.50BSC	---
L	0.30	0.40	0.50
θ	0	---	12
aaa	---	0.25	---
bbb	---	0.10	---
ccc	---	0.10	---
M	---	---	0.05

Notes:
1. Dimensions are in millimeters
2. RoHS compliant mold compound
3. Lead frame material: Copper alloy
4. Lead finish: 100% matte Sn
5. Indicated dimension/tolerance applies to leads and exposed pads

Recommended PCB Land Pattern

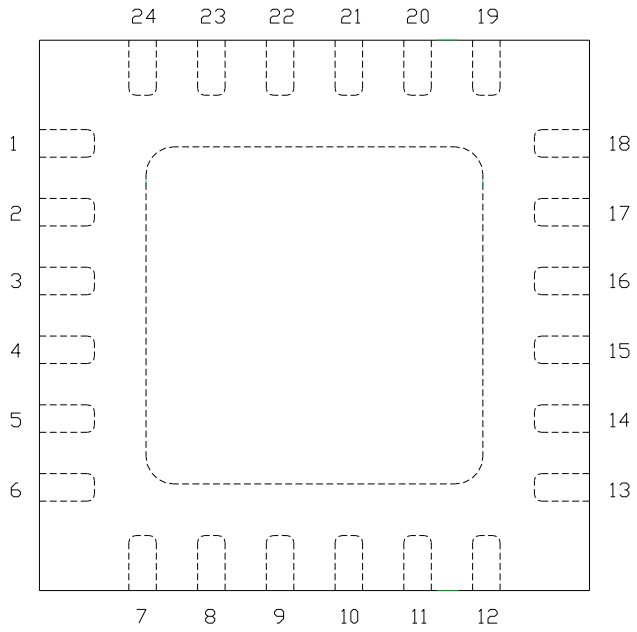
Qorvo recommends that the user develop the land pattern that will provide the best design for proper solder reflow and device attach for their specific application. Please review Qorvo Application Note AN 105 for a recommended land pattern approach.

Recommended Solder Reflow Profile

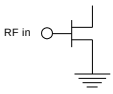
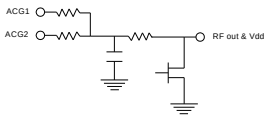
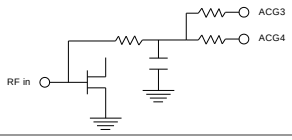
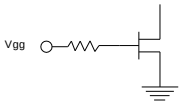
Qorvo recommends screen printing with belt furnace reflow to ensure proper solder reflow and device attach. Please review Qorvo Application Note AN 102 for a recommended solder reflow profile.

Pin Description

Pin Diagram

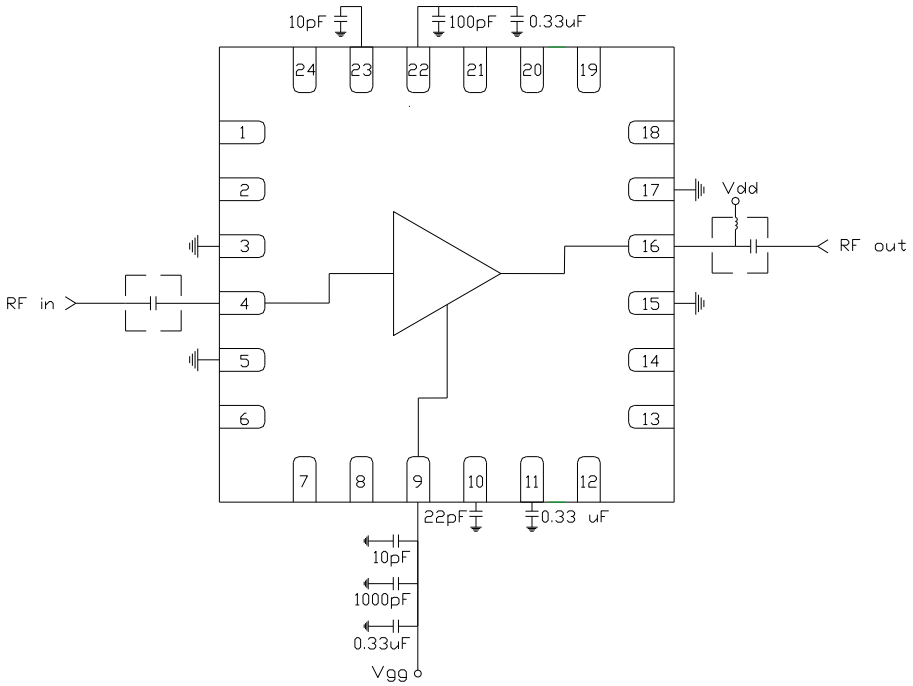


Functional Description

Pin	Function	Description	Schematic
1, 2, 6 - 8, 12 - 14, 18 - 21, 24	N/C	No connection required These pins may be connected to RF / DC ground	
4	RF in	50 ohm matched input	
22, 23	ACG2, 1	Low frequency termination Attach bypass capacitors per application circuit	
16	RF out & V_{dd}	Power supply voltage and 50 ohm matched output	
10, 11	ACG4, 3	Low frequency termination Attach bypass capacitors per application circuit	
9	V_{gg}	Power supply voltage Decoupling and bypass caps required	
3, 5, 15, 17 and die paddle	Ground	Connect to RF / DC ground	

Applications Information

Application Circuit



Note: Drain voltage (V_{dd}) must be applied through a broadband bias tee or external bias network.
External DC block is required on RF input.

Biasing and Operation

The CMD240P4 is biased with a positive drain supply and negative gate supply. Performance is optimized when the drain voltage is set to +5.0 V. The nominal gate voltage is -0.6 V.

Turn ON procedure:

1. Apply gate voltage V_{gg} and set to -2 V
2. Apply drain voltage V_{dd} and set to +5 V
3. Increase V_{gg} (less negative) to achieve a drain current of 80 mA

Turn OFF procedure:

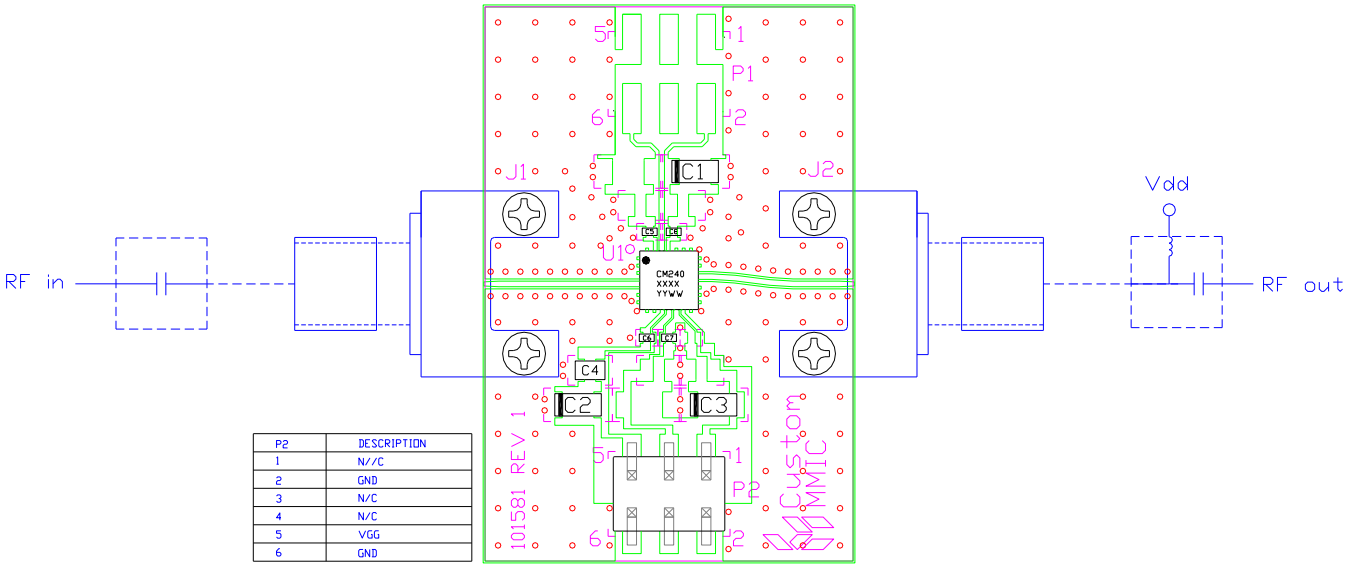
1. Turn off drain voltage V_{dd}
2. Turn off gate voltage V_{gg}

RF power can be applied at any time.

GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

Applications Information

Evaluation Board



Bill of Material

Designator	Value	Description
J1, J2		SMA End Launch Connector
P1		6 Pin DC Header
C1 - C3	0.33 µF	Capacitor, Tantalum
C4	1000 pF	Capacitor, 0603
C5, C6	10 pF	Capacitor, 0402
C7	22 pF	Capacitor, 0402
C8	100 pF	Capacitor, 0402
U1		CMD240P4 Driver Amplifier
PCB		101581 Evaluation PCB

Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 1A	ESDA / JEDEC JS-001-2012
MSL – Moisture Sensitivity Level	Level 1	IPC/JEDEC J-STD-020



Caution!
ESD-Sensitive Device

RoHS Compliance

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Antimony Free
- TBBP-A ($C_{15}H_{12}Br_4O_2$) Free
- SVHC Free
- PFOS Free
- Halogen Free



Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

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Email: customer.support@qorvo.com

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