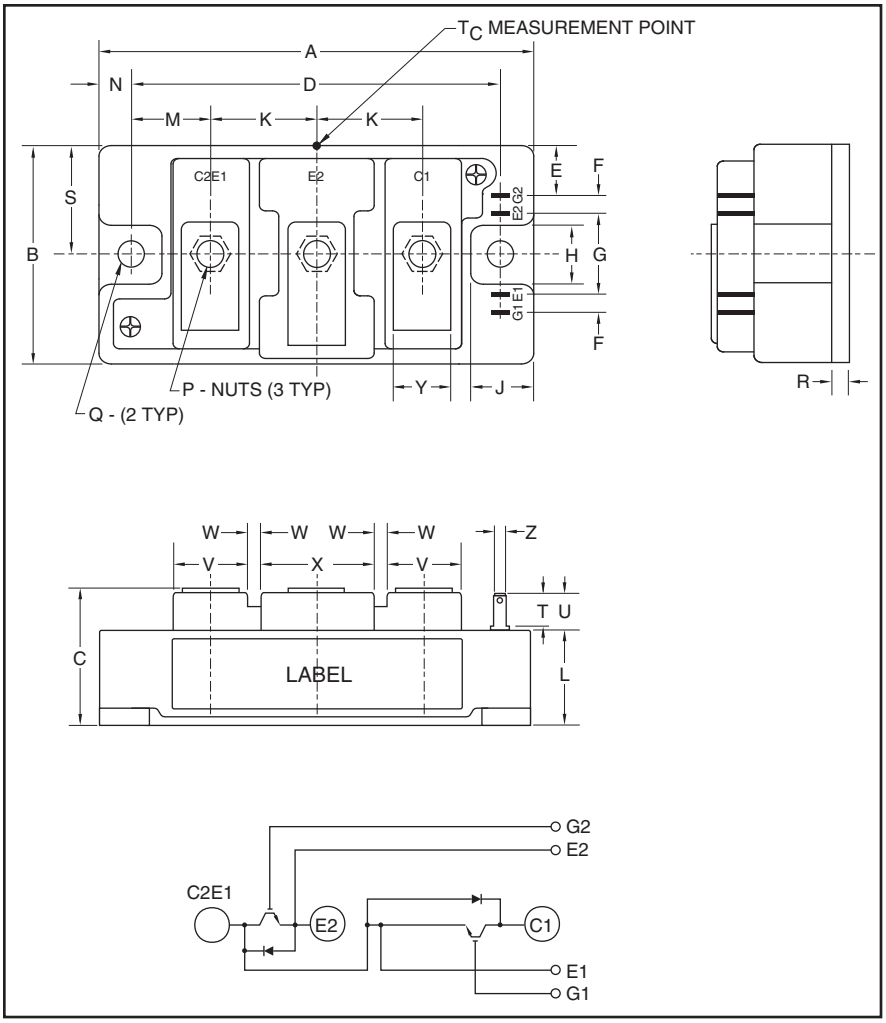
Powerex, Inc., 173 Pavilion Lane, Youngwood, Pennsylvania 15697 (724) 925-7272

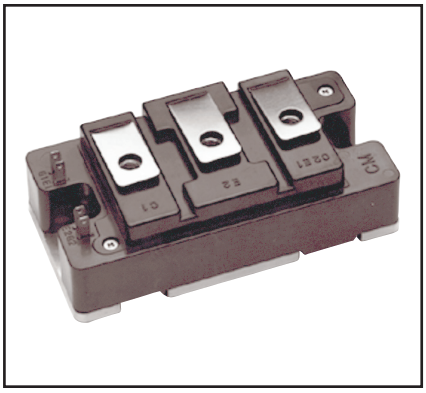
Dual IGBTMOD™ NFH-Series Module
100 Amperes/1200 Volts



Outline Drawing and Circuit Diagram

Dimensions	Inches	Millimeters
A	3.70	94.0
B	1.89	48.0
C	1.18+0.04/-0.01	30.0+1.0/-0.5
D	3.15±0.01	80.0±0.25
E	0.43	11.0
F	0.16	4.0
G	0.71	18.0
H	0.51	13.0
J	0.53	13.5
K	0.91	23.0
L	0.83	21.2
M	0.67	17.0

Dimensions	Inches	Millimeters
N	0.28	7.0
P	M5 Metric	M5
Q	0.26 Dia.	Dia. 6.5
R	0.02	4.0
S	0.94	24.0
T	0.3	7.5
U	0.33	8.5
V	0.63	16.0
W	0.1	2.5
X	0.98	25.0
Y	0.47	12.0
Z	0.11	2.8



Description:
Powerex IGBTMOD™ Modules are designed for use in high frequency applications; 30 kHz for hard switching applications and 60 to 70 kHz for soft switching applications. Each module consists of two IGBT Transistors in a half-bridge configuration with each transistor having a reverse-connected super-fast recovery free-wheel diode. All components and interconnects are isolated from the heat sinking baseplate, offering simplified system assembly and thermal management.

Features:
- ☐ Low $E_{SW(off)}$
- ☐ Discrete Super-Fast Recovery Free-Wheel Diode
- ☐ Isolated Baseplate for Easy Heat Sinking

Applications:
- ☐ Power Supplies
- ☐ Induction Heating
- ☐ Welders

Ordering Information:
Example: Select the complete part module number you desire from the table below -i.e. CM100DU-24NFH is a 1200V (V_{CES}), 100 Ampere Dual IGBTMOD™ Power Module.

Type	Current Rating Amperes	V_{CES} Volts (x 50)
CM	100	24

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Absolute Maximum Ratings, T_j = 25 °C unless otherwise specified

Ratings	Symbol	CM100DU-24NF	Units
Junction Temperature	T_j	–40 to 150	°C
Storage Temperature	T_{stg}	–40 to 125	°C
Collector-Emitter Voltage (G-E Short)	V_{CES}	1200	Volts
Gate-Emitter Voltage (C-E Short)	V_{GES}	±20	Volts
Collector Current (T_C = 25°C)	I_C	100*	Amperes
Peak Collector Current	I_{CM}	200*	Amperes
Emitter Current** (T_C = 25°C)	I_E	100*	Amperes
Peak Emitter Current**	I_{EM}	200*	Amperes
Maximum Collector Dissipation (T_C = 25°C, T_j ≤ 150°C)	P_C	560	Watts
Maximum Collector Dissipation ($T_{C'}$ = 25°C, $T_{j'}$ ≤ 150°C)	P_C	730	Watts
Mounting Torque, M5 Main Terminal	—	30	in-lb
Mounting Torque, M6 Mounting	—	40	in-lb
Weight	—	310	Grams
Isolation Voltage (Main Terminal to Baseplate, AC 1 min.)	V_{ISO}	2500	Volts

Static Electrical Characteristics, T_j = 25 °C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Collector-Cutoff Current	I_{CES}	$V_{CE} = V_{CES}$, V_{GE} = 0V	—	—	1.0	mA
Gate Leakage Current	I_{GES}	$V_{GE} = V_{GES}$, V_{CE} = 0V	—	—	0.5	µA
Gate-Emitter Threshold Voltage	$V_{GE(th)}$	I_C = 10mA, V_{CE} = 10V	4.5	6.0	7.5	Volts
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	I_C = 100A, V_{GE} = 15V, T_j = 25°C	—	5.0	6.5	Volts
		I_C = 100A, V_{GE} = 15V, T_j = 125°C	—	5.0	—	Volts
Total Gate Charge	Q_G	V_{CC} = 600V, I_C = 100A, V_{GE} = 15V	—	450	—	nC
Emitter-Collector Voltage**	V_{EC}	I_E = 100A, V_{GE} = 0V	—	—	3.5	Volts

Dynamic Electrical Characteristics, T_j = 25 °C unless otherwise specified

Characteristics		Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance		C_{ies}	V_{CE} = 10V, V_{GE} = 0V	—	—	16	nf
Output Capacitance		C_{oes}		—	—	1.3	nf
Reverse Transfer Capacitance		C_{res}		—	—	0.3	nf
Inductive	Turn-on Delay Time	$t_{d(on)}$	V_{CC} = 600V, I_C = 100A, $V_{GE1} = V_{GE2}$ = 15V, R_G = 3.1Ω, Inductive Load Switching Operation, I_E = 100A	—	—	100	ns
Load	Rise Time	t_r		—	—	50	ns
Switch	Turn-off Delay Time	$t_{d(off)}$		—	—	250	ns
Time	Fall Time	t_f		—	—	150	ns
Diode Reverse Recovery Time**		t_{rr}		—	—	150	ns
Diode Reverse Recovery Charge**		Q_{rr}		—	5.0	—	µC

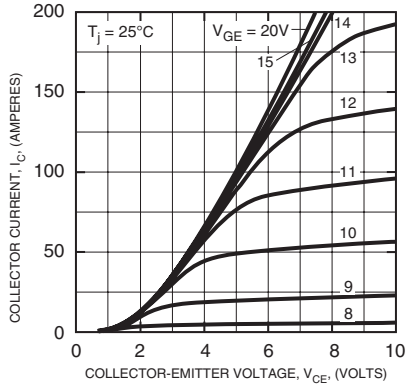
* Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed $T_{j(max)}$ rating.
**Represents characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

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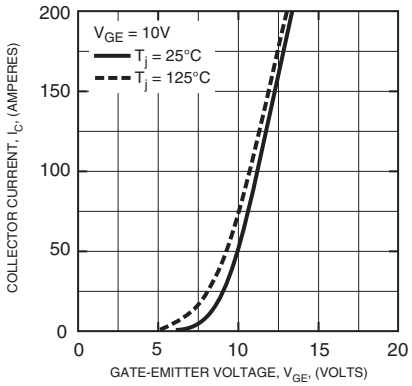
Thermal and Mechanical Characteristics, T_j = 25 °C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case	$R_{th(j-c)}Q$	Per IGBT 1/2 Module, T_C Reference Point per Outline Drawing	—	—	0.22	°C/W
Thermal Resistance, Junction to Case	$R_{th(j-c)}D$	Per FWDi 1/2 Module, T_C Reference Point per Outline Drawing	—	—	0.47	°C/W
Thermal Resistance, Junction to Case	$R_{th(j-c)}'Q$	Per IGBT 1/2 Module, T_C Reference Point Under Chips	—	—	0.17	°C/W
Thermal Resistance, Junction to Case	$R_{th(j-c)}'D$	Per FWDi 1/2 Module, T_C Reference Point per Outline Drawing	—	—	0.29	°C/W
Contact Thermal Resistance	$R_{th(c-f)}$	Per 1/2 Module, Thermal Grease Applied	—	0.07	—	°C/W
External Gate Resistance	R_G		3.1	—	31	Ω

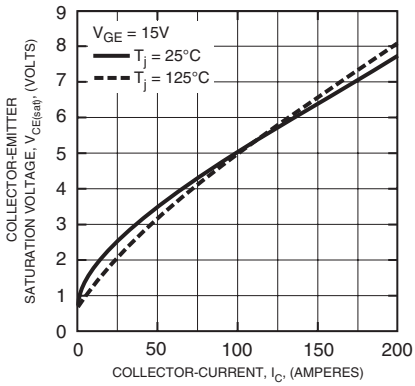
OUTPUT CHARACTERISTICS (TYPICAL)



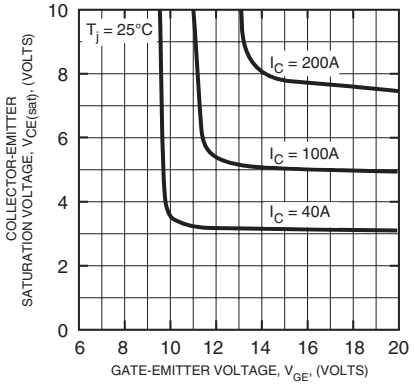
TRANSFER CHARACTERISTICS (TYPICAL)



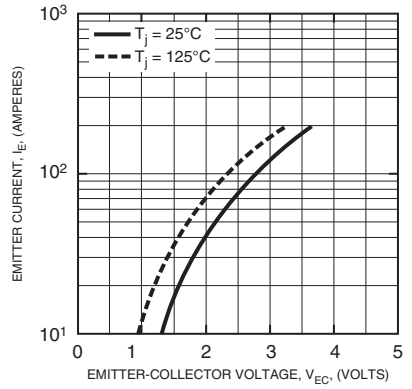
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



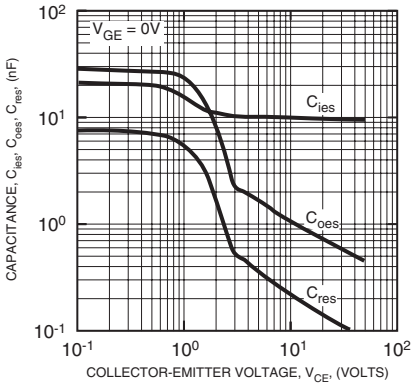
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)

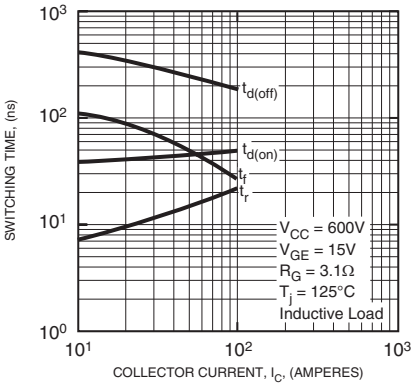


CAPACITANCE VS. V_{CE} (TYPICAL)

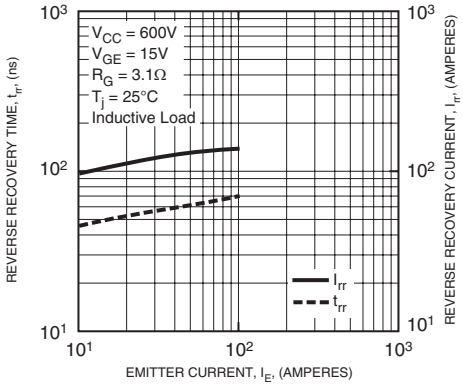


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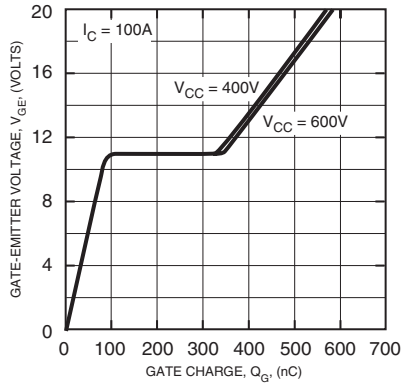
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



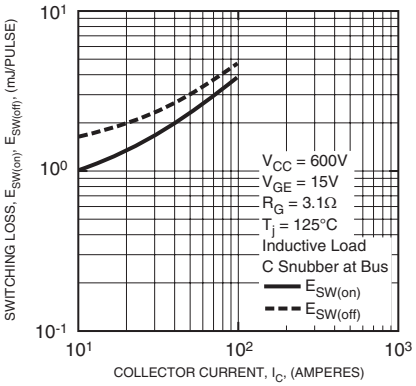
REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



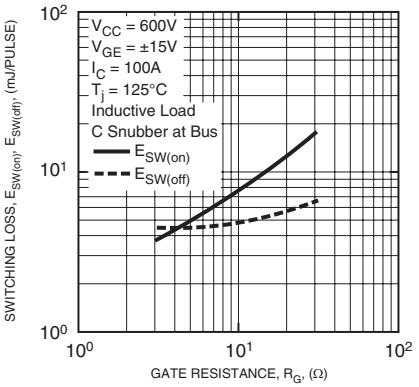
GATE CHARGE VS. V_{GE}



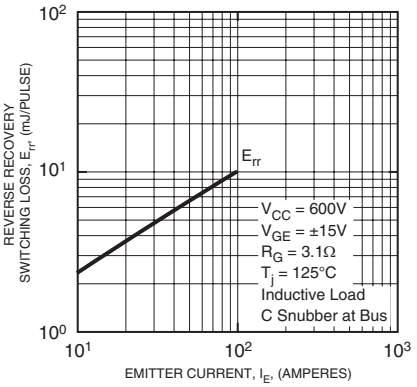
SWITCHING LOSS VS. COLLECTOR CURRENT (TYPICAL)



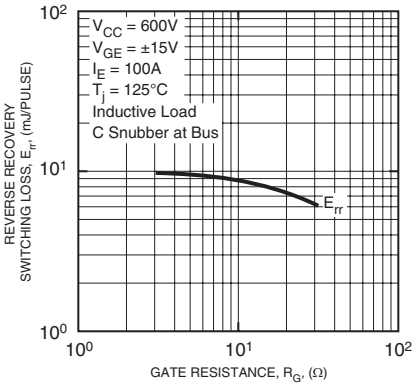
SWITCHING LOSS VS. GATE RESISTANCE (TYPICAL)



REVERSE RECOVERY SWITCHING LOSS VS. EMITTER CURRENT (TYPICAL)



REVERSE RECOVERY SWITCHING LOSS VS. GATE RESISTANCE (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (IGBT & FWDi)

