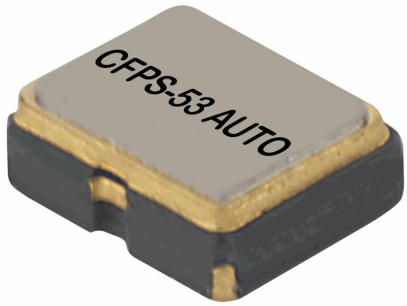
Crystal Clock Oscillator Specification
CFPS-53 AUTO

ISSUE 1; May 2016

Description

- Ceramic package with a hermetically seam sealed metal lid suitable for automotive applications. Qualified to AEC-Q200 and with TS16949 release.



Frequency Parameters

Frequency	2.0MHz to 50.0MHz
Frequency Stability	±25.00ppm to ±100.00ppm
Ageing	±5ppm max per year @ 25°C

Electrical Parameters

Supply Voltage	1.8V ±5%

Operating Temperature Ranges

- -40 to 85°C
- -40 to 125°C

Output Details

Output Compatability	CMOS
Drive Capability	15pF

Output Control

- Enable/Disable Operation:
  Logic '1' (>70% Vs) to pad 1 enables oscillator output.
  Logic '0' (<30% Vs) to pad 1 disables oscillator output; the oscillator output goes to the high impedance state.
  No connection to pad 1 enables oscillator output.
- Standby Current:
  -40 to 85°C: 10µA max
  -40 to 125°C: 20µA max

Environmental Parameters

- Storage Temperature Range: -55 to 150°C
- Qualified to AEC-Q200

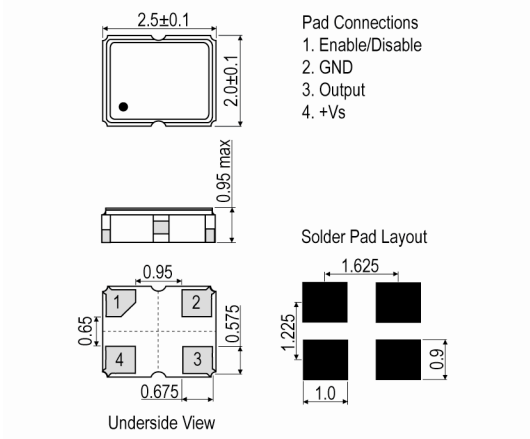
Ordering Information

- Frequency*
  Model*
  Output
  Frequency Stability*
  Operating Temperature Range*
  Supply Voltage
  (*minimum required)
- Example
  20.0MHz CFPS-53 AUTO
  CMOS ±100ppm –40 to 125C 1.8V

Compliance

RoHS Status (2011/65/EU)	Compliant
REACh Status	Compliant
MSL Rating (JDEC-STD-033):	Not Applicable

Outline (mm)



Sales Office Contact Details:

UK: +44 (0)1460 270200
France: 0800 901 383
Email: info@iqdfrequencyproducts.com
Germany: 0800 1808 443
USA: +1.760.318.2824
Web: www.iqdfrequencyproducts.com

Packaging Details

- Pack Style: Cutt — In tape, cut from a reel
  Pack Size: 1
- Pack Style: Reel — Tape & reel in accordance with EIA-481-D
  Pack Size: 1,000

Electrical Specification - maximum limiting values 1.8V ±5%

Frequency Min	Frequency Max	Temperature Range	Stability (Min)	Current Draw	Rise and Fall Time	Duty Cycle
		°C	ppm	mA	ns	%
2.0MHz	9.999999MHz	-40 to 85	±25.0	3	5	40/60%
		-40 to 125	±50.0	3	5	40/60%
10.0MHz	19.999999MHz	-40 to 85	±25.0	5	5	40/60%
		-40 to 125	±50.0	5	5	40/60%
20.0MHz	31.999999MHz	-40 to 85	±25.0	5	5	40/60%
		-40 to 125	±50.0	5	5	40/60%
32.0MHz	50.0MHz	-40 to 85	±25.0	10	5	40/60%
		-40 to 125	±50.0	10	5	40/60%

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Sales Office Contact Details:
UK: +44 (0)1460 270200
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Germany: 0800 1808 443
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