

RazerThin® Gen III LEDs

CxxxRT200-Sxxxx

Cree's RazerThin LEDs are a new generation of solid-state LED emitters that combine highly efficient InGaN materials with Cree's proprietary G•SiC® substrate to deliver superior price/performance for high-intensity blue and green LEDs. These vertically structured LED chips are approximately 95 microns in height and require a low forward voltage. Cree's RazerThin series chips have the ability to withstand 1000 V ESD.

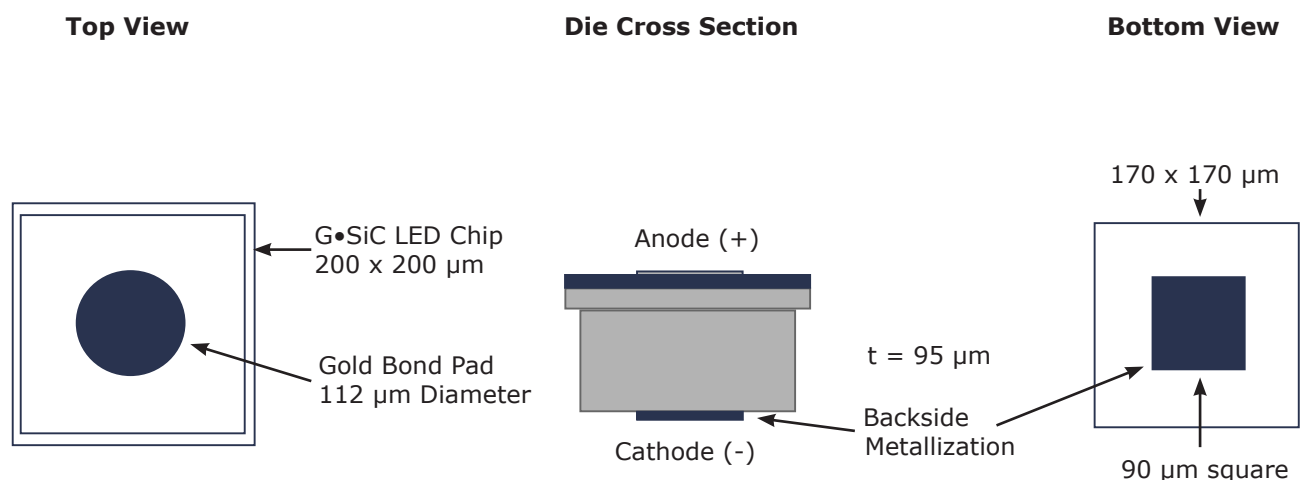
FEATURES

- Thin 95 μm Chip
- Reduced Forward Voltage
 - 3.0 V Typical at 5 mA
- RazerThin LED Performance
 - 460 nm - 10 mW min.
 - 470 nm - 8 mW min.
 - 527 nm - 2 mW min.
- Single Wire Bond Structure
- Class 2 ESD Rating

APPLICATIONS

- Mobile Phone Key Pads
 - White LEDs
 - Blue LEDs
 - Green LEDs
- Cellular Phone LCD Backlighting
- Automotive Dashboard Lighting
- LED Video Displays
- Audio Product Display Lighting

CxxxRT200-Sxxxx Chip Diagram





Maximum Ratings at $T_A = 25^{\circ}\text{C}$ Notes 1&3		CxxxRT200-Sxxxx
DC Forward Current		30 mA
Peak Forward Current (1/10 duty cycle @ 1kHz)		100 mA
LED Junction Temperature		125°C
Reverse Voltage		5 V
Operating Temperature Range		-40°C to +100°C
Storage Temperature Range		-40°C to +100°C
Electrostatic Discharge Threshold (HBM) Note 2		1000 V
Electrostatic Discharge Classification (MIL-STD-883E) Note 2		Class 2

Typical Electrical/Optical Characteristics at $T_A = 25^{\circ}\text{C}$, $I_F = 5 \text{ mA}$ Note 3					
Part Number	Forward Voltage (V_F , V)			Reverse Current [$I(V_R=5\text{V})$, μA]	Full Width Half Max. (λ_{DF} , nm)
	Min.	Typ.	Max.	Max.	Typ.
C460RT200-Sxxxx	2.7	3.0	3.3	1	24
C470RT200-Sxxxx	2.7	3.0	3.3	1	25
C527RT200-Sxxxx	2.7	3.1	3.4	1	40

Mechanical Specifications			CxxxRT200-Sxxxx	
Description	Dimension		Tolerance	
P-N Junction Area (μm)	150 x 150		± 35	
Top Area (μm)	200 x 200		± 35	
Bottom Area (μm)	170 x 170		± 35	
Chip Thickness (μm)	95		± 15	
Au Bond Pad Diameter (μm)	112		± 20	
Au Bond Pad Thickness (μm)	1.0		± 0.5	
Back Contact Metal Width (μm)	90		± 10	

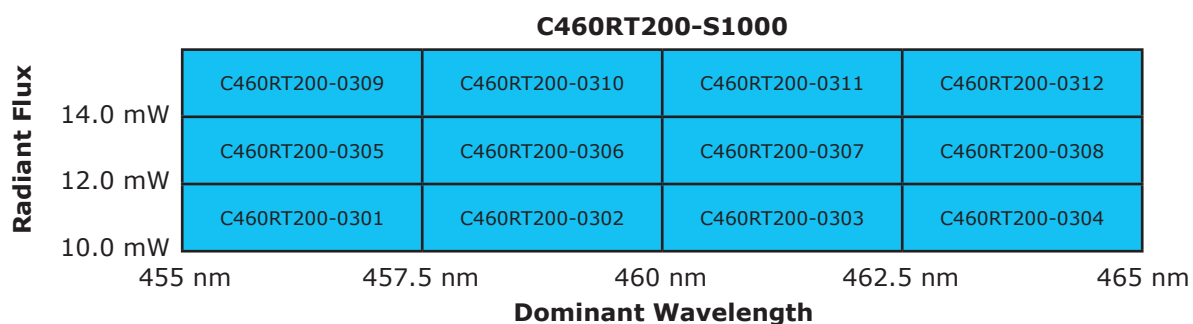
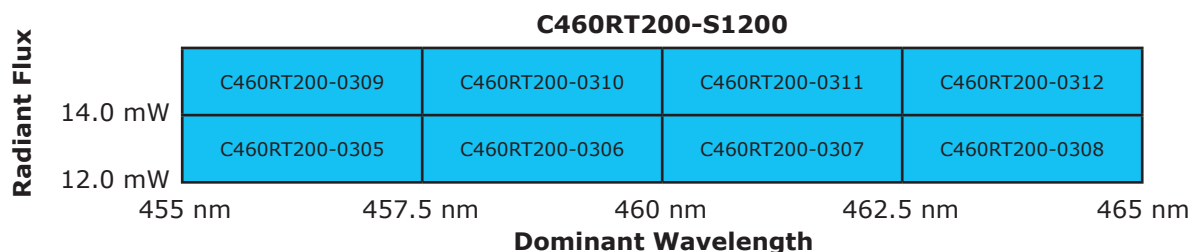
Notes:

- Maximum ratings are package dependent. The above ratings were determined using a T-1 3/4 package (with Hysol OS4000 epoxy) for characterization. Seller makes no representations regarding ratings for packages other than the T-1 3/4 package used by Seller. The forward currents (DC and Peak) are not limited by the G•SiC die but by the effect of the LED junction temperature on the package. The junction temperature limit of 125°C is a limit of the T-1 3/4 package; junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (< 5 seconds).
- Product resistance to electrostatic discharge (ESD) is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the maximum ESD ratings shown. Seller gives no other assurances regarding the ability of Products to withstand ESD.
- All products conform to the listed minimum and maximum specifications for electrical and optical characteristics when assembled and operated at 5 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are the average values expected by Seller in large quantities and are provided for information only. Seller gives no assurances products shipped will exhibit such typical ratings. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy). Dominant wavelength measurements taken using Illuminance E.
- Specifications are subject to change without notice.



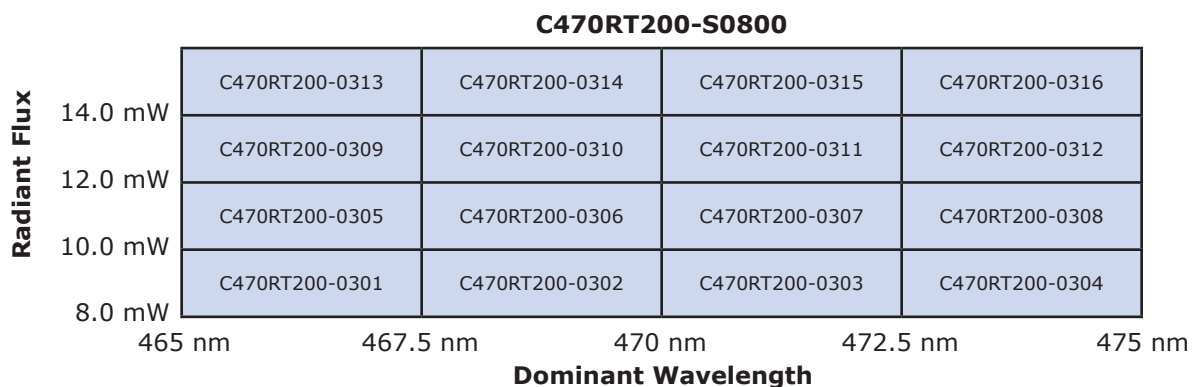
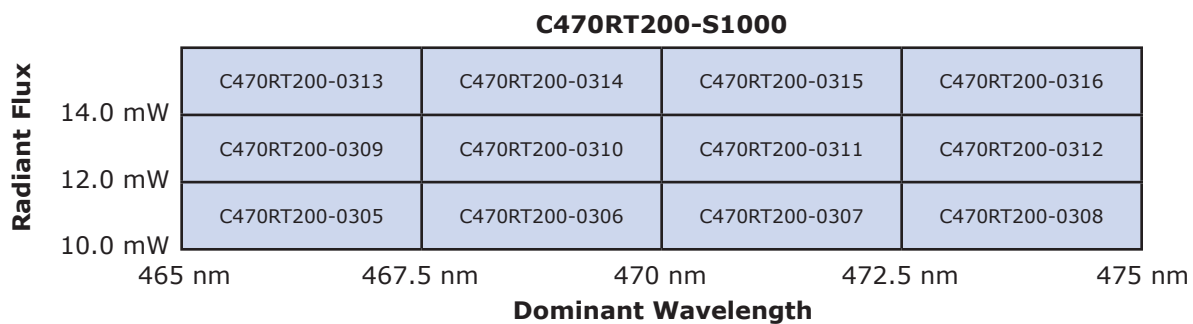
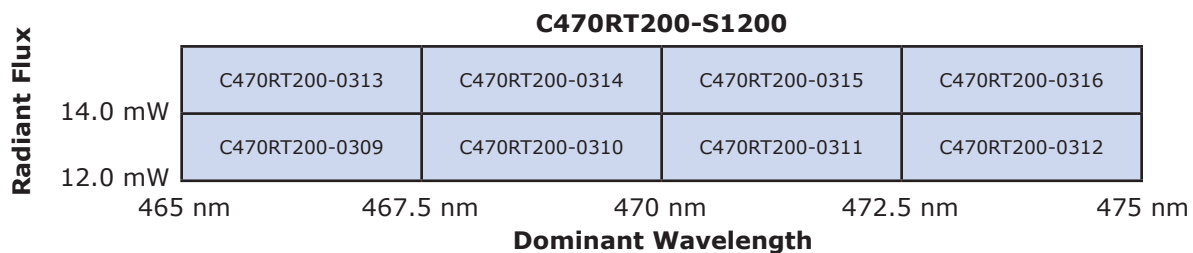
Standard Bins for CxxxRT200-Sxx000

LED chips are sorted to the **radiant flux** and **dominant wavelength** bins shown. A sorted die sheet contains die from only one bin. Sorted die kit (CxxxRT200-Sxx000) orders may be filled with any or all bins (CxxxRT200-xxxx) contained in the kit. All radiant flux values shown and specified are at $I_f = 20$ mA and dominant wavelength values are at $I_f = 5$ mA.



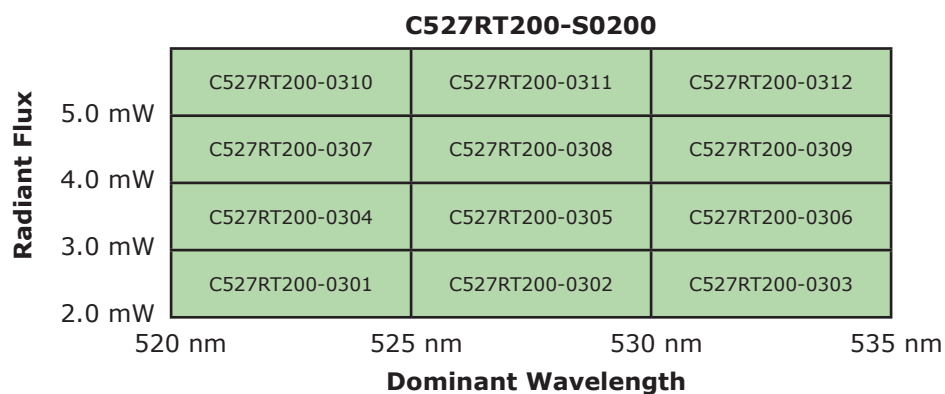
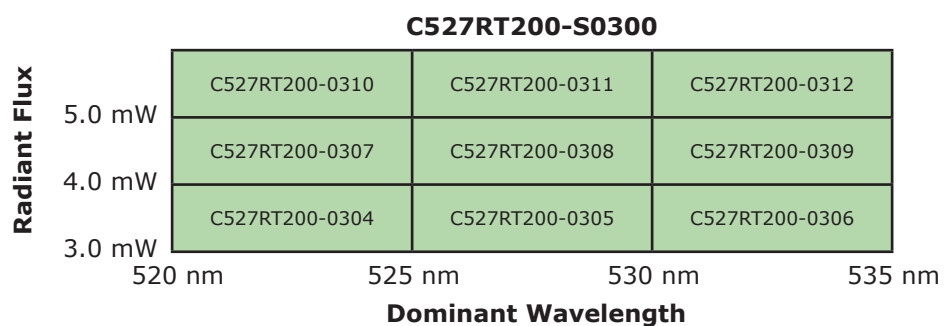
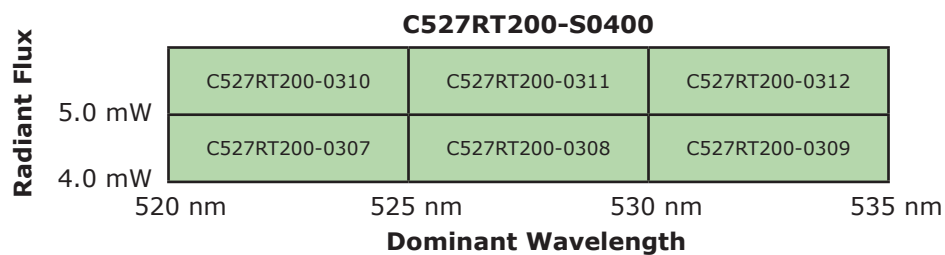


Standard Bins for CxxxRT200-Sxx000 (continued)





Standard Bins for CxxxRT200-Sxx000 (continued)

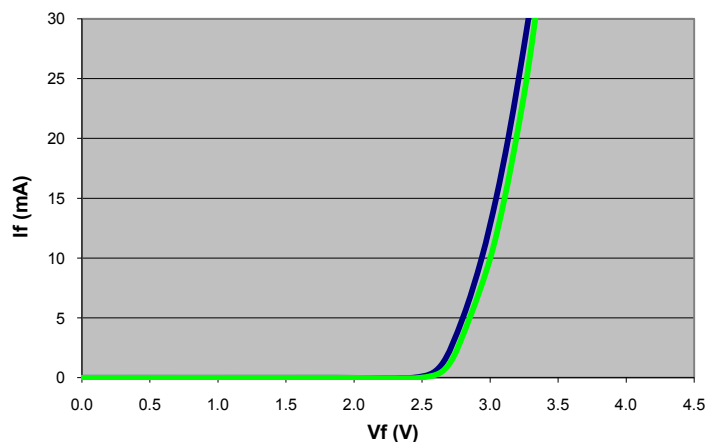




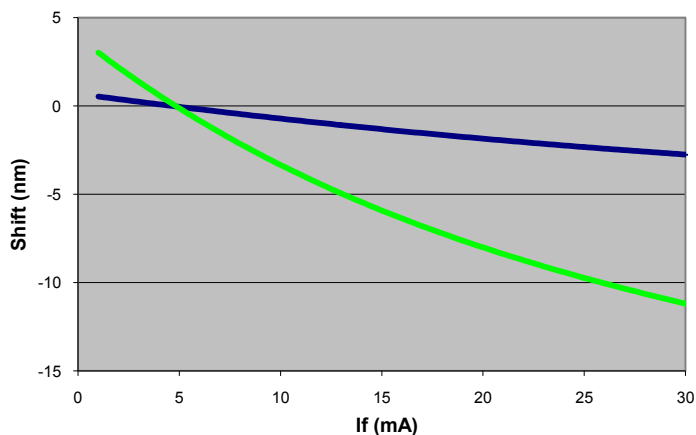
Characteristic Curves

These are representative measurements for the RazerThin products. Actual curves will vary slightly for the various radiant flux and dominant wavelength bins.

Forward Current vs. Forward Voltage



Wavelength Shift vs. Forward Current



Relative Intensity vs. Forward Current

