

Cree® UltraThin™ Gen 3 LEDs

Data Sheet

CxxxUT190-Sxxxx-31

Cree's UltraThin LEDs combine highly efficient InGaN materials with Cree's proprietary G•SiC® substrate to deliver superior price/performance for blue LEDs. These vertically structured LED chips are small in size and require a low forward voltage. Cree's UT™ series chips are tested for conformity to optical and electrical specifications and the ability to withstand 1000 V ESD. Applications include keypad backlighting where sub-miniaturization and thinner form factors are required.

FEATURES

- Small Chip – 190 x 190 x 50 μm
- UT LED Performance
 - 450 & 460 nm – 12 mW min.
 - 470 nm – 10 mW min.
 - 527 nm – 3.0 mW min.
- Low Forward Voltage
 - 2.9 V Typical at 5 mA
- Single Wire Bond Structure
- Class 2 ESD Rating

APPLICATIONS

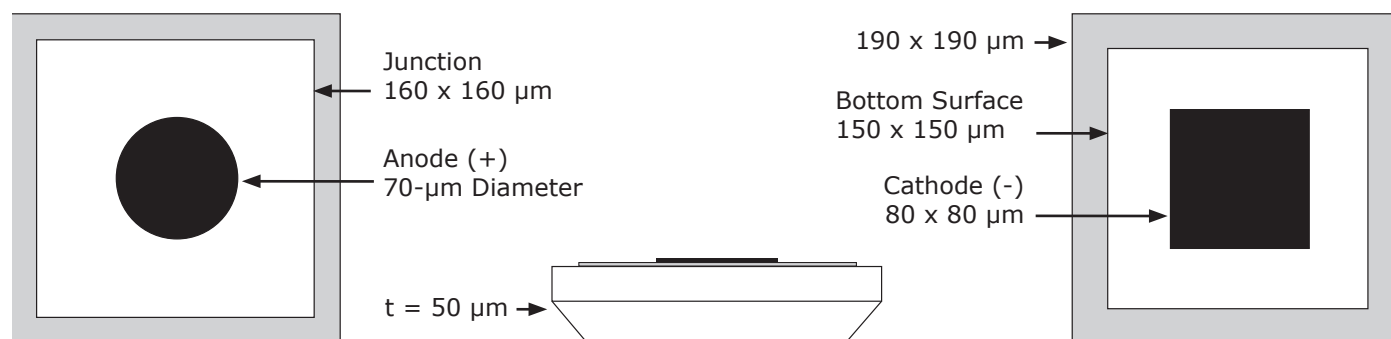
- Mobile Phone Keypads
- Audio Product Display Lighting
- Mobile Appliance Keypads
- Automotive Applications

CxxxUT190-Sxxxx-31 Chip Diagram

Top View

Die Cross Section

Bottom View



Maximum Ratings at $T_A = 25^{\circ}\text{C}$ Notes 1&3		CxxxUT190-Sxxxx-31
DC Forward Current		30 mA
Peak Forward Current (1/10 duty cycle @ 1 kHz)		100 mA
LED Junction Temperature		125°C
Reverse Voltage		5 V
Operating Temperature Range		-40°C to +100°C
LED Chip Storage Temperature		-40°C to +120°C
Recommended Die Sheet Storage Conditions		$\leq 30^{\circ}\text{C}$ / $\leq 85\%$ RH
Electrostatic Discharge Threshold (HBM) Note 2		1000 V
Electrostatic Discharge Classification (MIL-STD-883E) Note 2		Class 2

Typical Electrical/Optical Characteristics at $T_A = 25^{\circ}\text{C}$, $I_f = 5\text{ mA}$ Note 3					
Part Number	Forward Voltage (V_f , V)			Reverse Current [$I(V_r=5\text{ V})$, μA]	Full Width Half Max (λ_d , nm)
	Min.	Typ.	Max.	Max.	Typ.
C450UT190-Sxxxx-31	2.7	2.9	3.1	2	21
C460UT190-Sxxxx-31	2.7	2.9	3.1	2	21
C470UT190-Sxxxx-31	2.7	2.9	3.1	2	22
C527UT190-Sxxxx-31	2.7	3.0	3.2	2	35

Mechanical Specifications		CxxxUT190-Sxxxx-31	
Description		Dimension	Tolerance
P-N Junction Area (μm)		160 x 160	± 25
Chip Top Area (μm)		190 x 190	± 25
Chip Bottom Area (μm)		150 x 150	± 25
Chip Thickness (μm)		50	± 10
Au Bond Pad Diameter (μm)		70	-5, +15
Au Bond Pad Thickness (μm)		1.2	± 0.5
Backside Contact Metal Area (μm)		80 x 80	± 25

Notes:

- Maximum ratings are package dependent. The above ratings were determined using a T-1 3/4 package (with Hysol OS4000 epoxy) for characterization. Ratings for other packages may differ. The forward currents (DC and Peak) are not limited by the die but by the effect of the LED junction temperature on the package. The junction temperature limit of 125°C is a limit of the T-1 3/4 package; junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (< 5 seconds).
- Product resistance to electrostatic discharge (ESD) according to the HBM is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the minimum ESD ratings shown. The ESD classification of Class 2 is based on sample testing according to MIL-STD-883E.
- All products conform to the listed minimum and maximum specifications for electrical and optical characteristics when assembled and operated at 5 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are within the range of average values expected by manufacturer in large quantities and are provided for information only. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy). Optical characteristics measured in an integrating sphere using Illuminance E.
- Caution: To obtain optimum output efficiency, the amount of epoxy used should be characterized based upon the specific application.

Standard Bins for CxxxUT190-Sxxxx-31

LED chips are sorted to the **radiant flux** and **dominant wavelength** bins shown. Sorted die sheets contain die from only one bin. Sorted die kit (CxxxUT190-Sxxxx-31) orders may be filled with any or all bins (CxxxUT190-xxxx-31) contained in the kit. All radiant flux values are measured at $I_f = 20$ mA and all dominant wavelength values are measured at $I_f = 5$ mA.

C450UT190-S1200-31

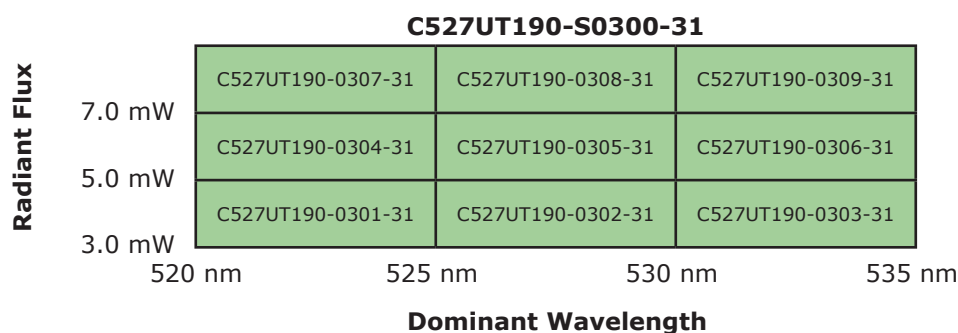
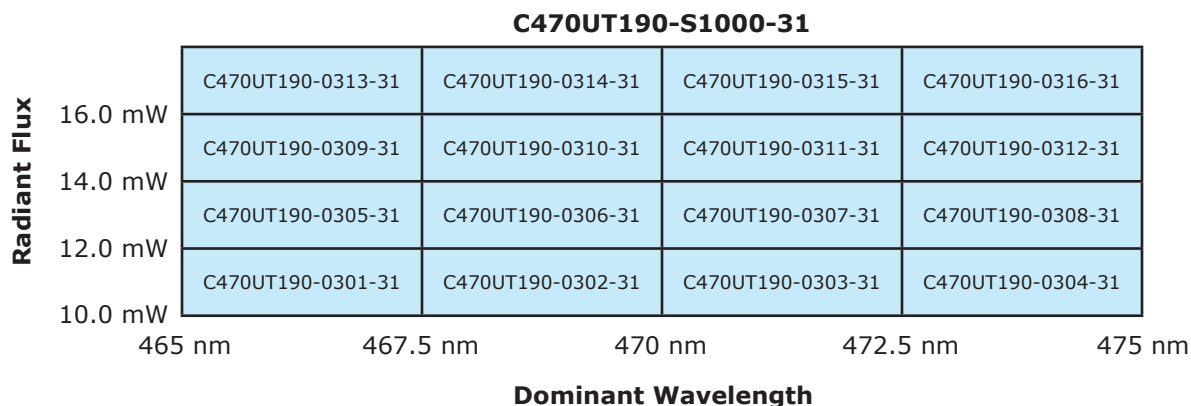
Radiant Flux	18.0 mW	C450UT190-0317-31	C450UT190-0318-31	C450UT190-0319-31	C450UT190-0320-31
	16.0 mW	C450UT190-0313-31	C450UT190-0314-31	C450UT190-0315-31	C450UT190-0316-31
	14.0 mW	C450UT190-0309-31	C450UT190-0310-31	C450UT190-0311-31	C450UT190-0312-31
	12.0 mW	C450UT190-0305-31	C450UT190-0306-31	C450UT190-0307-31	C450UT190-0308-31
		445 nm	447.5 nm	450 nm	452.5 nm
Dominant Wavelength					

C460UT190-S1200-31

Radiant Flux	18.0 mW	C460UT190-0317-31	C460UT190-0318-31	C460UT190-0319-31	C460UT190-0320-31
	16.0 mW	C460UT190-0313-31	C460UT190-0314-31	C460UT190-0315-31	C460UT190-0316-31
	14.0 mW	C460UT190-0309-31	C460UT190-0310-31	C460UT190-0311-31	C460UT190-0312-31
	12.0 mW	C460UT190-0305-31	C460UT190-0306-31	C460UT190-0307-31	C460UT190-0308-31
		455 nm	457.5 nm	460 nm	462.5 nm
Dominant Wavelength					

Standard Bins for CxxxUT190-Sxxxx-31 (continued)

LED chips are sorted to the **radiant flux** and **dominant wavelength** bins shown. Sorted die sheets contain die from only one bin. Sorted die kit (CxxxUT190-Sxxxx-31) orders may be filled with any or all bins (CxxxUT190-xxxx-31) contained in the kit. All radiant flux values are measured at $I_f = 20$ mA and all dominant wavelength values are measured at $I_f = 5$ mA.

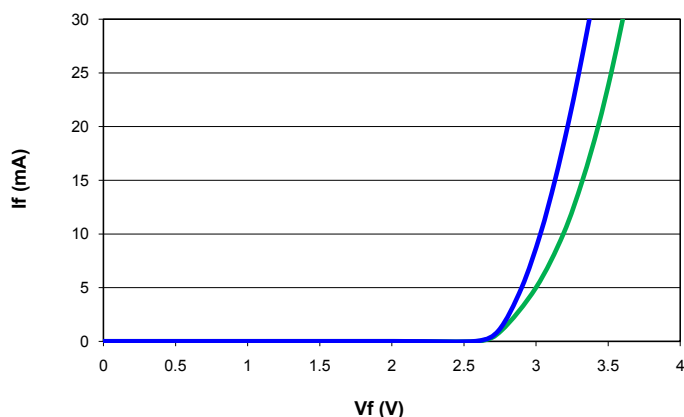




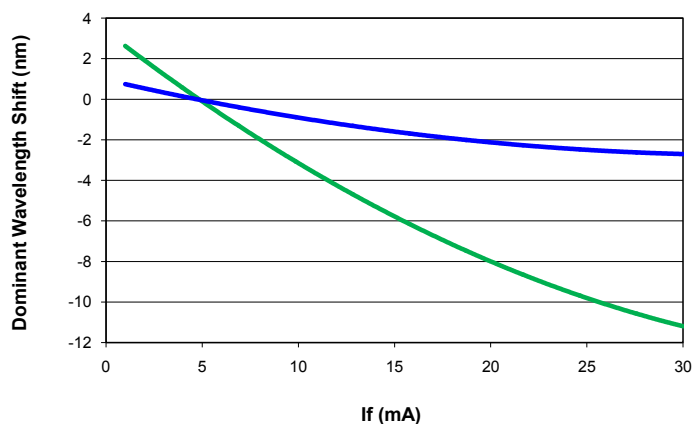
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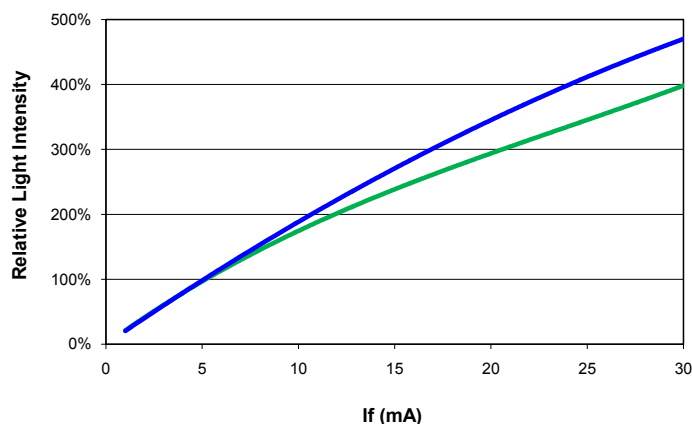
Forward Current vs. Forward Voltage



Wavelength Shift vs. Forward Current



Relative Intensity vs. Forward Current



Radiation Pattern

This is a representative radiation pattern for the UltraThin Chip LED product. Actual patterns will vary slightly for each chip.

