Silicon NPN Power Transistors BU1508AX

DESCRIPTION

·With TO-220F package
·High voltage
·High speed switching

APPLICATIONS

·For use in horizontal deflection circuits of colour TV receivers.

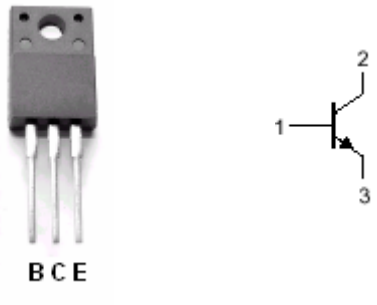


Fig.1 simplified outline (TO-220F) and symbol

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

Absolute maximum ratings (Ta=25□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	1500	V
V_{CEO}	Collector-emitter voltage	Open base	700	V
V_{EBO}	Emitter-base voltage	Open collector	7.5	V
I_C	Collector current		8	A
I_{CM}	Collector current (peak)		15	A
I_B	Base current		4	A
I_{BM}	Base current (peak)		6	A
P_T	Total power dissipation	T_C=25□	35	W
T_j	Junction temperature		150	□
T_{stg}	Storage temperature		-65~150	□

CHARACTERISTICS

Tj=25□ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)EBO}$	Emitter-base breakdown voltage	I_E=1mA ;I_C=0	7.5	13.5		V
$V_{CEO(SUS)}$	Collector-emitter sustaining voltage	I_C=100mA ;I_B=0;L=25mH	700			V
V_{CEsat}	Collector-emitter saturation voltage	I_C=4.5A; I_B=1.1A			1.0	V
V_{BEsat}	Base-emitter saturation voltage	I_C=4.5A; I_B=1.7A			1.1	V
I_{CES}	Collector cut-off current	V_{CE}=rated;V_{BE}=0 T_j=125□			1.0 2.0	mA
I_{EBO}	Emitter cut-off current	V_{EB}=7.5V; I_C=0			1.0	mA
h_{FE-1}	DC current gain	I_C=0.1A ; V_{CE}=5V		13		
h_{FE-2}	DC current gain	I_C=4.5A ; V_{CE}=1V	4.0	5.5	7.0	
C_C	Collector output capacitance	I_E=0;f=1MHz;V_{CB}=10V		80		pF

PACKAGE OUTLINE

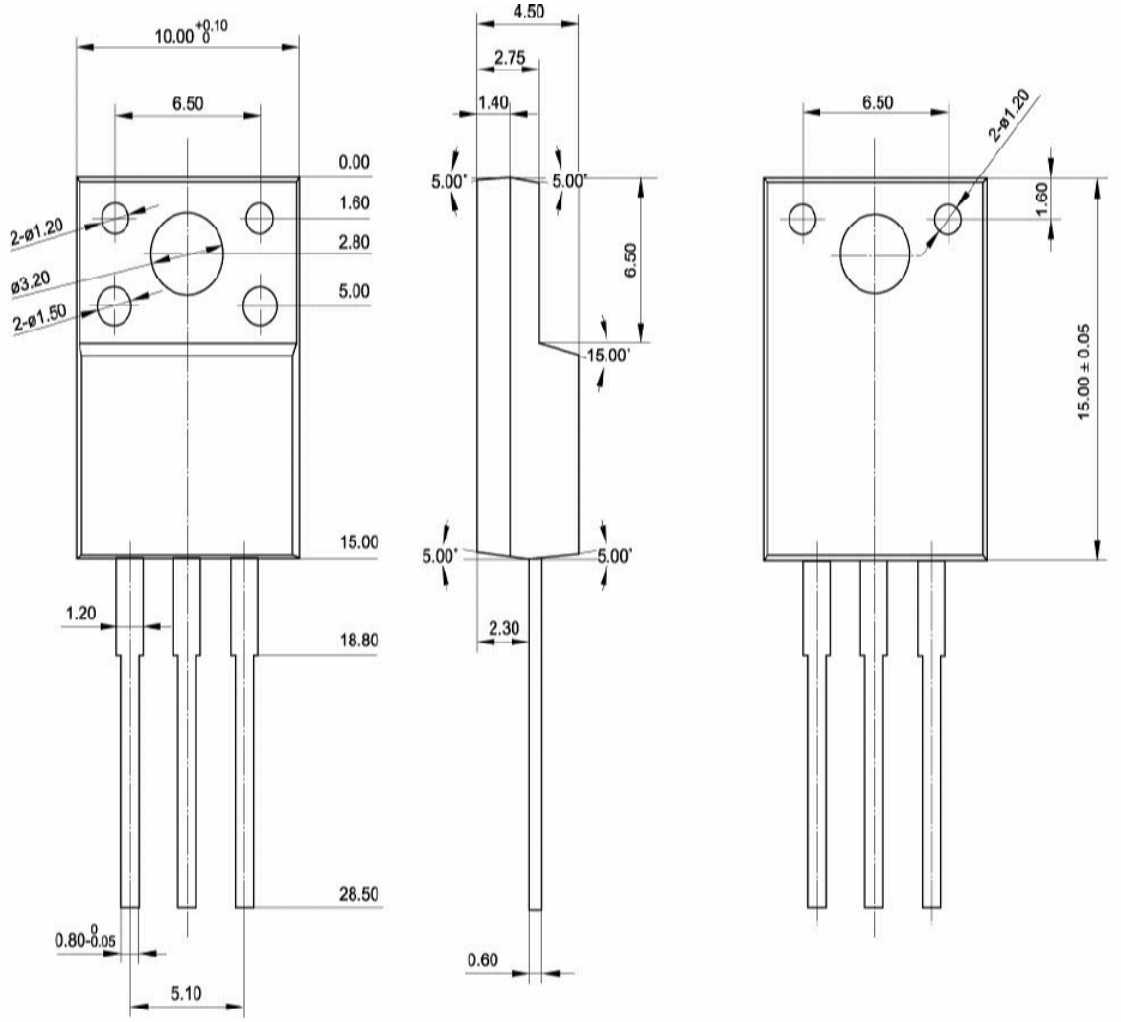


Fig.2 Outline dimensions