STRUCTURE	Silicon Monolithic Integrated Circuit
PRODUCTS	1ch 75Ω driver for DVD（Y／C M I X Type）
TYPE	B A 7 6 6 4 A F V
PACKAGE	Figure - 1 S S O P B－ 8 P I N （Plastic Mold）
BLOCK DIAGRAM	Figure - 2

Features
1) A low consumption electric power movement
2) Output mute circuit built-in
3) The power save circuit built-in
4) Output protection circuit built-in
5) Output coupling capacitor is made in the small capacity by sag compensation circuit built-in
6) The driver can have two loads(each channel)
7) Y/C MIX circuit built-in

Absolute Maximum Ratings（Ta=25℃）

Item	Symbol	Limits	Unit
Power Suuply Voltage	Vcc	8	V
Power Dissipation	Pd	3 5 0	mW
Operating Temperature Range	Topr	－2 5～＋7 5	℃
Storage Temperature Range	Tstg	－5 5～＋1 2 5	℃

※ When absolute temperature exceeds Ta=25℃, the rated value is reduced by 3.5mW/℃.

Operating Range（Ta=25℃）

Item	Symbol	Min.	TYP.	Max.	Unit
Supply voltage	Vcc	4.5	5.0	5.5	V

This product design is not intended for use involving radioactive rays.

Application example
The application circuit is recommended for use. Make sure to confirm the characteristics.
When using the circuit with changes to the external circuit constants, make sure to leave an adequate margin for external components including static and transitional characteristics as well as dispersion of the IC.
Note that ROHM cannot provide adequate confirmation of patents.

The product described in this specification is designed to be used with ordinary electronic equipment or devices(such as audio-visual equipment, office-automation equipment , communications devices, electrical appliances, and electronic toys). Should you intend to use this product with equipment or devices which require an extremely high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

ROHM assumes no responsibility for use of any circuits described herein, conveys no license under any patent or other right, and makes no representations that the circuits are free from patent infringement.

■Electrical Characteristics (Unless otherwise specified, Vcc=5V, Ta=25℃)

Item	Symbol	Limit			Unit	Condition
		MIN.	TYP.	MAX.		
Circuit Current	ICC	6.1	12.2	18.3	mA	No signal
Maximum Output Level	Vom	2.6	3.0	—	Vp-p	f=1kHz,THD=1%/VO2
Voltage Gain	GV	-1.0	-0.2	0.6	dB	f=4.43MHz/ VO1
Frequency Characteristics	Gf	-1.5	-0.5	0.5	dB	f=7MHz/1MHz, 1Vp-p/VO1
MUTE Attenuation	MT	—	-60	—	dB	f=4.43MHz, 1Vp-p/VO1
MUTE Change level "H"	VTHH	2.2	—	VCC	V	
MUTE Change level "L"	VTHL	0	—	0.7	V	
Input Impedance	ZIN	16	20	24	KΩ	Chroma input pin (7pin)
Supply Current in MUTE	IMUTE	—	1.3	2.6	mA	MUTE "H"

■PACKAGE

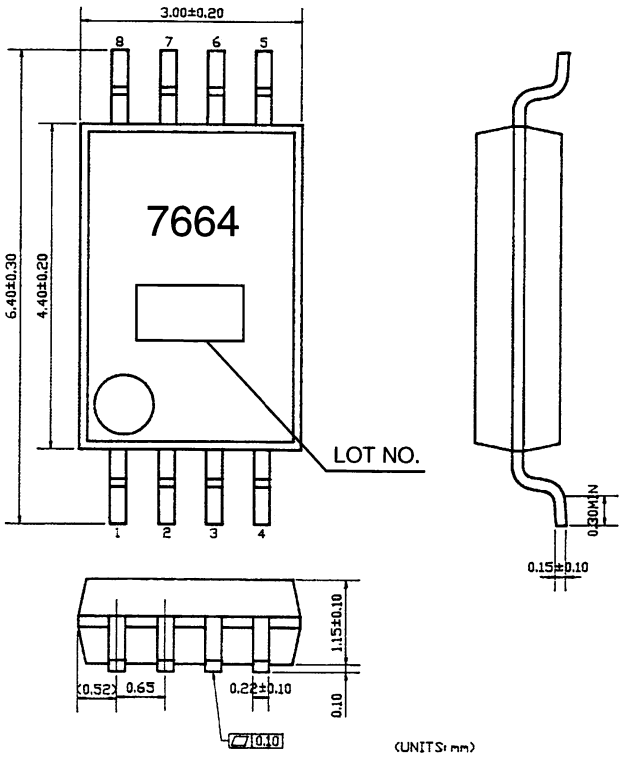


Figure 1 SSOPB 8PIN (Plastic Mold)

■BLOCK DIAGRAM

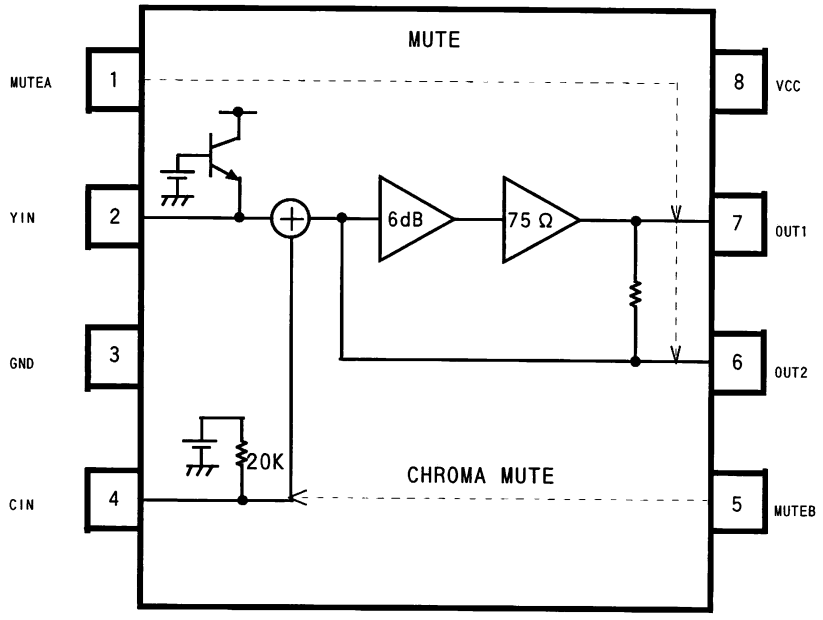


Figure 2

■PIN Assignment

PIN No.	PIN NAME
1	MUTEA
2	YIN
3	GND
4	CIN
5	MUTEB
6	OUT2
7	OUT1
8	VCC

■Cautions on use

(1) Numbers and data in entries are representative design values and are not guaranteed values of the items.

(2) Although we are confident in recommending the sample application circuits, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that they have sufficient margins by taking into account variations in externally attached components and the Rohm LSI, not only for static characteristics but also including transient characteristics.

(3) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltage or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

(4) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

(5) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

(6) Shorts between pins and misinstallation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

(7) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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