Ordering number : EN8985A

ATP216

N-Channel Power MOSFET
50V, 35A, 23mΩ, Single ATPAK

ON Semiconductor®
http://onsemi.com

Features

- ON-resistance $R_{DS}(on)1$=17mΩ(typ.)
- 1.8V drive
- Protection diode in
- Slim package
- Halogen free compliance

Specifications

Absolute Maximum Ratings at Ta=25°C

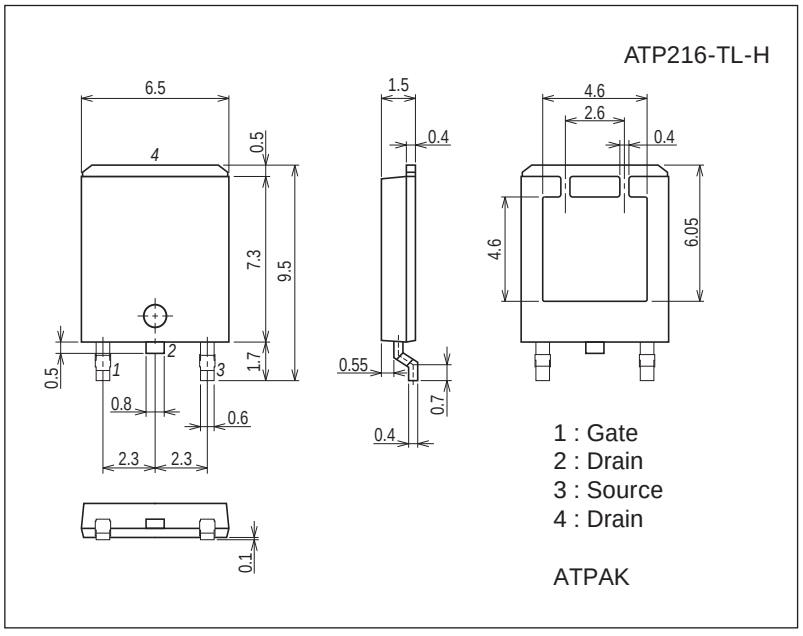
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		50	V
Gate-to-Source Voltage	V_{GSS}		±10	V
Drain Current (DC)	I_D		35	A
Drain Current (PW≤10μs)	I_{DP}	PW≤10μs, duty cycle≤1%	105	A
Allowable Power Dissipation	P_D	Tc=25°C	40	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C
Avalanche Energy (Single Pulse) *1	E_{AS}		40	mJ
Avalanche Current *2	I_{AV}		17.5	A

Note :*1 V_{DD}=10V, L=100μH, I_{AV}=18A
*2 L≤100μH, Single pulse

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

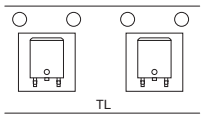
unit : mm (typ)
7057-001



Product & Package Information

- Package : ATPAK
- JEITA, JEDEC : -
- Minimum Packing Quantity : 3,000 pcs./reel

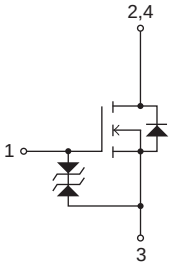
Packing Type: TL



Marking



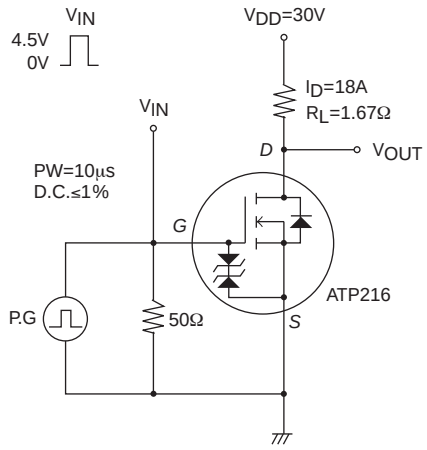
Electrical Connection



Electrical Characteristics at Ta=25°C

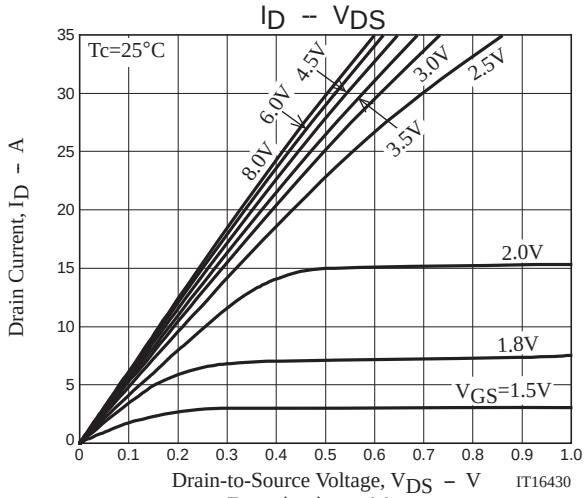
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	50			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=50V, V_{GS}=0V			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±8V, V_{DS}=0V			±10	μA
Cutoff Voltage	V_{GS}(off)	V_{DS}=10V, I_D=1mA	0.4		1.4	V
Forward Transfer Admittance	| yfs |	V_{DS}=10V, I_D=18A		58		S
Static Drain-to-Source On-State Resistance	R_{DS}(on)1	I_D=18A, V_{GS}=4.5V		17	23	mΩ
	R_{DS}(on)2	I_D=9A, V_{GS}=2.5V		20	28	mΩ
	R_{DS}(on)3	I_D=5A, V_{GS}=1.8V		30	45	mΩ
Input Capacitance	Ciss	V_{DS}=20V, f=1MHz		2700		pF
Output Capacitance	Coss			150		pF
Reverse Transfer Capacitance	Crss			110		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		27		ns
Rise Time	t_r			90		ns
Turn-OFF Delay Time	$t_{d(off)}$			220		ns
Fall Time	t_f			105		ns
Total Gate Charge	Qg	V_{DS}=30V, V_{GS}=4.5V, I_D=35A		30		nC
Gate-to-Source Charge	Qgs			5.9		nC
Gate-to-Drain “Miller” Charge	Qgd			7.9		nC
Diode Forward Voltage	V_{SD}	I_S=35A, V_{GS}=0V		0.96	1.2	V

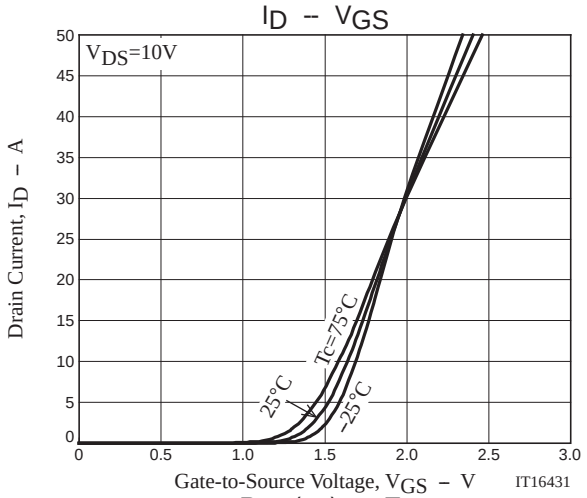
Switching Time Test Circuit

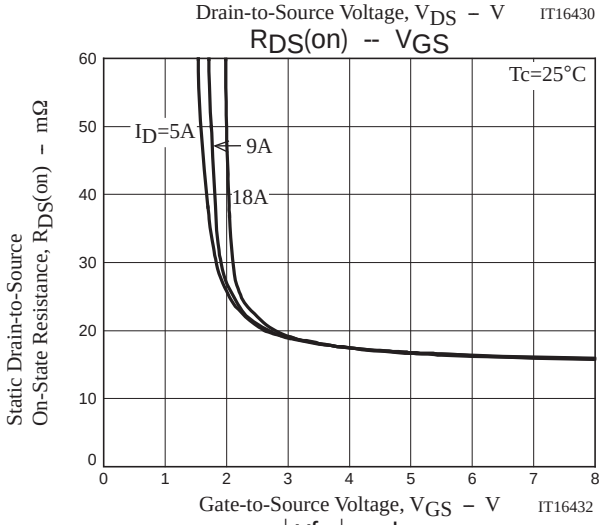


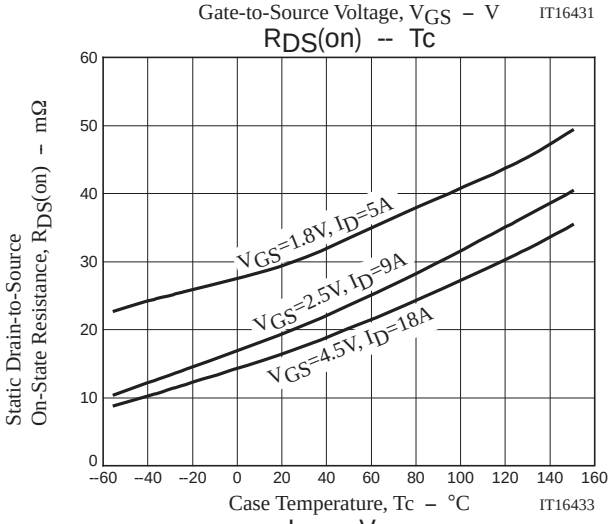
Ordering Information

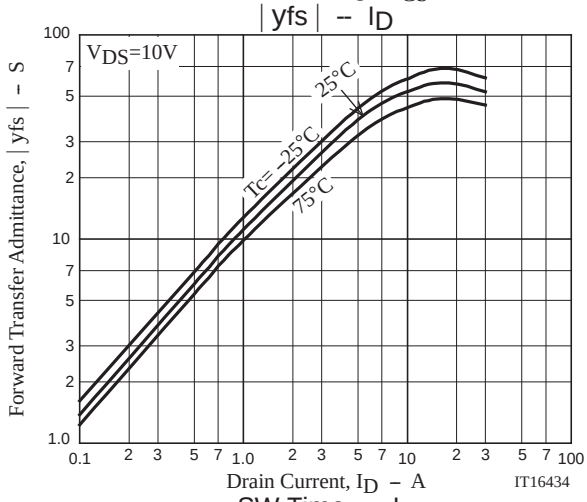
Device	Package	Shipping	memo
ATP216-TL-H	ATPAK	3,000pcs./reel	Pb Free and Halogen Free

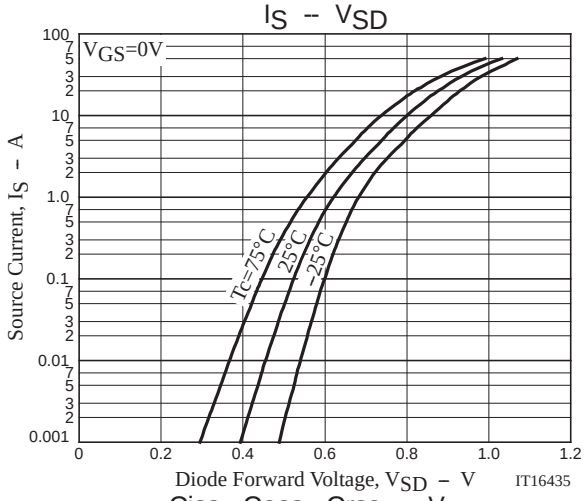

ID -- VDS
Tc=25°C
8.0V
6.0V
4.5V
3.5V
3.0V
2.5V
2.0V
1.8V
VGS=1.5V
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT16430

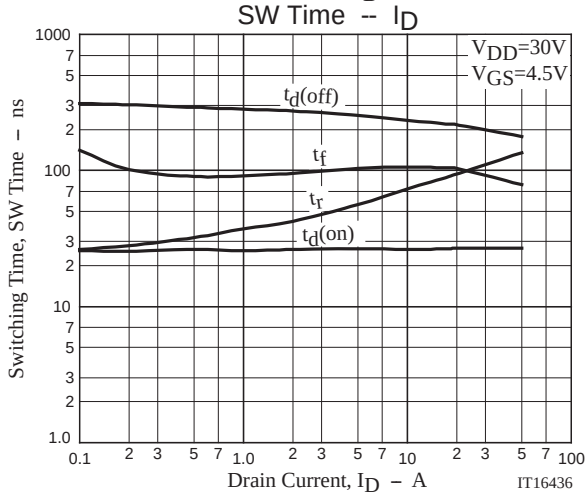

ID -- VGS
VDS=10V
Tc=75°C
25°C
–25°C
Drain Current, ID – A
Gate-to-Source Voltage, VGS – V
IT16431

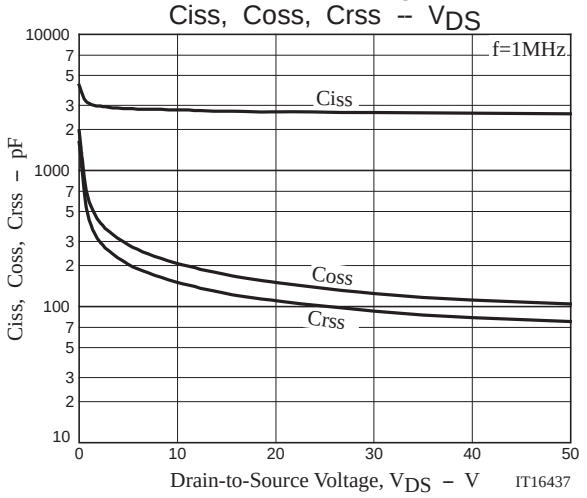

RDS(on) -- VGS
Tc=25°C
ID=5A
9A
18A
Static Drain-to-Source On-State Resistance, RDS(on) – mΩ
Gate-to-Source Voltage, VGS – V
IT16432

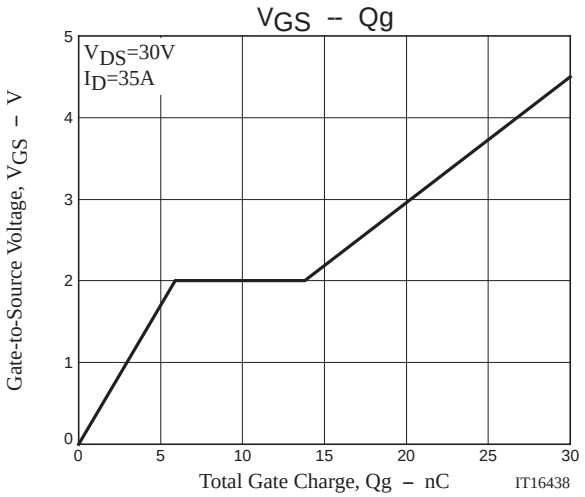

RDS(on) -- Tc
VGS=1.8V, ID=5A
VGS=2.5V, ID=9A
VGS=4.5V, ID=18A
Static Drain-to-Source On-State Resistance, RDS(on) – mΩ
Case Temperature, Tc – °C
IT16433

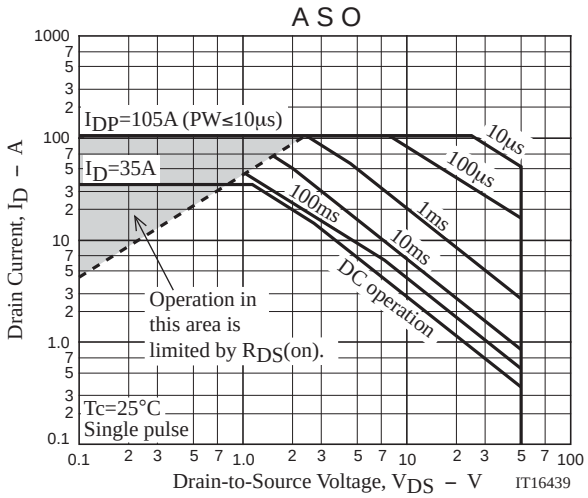

| yfs | -- ID
VDS=10V
25°C
Tc= –25°C
75°C
Forward Transfer Admittance, | yfs | – S
Drain Current, ID – A
IT16434

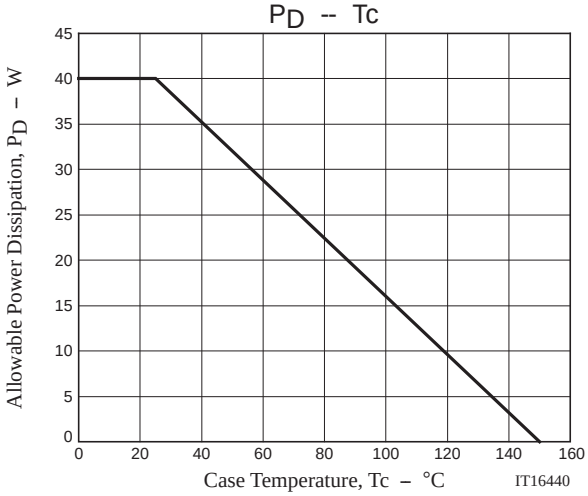

IS -- VSD
VGS=0V
Tc=75°C
25°C
–25°C
Source Current, IS – A
Diode Forward Voltage, VSD – V
IT16435

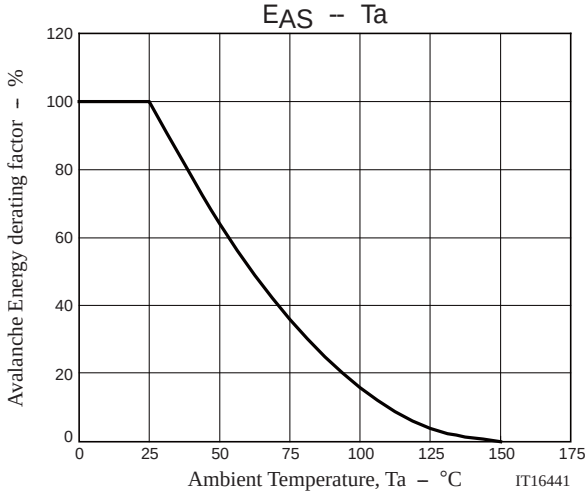

SW Time -- ID
VDD=30V
VGS=4.5V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A
IT16436


Ciss, Coss, Crss -- VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT16437


VGS – Qg
VDS=30V
ID=35A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT16438


A S O
IDP=105A (PW≤10μs)
ID=35A
10μs
100μs
1ms
10ms
100ms
DC operation
Operation in this area is limited by RDS(on).
Tc=25°C
Single pulse
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT16439


PD -- Tc
Allowable Power Dissipation, PD – W
Case Temperature, Tc – °C
IT16440


EAS -- Ta
Avalanche Energy derating factor – %
Ambient Temperature, Ta – °C
IT16441

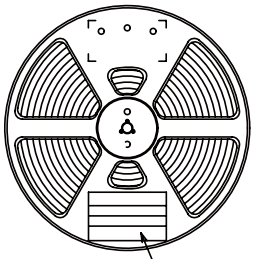
Taping Specification

ATP216-TL-H

1. Packing Format (TL)

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	INNER BOX SD-C-18	OUTER BOX SD-A-18
ATPAK	ATP	3,000	3,000	15,000	1 reels contained Dimensions:mm (external) 340×340×28	5 inner boxes contained Dimensions:mm (external) 355×355×165

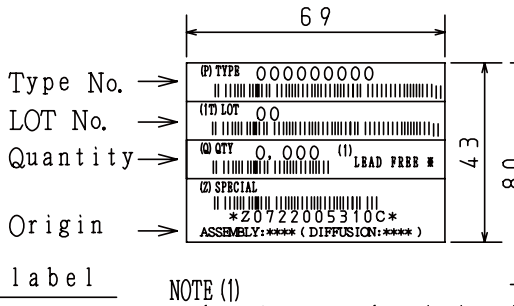
Packing method



Reel label

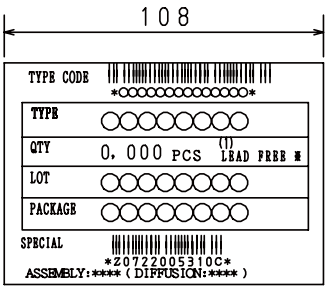
Reel label, Inner box label

(unit:mm)



Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



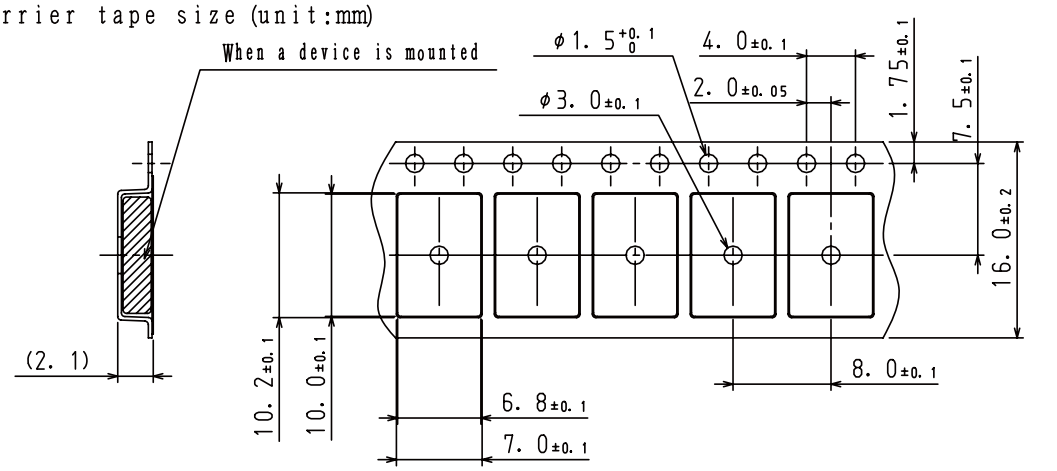
NOTE (1)

The LEAD FREE ※ description shows that the surface treatment of the terminal is lead free.

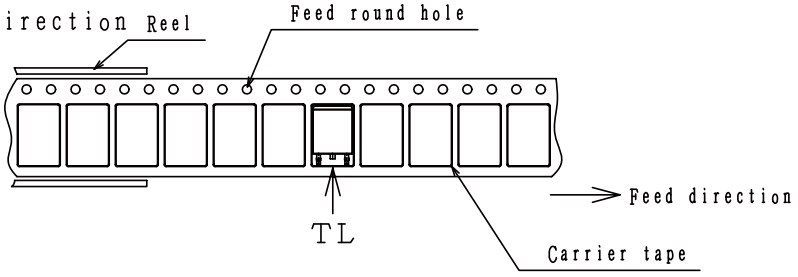
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



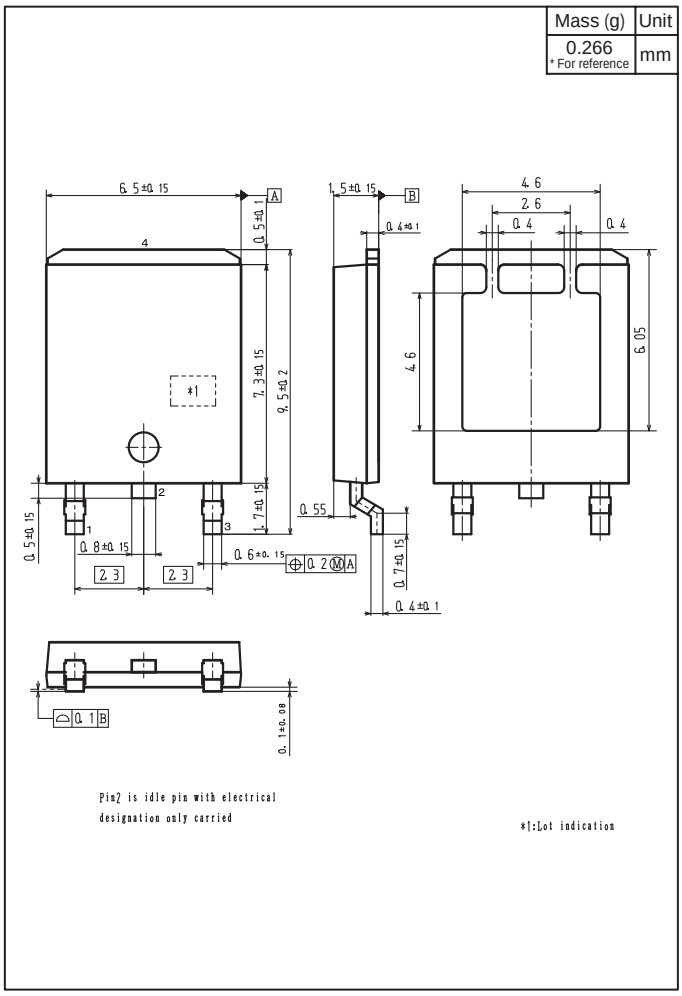
2-2. Device placement direction



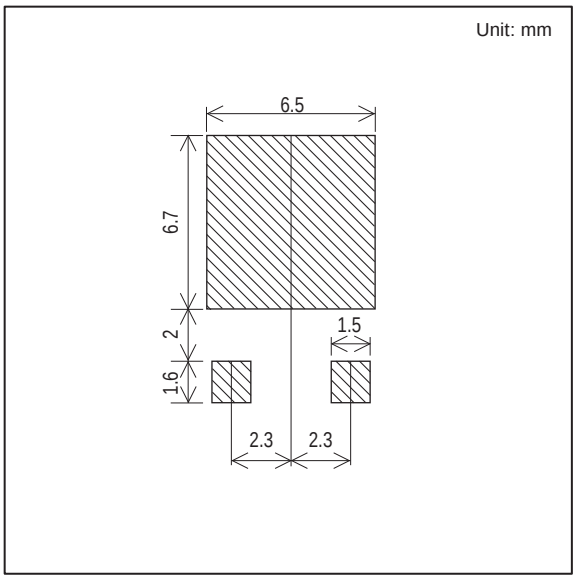
The one erectrode terminals on feed hole side····TL

Outline Drawing

ATP216-TL-H



Land Pattern Example



Note on usage : Since the ATP216 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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