

Active Errata List

- C51 Core – Bad Exit of Power-down in X2 Mode

Errata History

Lot Number	Errata List
All	1

Errata Descriptions

1. C51 Core – Bad Exit of Power-down in X2 Mode

When exiting power-down mode by interrupt while CPU is in X2 mode, it leads to a bad execution of first instruction run when CPU restarts.

Workaround

Set the CPU in X1 mode just before entering power-down mode or add a 'NOP' instruction directly after the power-down sequence.



80C51 MCUs

AT87C5103

AT83C5103

Errata Sheet

Rev. 4258A–8051–06/03





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